

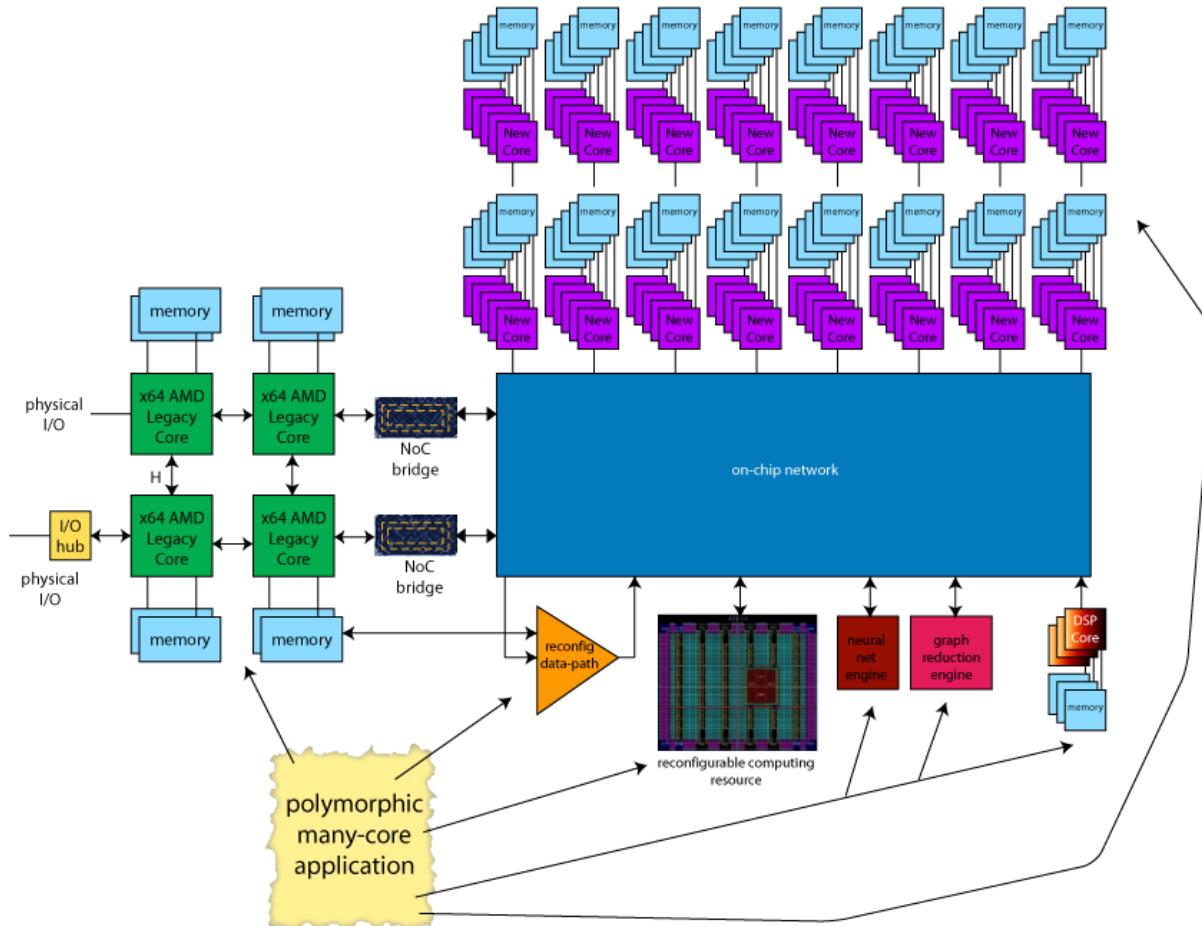


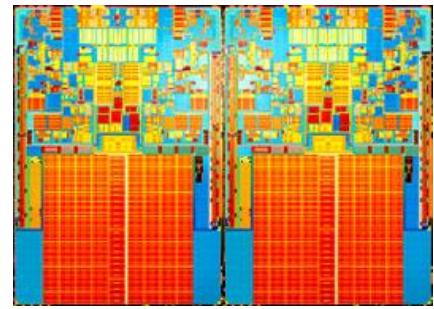
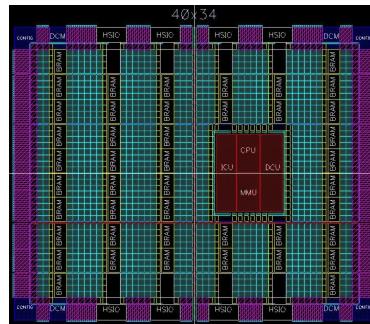
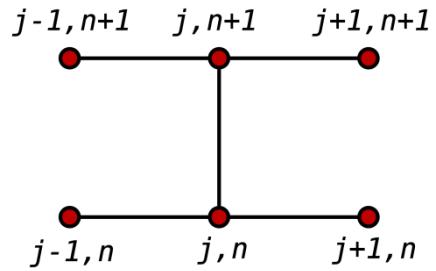
Alchemy: Three Ways of Transmutating Programs into Circuits

Satnam Singh
Microsoft Research, Cambridge, UK



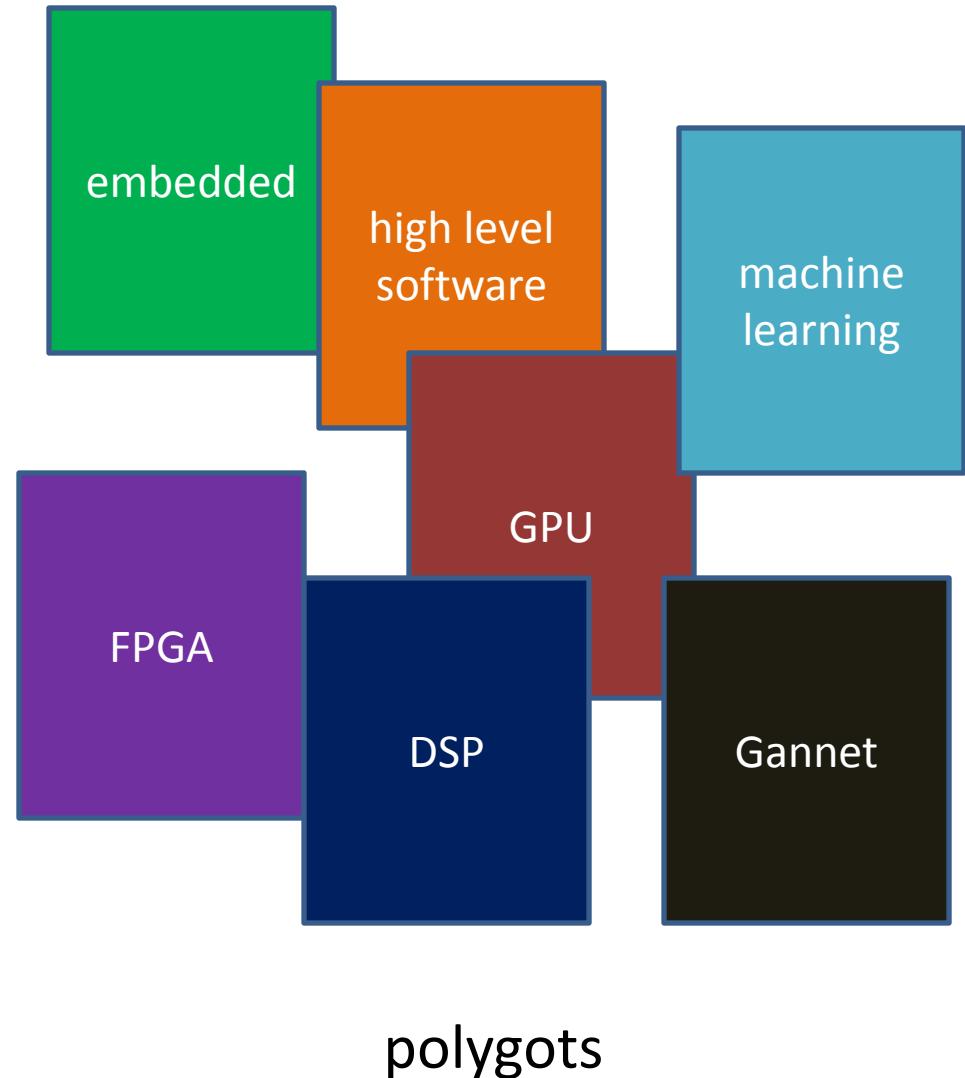
A Heterogeneous Future





universal
language?

grand unification
theory



Sequence analysis

Striped Smith–Waterman speeds database searches six times over other SIMD implementations

Michael Farrar

Received on June 22, 2006; revised on November 13, 2006; accepted on November 14, 2006

Advance Access publication November 16, 2006

Associate Editor: Nikolaus Rajewsky

ABSTRACT

Motivation: The only algorithm guaranteed to find the optimal local alignment is the Smith–Waterman. It is also one of the slowest due to the number of computations required for the search. To speed up the algorithm, Single-Instruction Multiple-Data (SIMD) instructions have been used to parallelize the algorithm at the instruction level.

Results: A faster implementation of the Smith–Waterman algorithm is presented. This algorithm achieved 2–8 times performance improvement over other SIMD based Smith–Waterman implementations. On a 2.0 GHz Xeon Core 2 Duo processor, speeds of >3.0 billion cell updates/s were achieved.

Availability: <http://farrar.michael.googlepages.com/Smith-waterman>

Contact: farrar.michael@gmail.com

search. A disadvantage introduced by processing the values vertically is that conditional branches are placed in the inner loop to compute F . With conditional code the execution time is dependent on the length of the query string and the database, the scoring matrix and gap penalties. A speedup of over six times was reported over an optimized non-SIMD implementation.

This paper presents a new Smith–Waterman implementation where the SIMD registers are parallel to the query sequence, but are accessed in a striped pattern. Like the Rognes implementation, the query profile is calculated once for the database search, but the conditional F calculations are moved outside the inner loop. Calculations speeds of >3.0 GCUPS are achieved. This is a speedup of 2–8 times over the Wozniak and Rognes SIMD implementations.

Research

Open Access

CUDA compatible GPU cards as efficient hardware accelerators for Smith-Waterman sequence alignmentSvetlin A Manavski*^{1,2} and Giorgio Valle¹Address: ¹CRIBI, University of Padova, Padova, Italy and ²Elaide, Srl, Padova, Italy

Email: Svetlin A Manavski* - svetlin.manavski@cribi.unipd.it; Giorgio Valle - giorgio.valle@unipd.it

* Corresponding author

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This is an open access article distributed under the terms of the Creative Commons Attribution License (<http://creativecommons.org/licenses/by/2.0/>), which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.**Abstract**

Background: Searching for similarities in protein and DNA databases has become a routine procedure in Molecular Biology. The Smith-Waterman algorithm has been available for more than 25 years. It is based on a dynamic programming approach that explores all the possible alignments between two sequences; as a result it returns the optimal local alignment. Unfortunately, the computational cost is very high, requiring a number of operations proportional to the product of the length of two sequences. Furthermore, the exponential growth of protein and DNA databases makes the Smith-Waterman algorithm unrealistic for searching similarities in large sets of sequences. For these reasons heuristic approaches such as those implemented in FASTA and BLAST tend to be preferred, allowing faster execution times at the cost of reduced sensitivity. The main motivation of our work is to exploit the huge computational power of commonly available graphic cards, to develop high performance solutions for sequence alignment.

Results: In this paper we present what we believe is the fastest solution of the exact Smith-Waterman algorithm running on commodity hardware. It is implemented in the recently released CUDA programming environment by NVidia. CUDA allows direct access to the hardware primitives of the last-generation Graphics Processing Units (GPU) G80. Speeds of more than 3.5 GCUPS (Giga Cell Updates Per Second) are achieved on a workstation running two GeForce 8800 GTX. Exhaustive tests have been done to compare our implementation to SSEARCH and BLAST, running on a 3 GHz Intel Pentium IV processor. Our solution was also compared to a recently published GPU implementation and to a Single Instruction Multiple Data (SIMD) solution. These tests show that our implementation performs from 2 to 30 times faster than any other previous attempt available on commodity hardware.

Methodology article

Open Access

160-fold acceleration of the Smith-Waterman algorithm using a field programmable gate array (FPGA)

Isaac TS Li¹, Warren Shum² and Kevin Truong*^{1,2}

Address: ¹Institute of Biomaterials and Biomedical Engineering, University of Toronto, 164 College Street, Toronto, Ontario, M5S 3G9, Canada and ²Edward S. Rogers Sr. Department of Electrical and Computer Engineering, University of Toronto, 10 King's College Circle, Toronto, Ontario, M5S 3G4, Canada

Email: Isaac TS Li - isaac.li@utoronto.ca; Warren Shum - warren.shum@utoronto.ca; Kevin Truong* - kevin.truong@utoronto.ca

* Corresponding author

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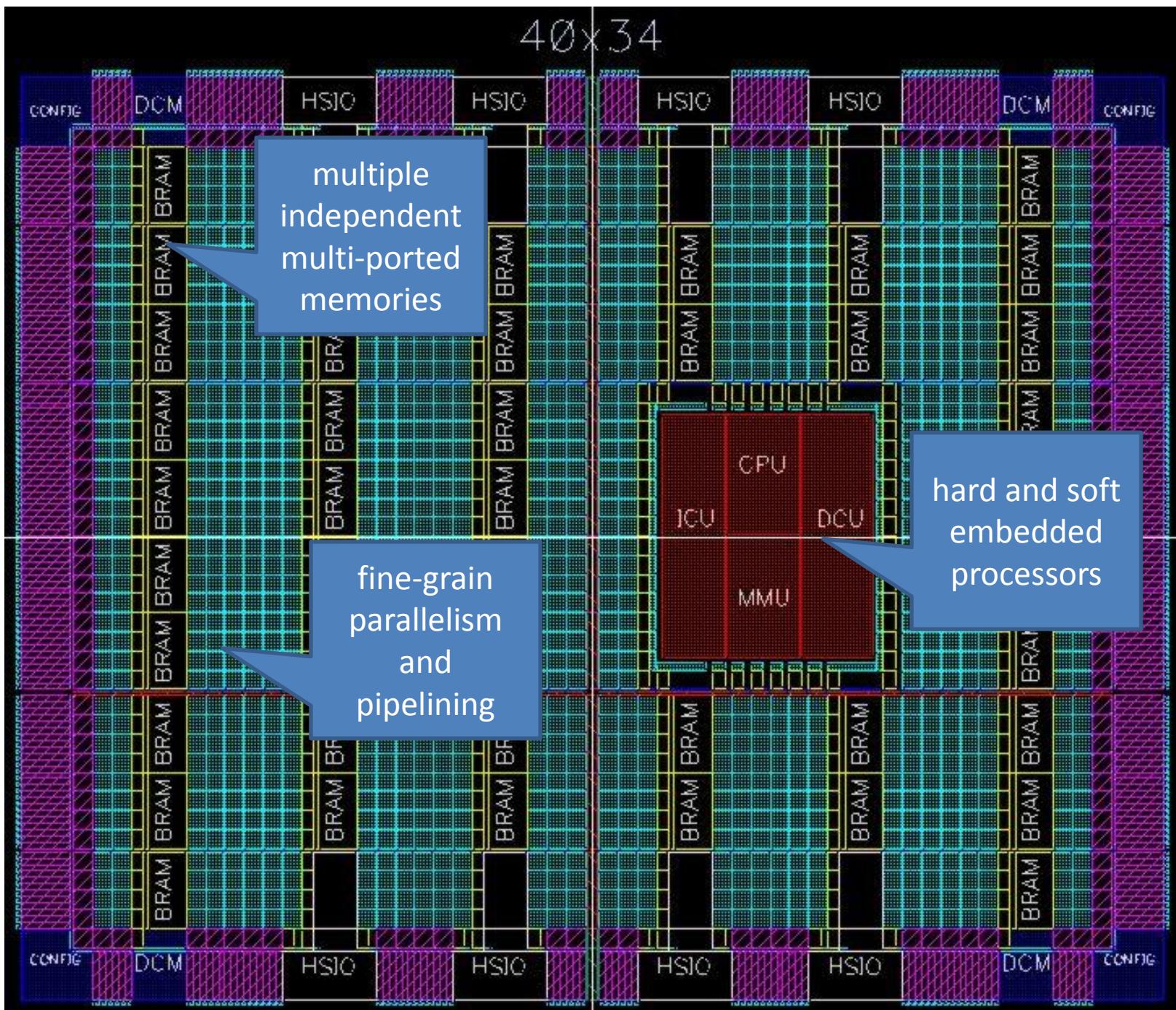
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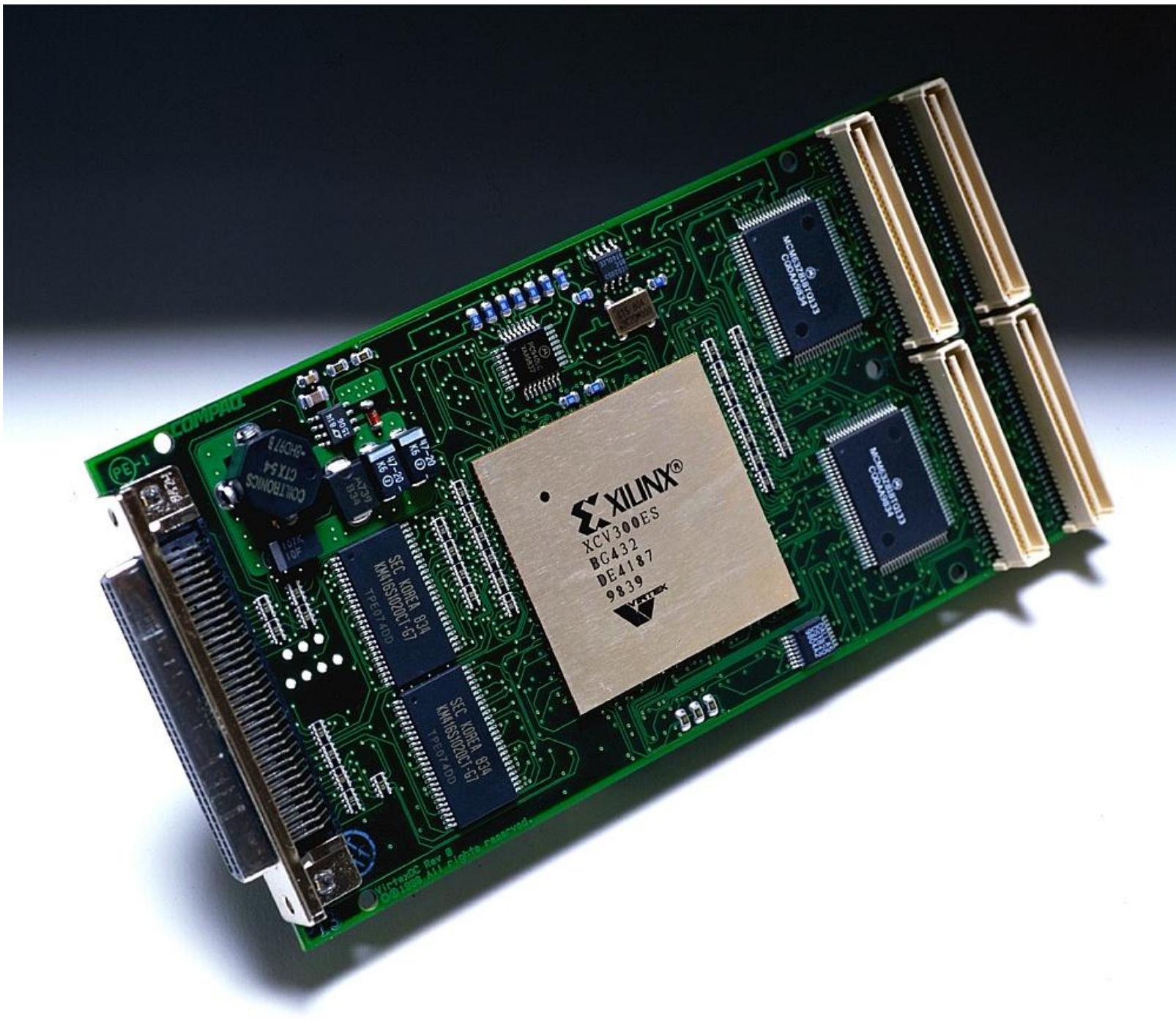
Abstract

Background: To infer homology and subsequently gene function, the Smith-Waterman (SW) algorithm is used to find the optimal local alignment between two sequences. When searching sequence databases that may contain hundreds of millions of sequences, this algorithm becomes computationally expensive.

Results: In this paper, we focused on accelerating the Smith-Waterman algorithm by using FPGA-based hardware that implemented a module for computing the score of a single cell of the SW matrix. Then using a grid of this module, the entire SW matrix was computed at the speed of field propagation through the FPGA circuit. These modifications dramatically accelerated the algorithm's computation time by up to 160 folds compared to a pure software implementation running on the same FPGA with an Altera Nios II softprocessor.

Conclusion: This design of FPGA accelerated hardware offers a new promising direction to seeking computation improvement of genomic database searching.





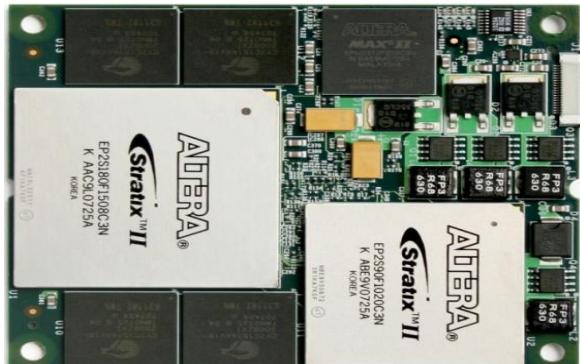


15 3:43PM

FPGAs as Co-Processors



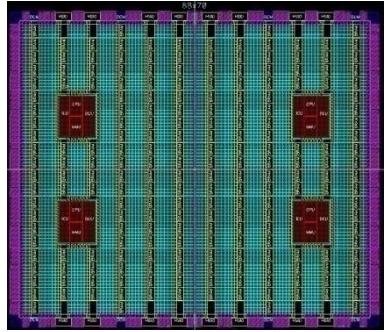
XD2000i FPGA in-socket
accelerator for Intel FSB



XD2000F FPGA in-socket
accelerator for AMD socket F



XD1000 FPGA co-processor
module for socket 940

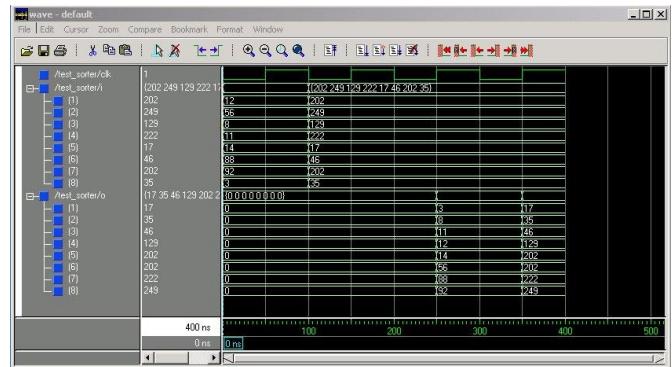


Verilog

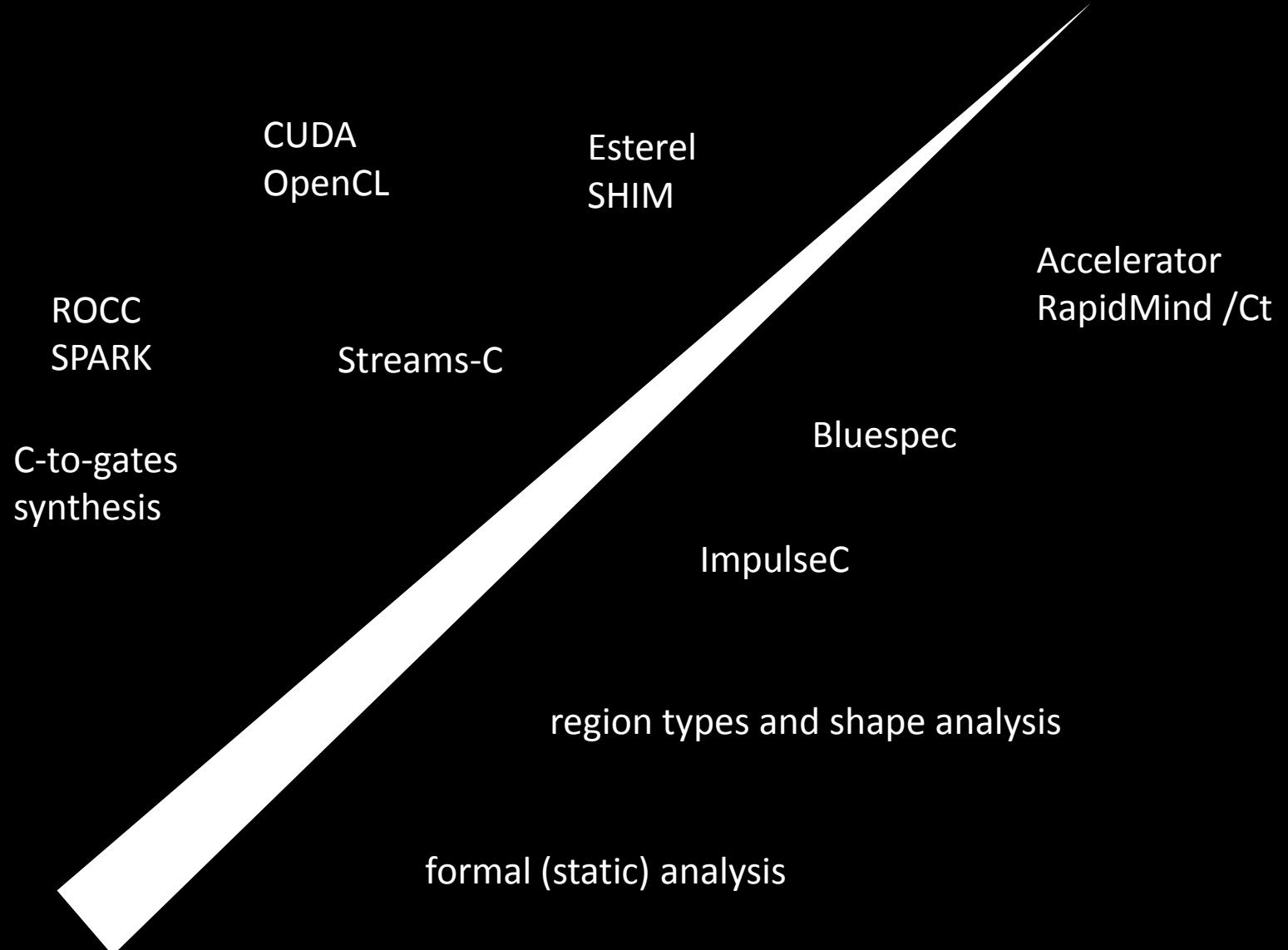
opportunity

scientific computing
data mining
search
image processing
financial analytics

challenge



ray of light



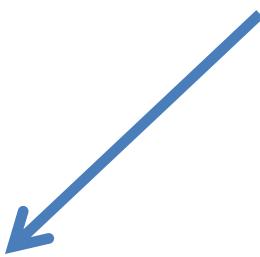
Self Imposed Constraints







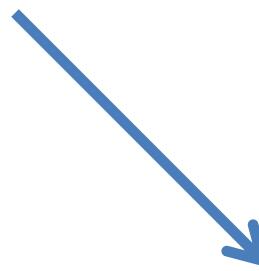
Our High Level Synthesis Projects



Kiwi: concurrent
C# programs for
control-oriented
applications
[Univ. Cambridge]

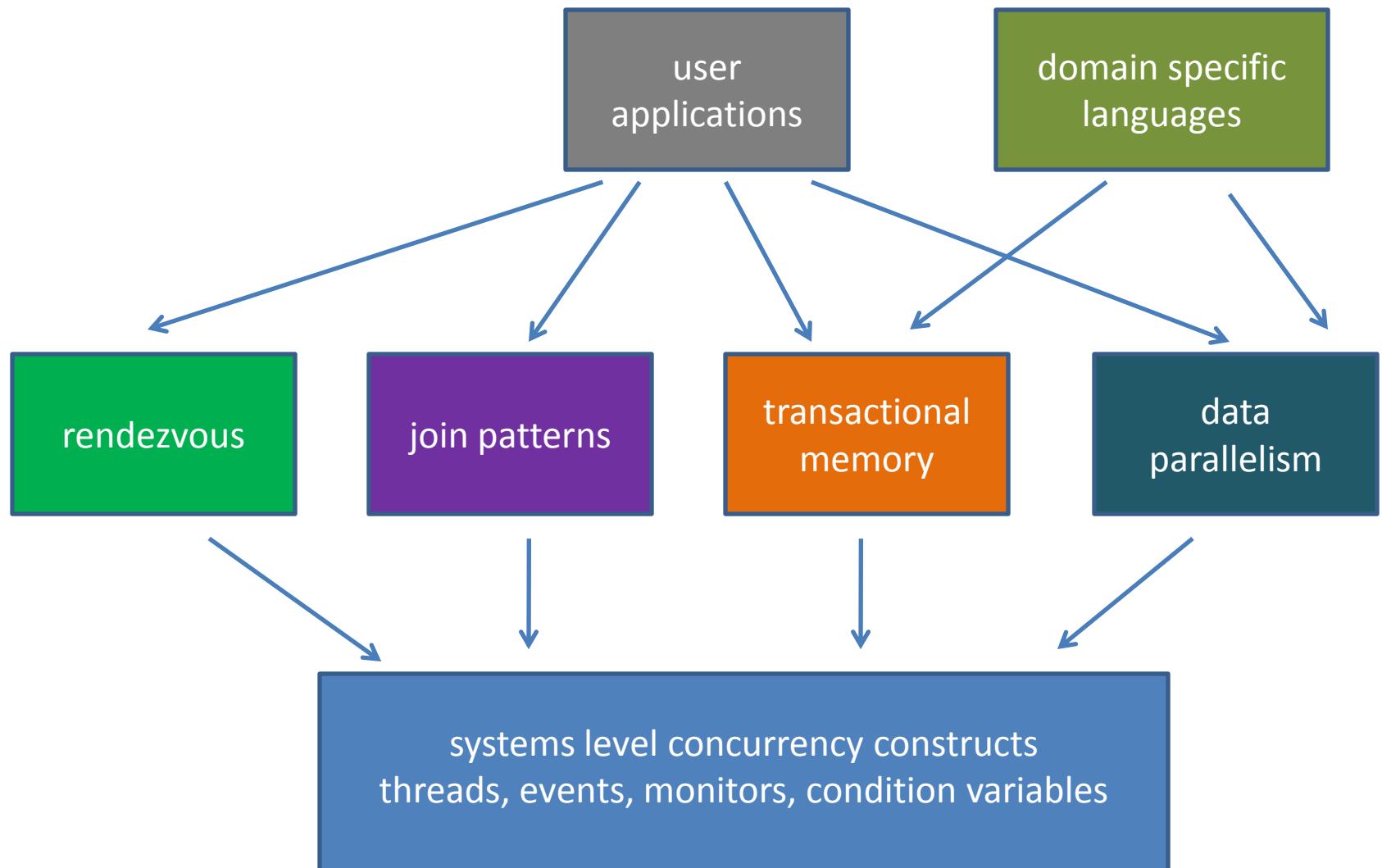


shape analysis:
synthesis of
**dynamic data
structures (C)**
[MPI and CMU]

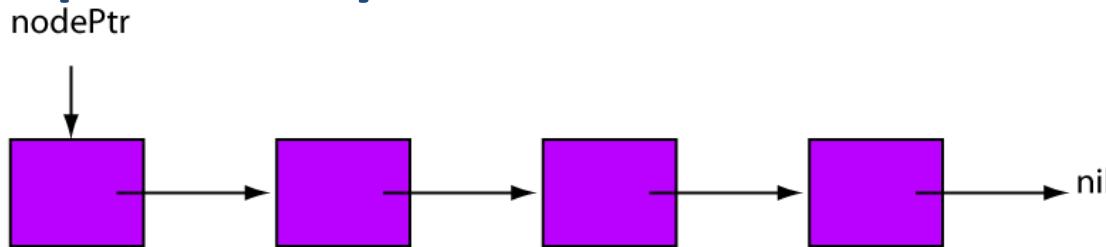


Accelerator/FPGA:
synthesis of **data
parallel** programs in
C++
[MSR Redmond]

1. Kiwi: Multi-threaded Models in C#

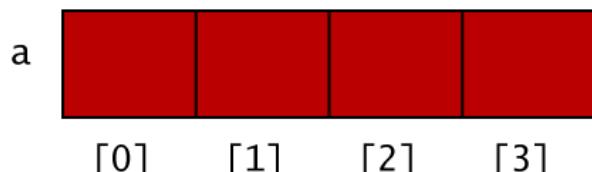


2. Synthesis of C Programs using the Heap for Dynamic Data Structures



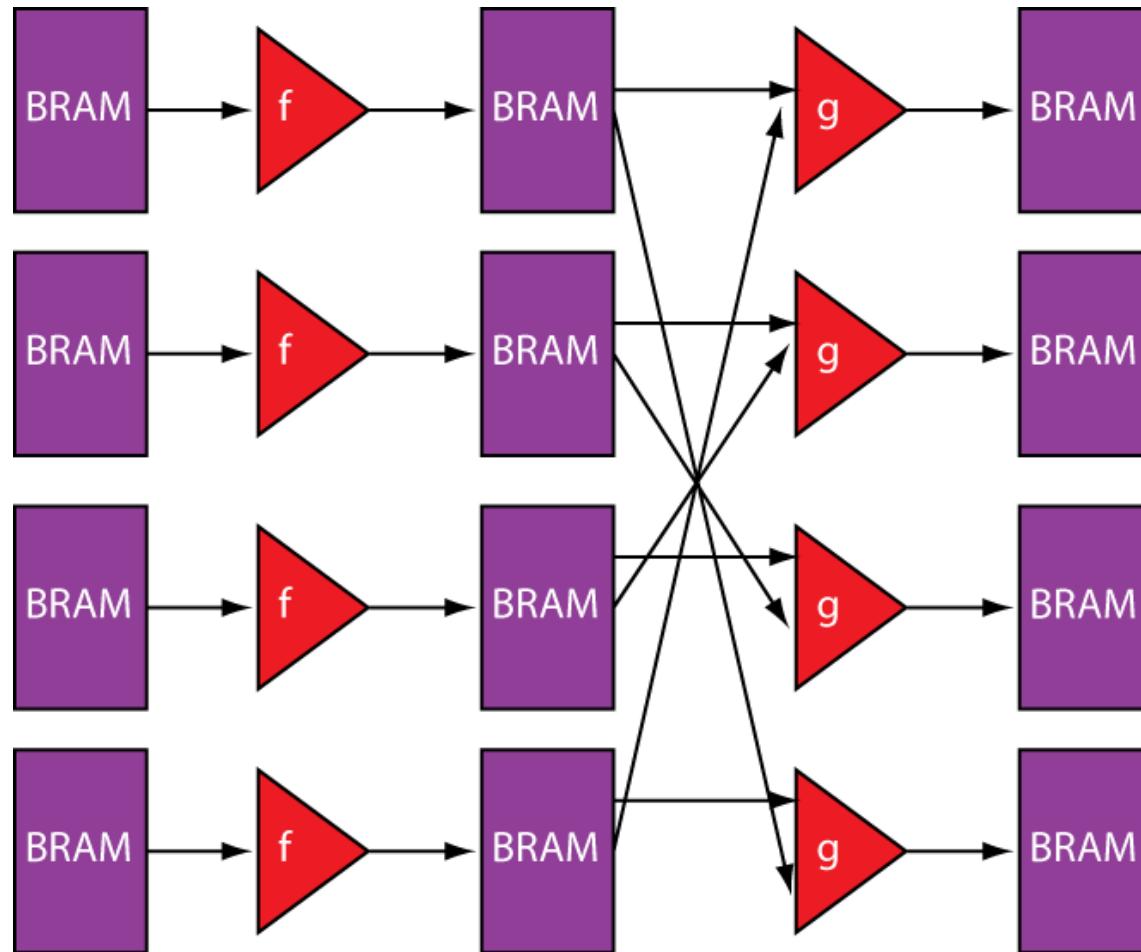
```
while (nodePtr != null)
{   ProcessNode(nodePtr);
    nodePtr = nodePtr->next;
}
```

use shape analysis tool to prove program invariant $Is(k, \text{nodePtr}, \text{null})$ in separation logic
or prove $Is(k, \text{nodePtr}, \text{null}) \And \And k < 4$ i.e. we use at most 4 cells in the circuit

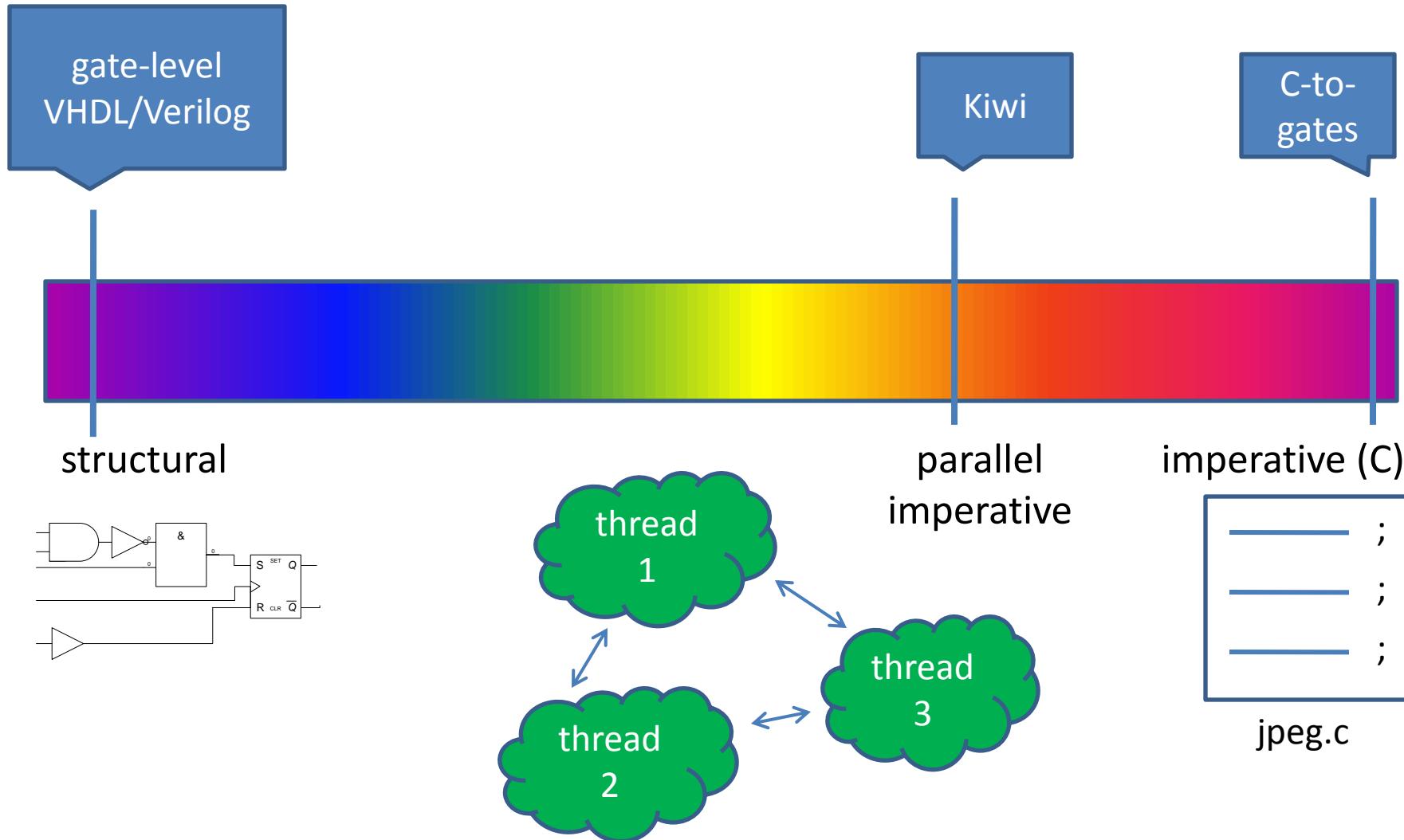


```
for (int i=0; i<4; i++)
    ProcessNode(a[i]);
```

3. Synthesis of a data-parallel DSL in C++



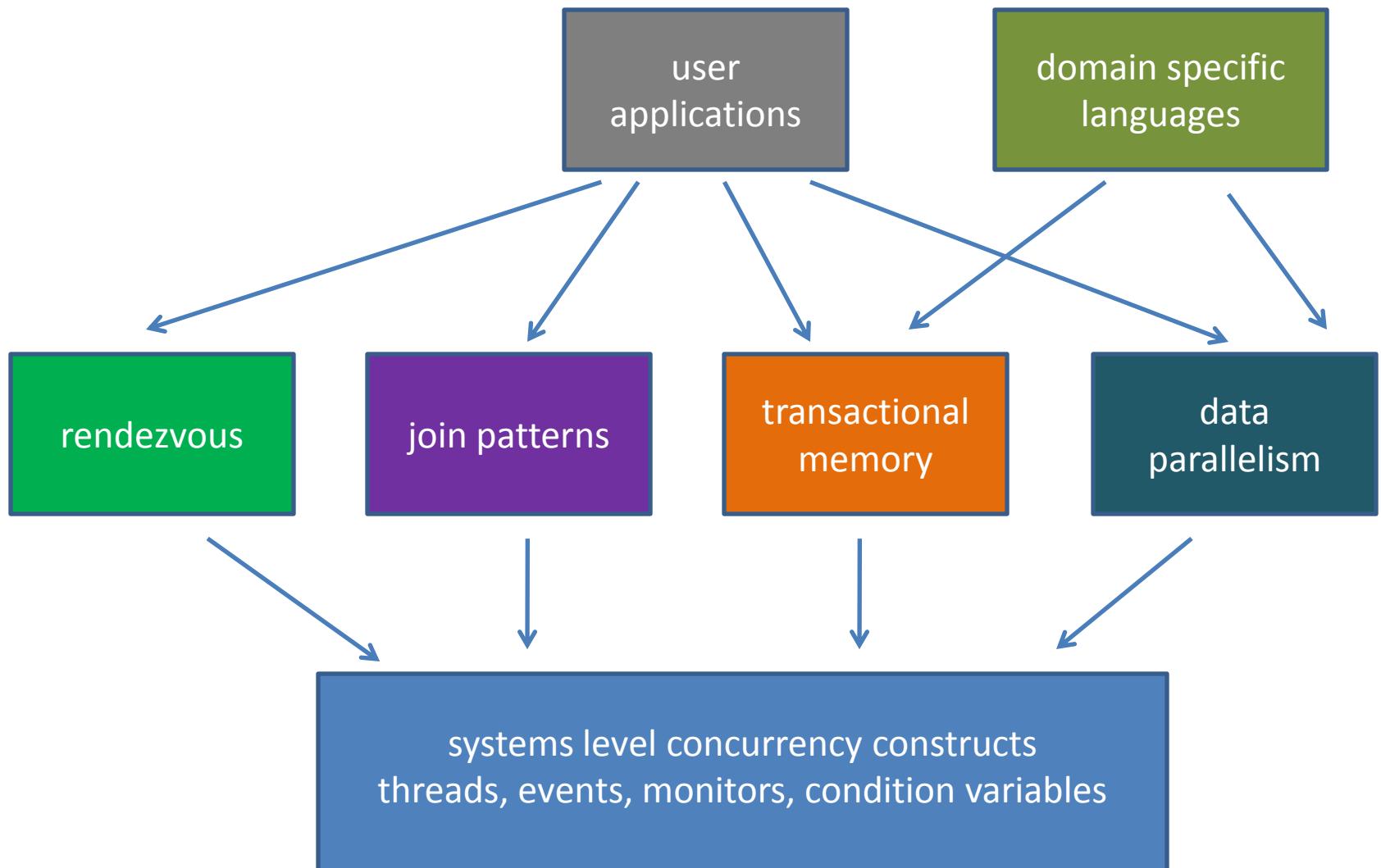
1. Kiwi

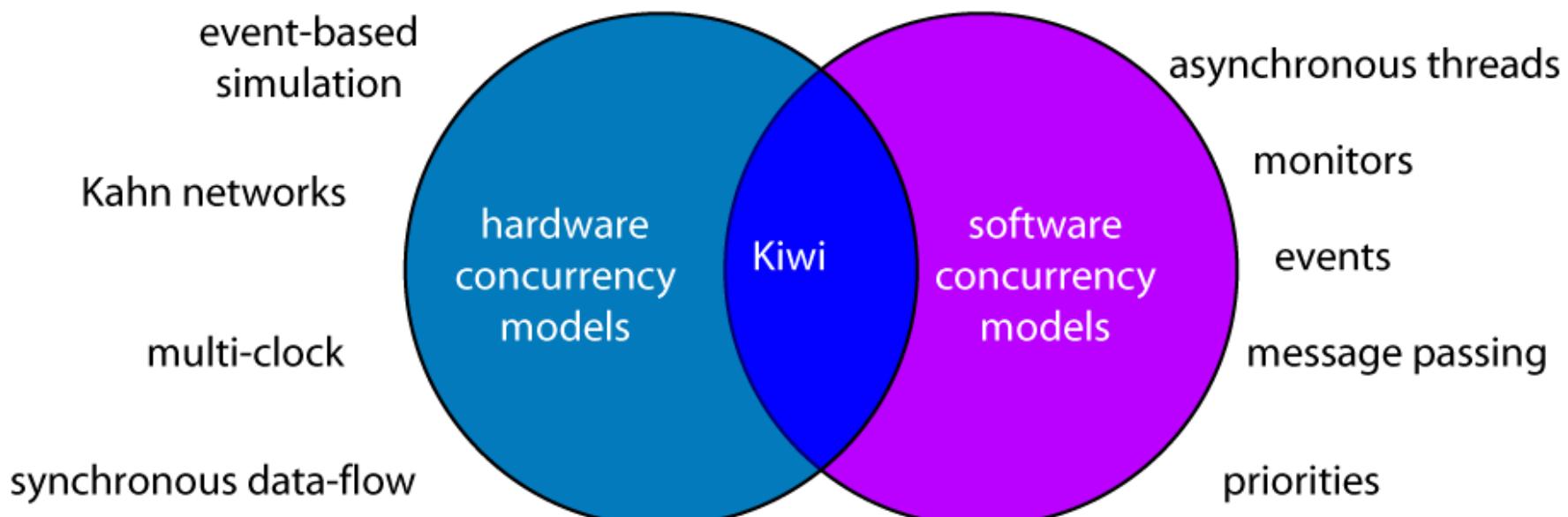


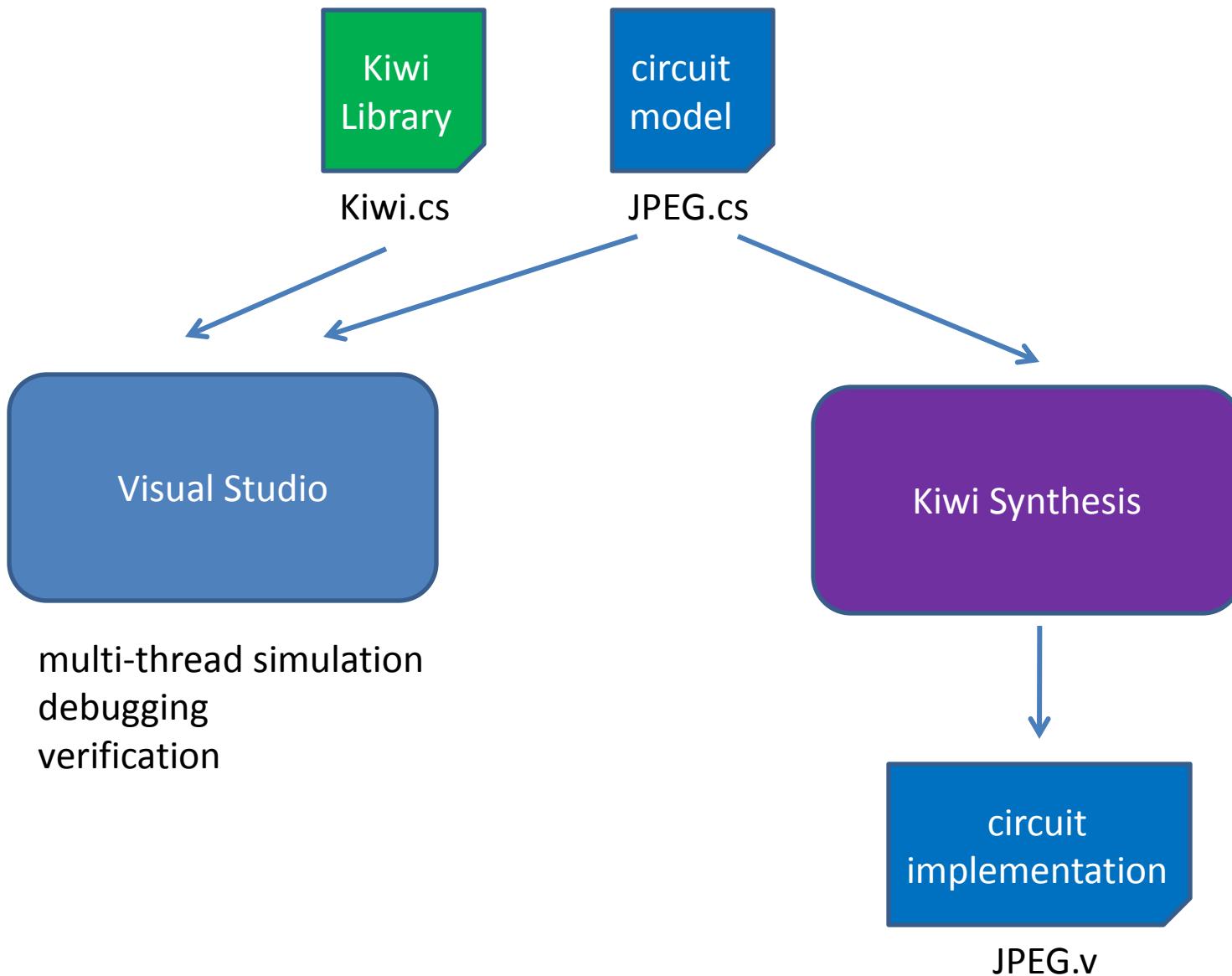
The Accidental Semi-colon

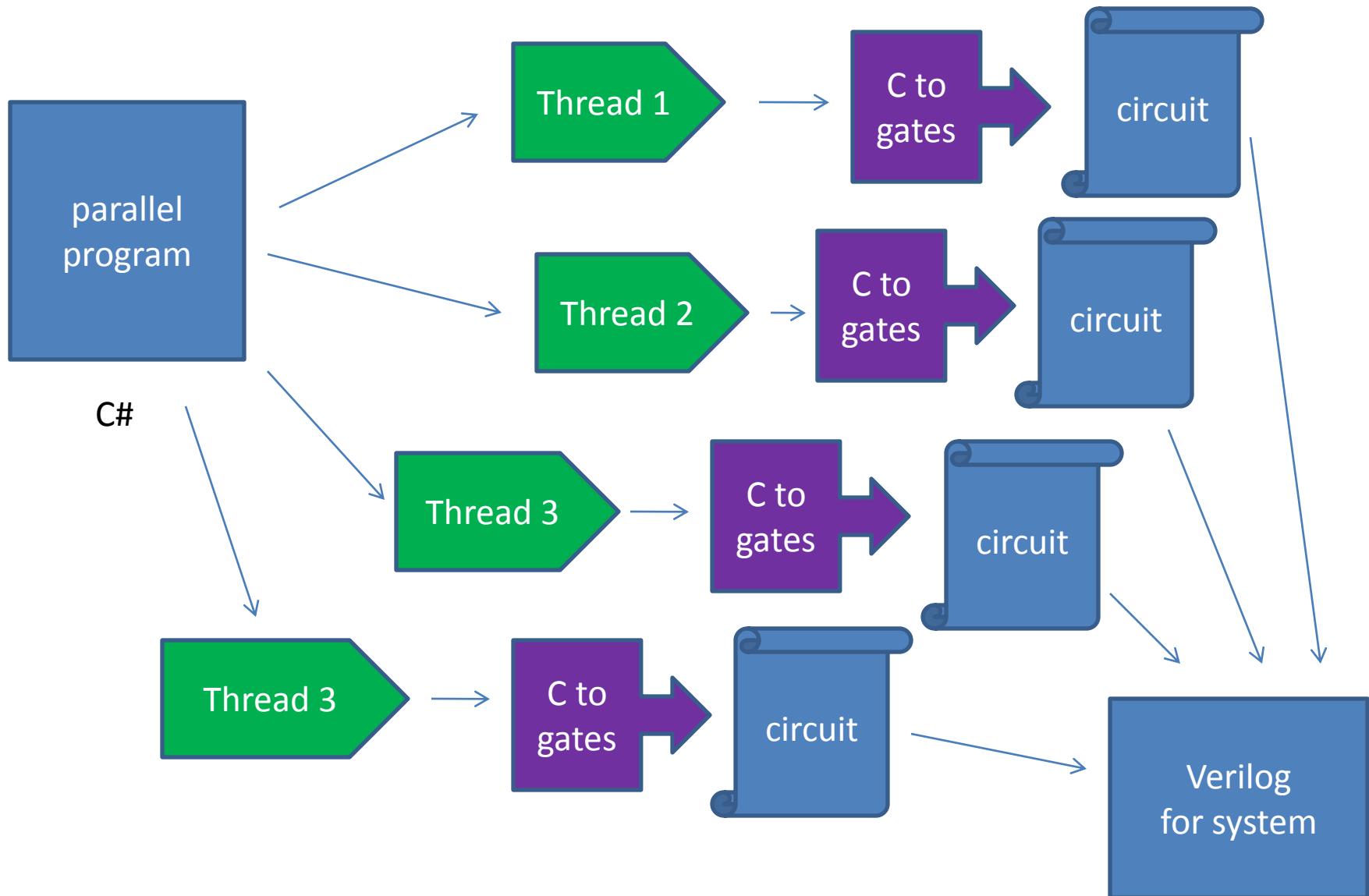


Concurrent Circuit Models









```
public static int max2(int a, int b)
{ int result;
  if (a > b)
    result = a;
  else
    result = b;
  return result;
}
```

max2(3, 7)

stack

| |
|---|
| 7 |
| 7 |

local memory

```
.method public hidebysig static
int32
max2(int32 a,
      int32 b) cil managed
{
```

// Code size 12 (0xc)

.maxstack 2

.locals init ([0] int32 result)

IL_0000: ldarg.0

IL_0001: ldarg.1

IL_0002: ble.s IL_0008

IL_0004: ldarg.0

IL_0005: stloc.0

IL_0006: br.s IL_000a

IL_0008: ldarg.1

IL_0009: stloc.0

IL_000a: ldloc.0

IL_000b: ret

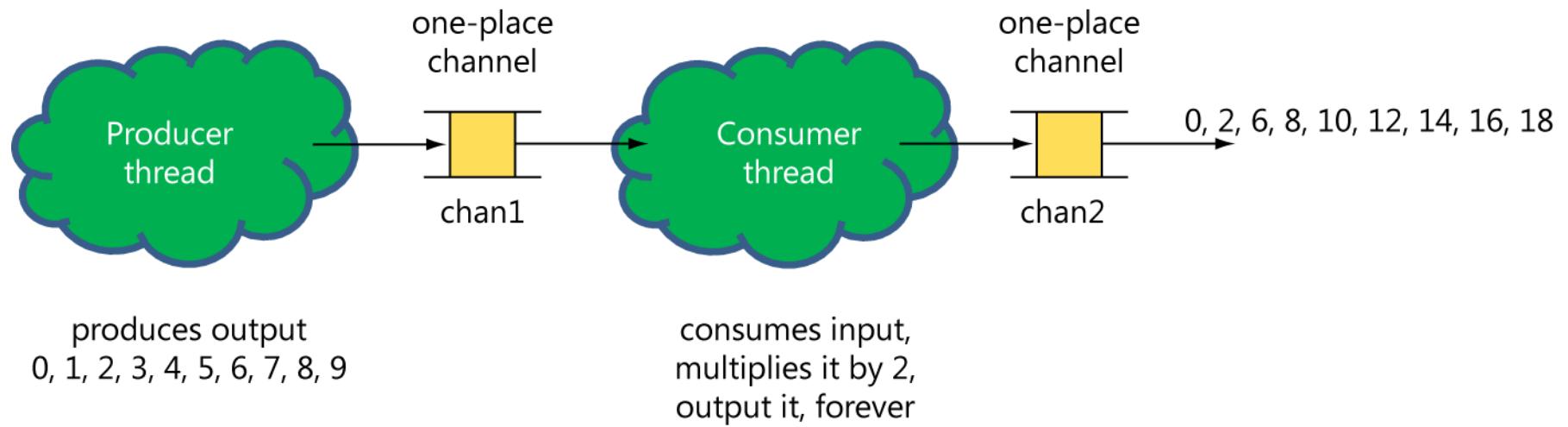
}

Writing to a Channel

```
public class Channel<T>
{
    T datum;
    bool empty = true;
    public void Write(T v)
    {
        lock (this)
        {
            while (!empty)
                Monitor.Wait(this);
            datum = v;
            empty = false;
            Monitor.PulseAll(this);
        }
    }
}
```

Reading from a Channel

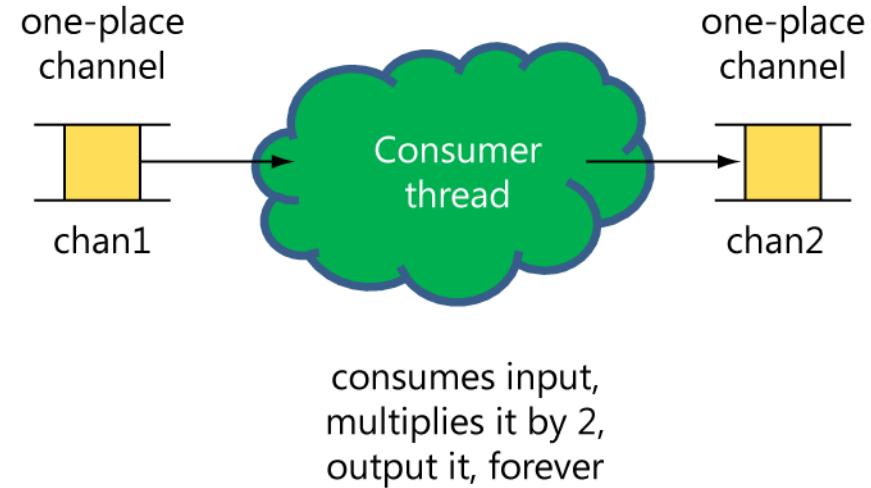
```
public T Read()
{
    T r;
    lock (this)
    {
        while (empty)
            Monitor.Wait(this);
        empty = true;
        r = datum;
        Monitor.PulseAll(this);
    }
    return r;
}
```



```

public static void Consumer()
{
    while (true)
    {
        int i = chan1.Read();
        chan2.Write(2 * i);
        Kiwi.Pause();
    }
}

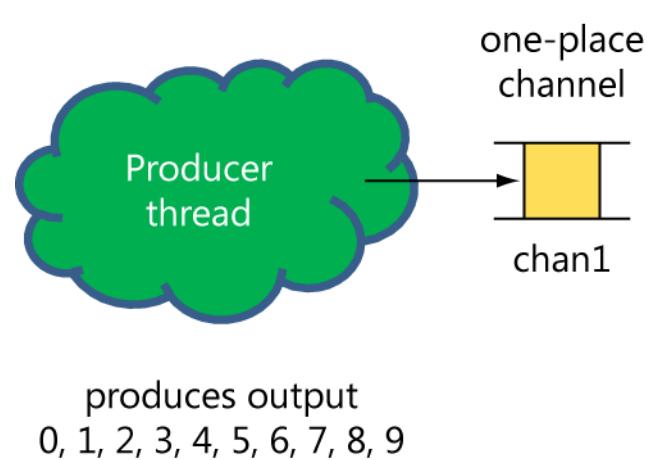
```



```

public static void Producer()
{
    for (int i = 0; i < 10; i++)
    {
        chan1.Write(i);
        Kiwi.Pause();
    }
}

```

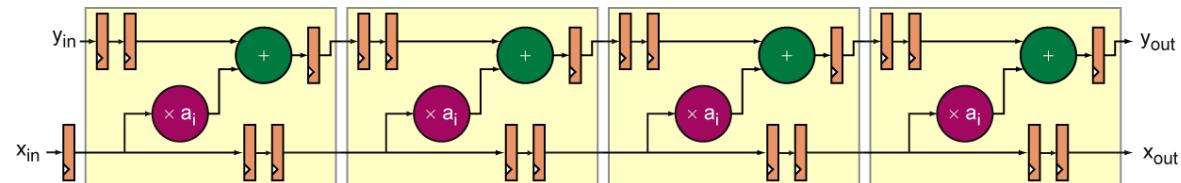
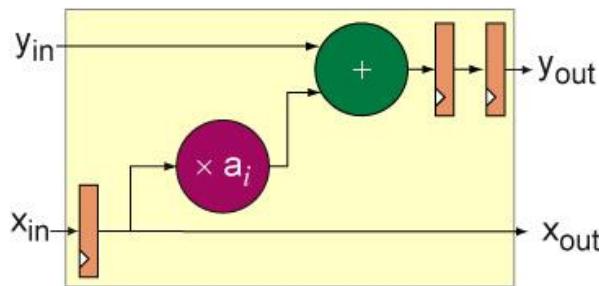
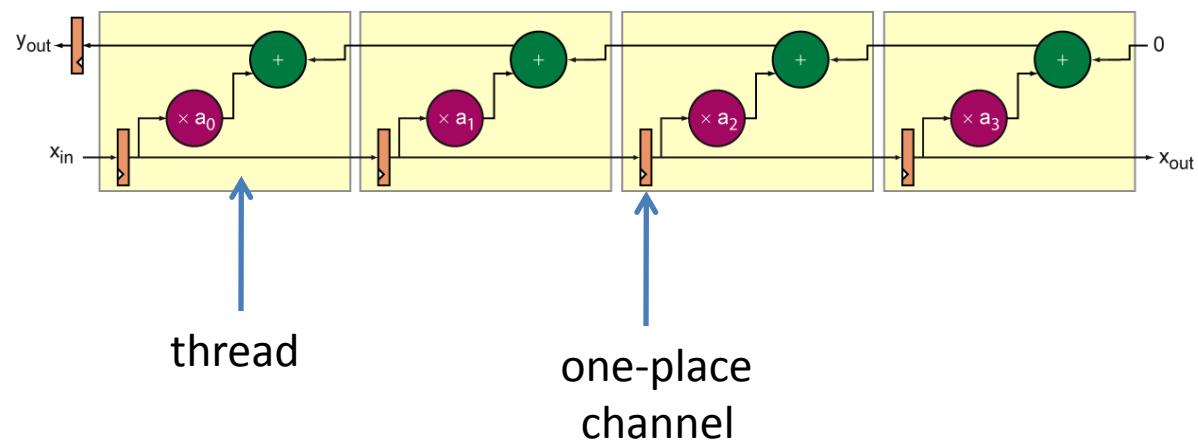
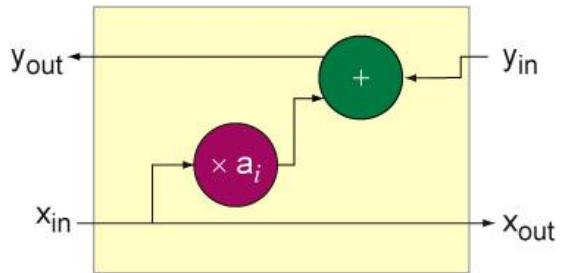


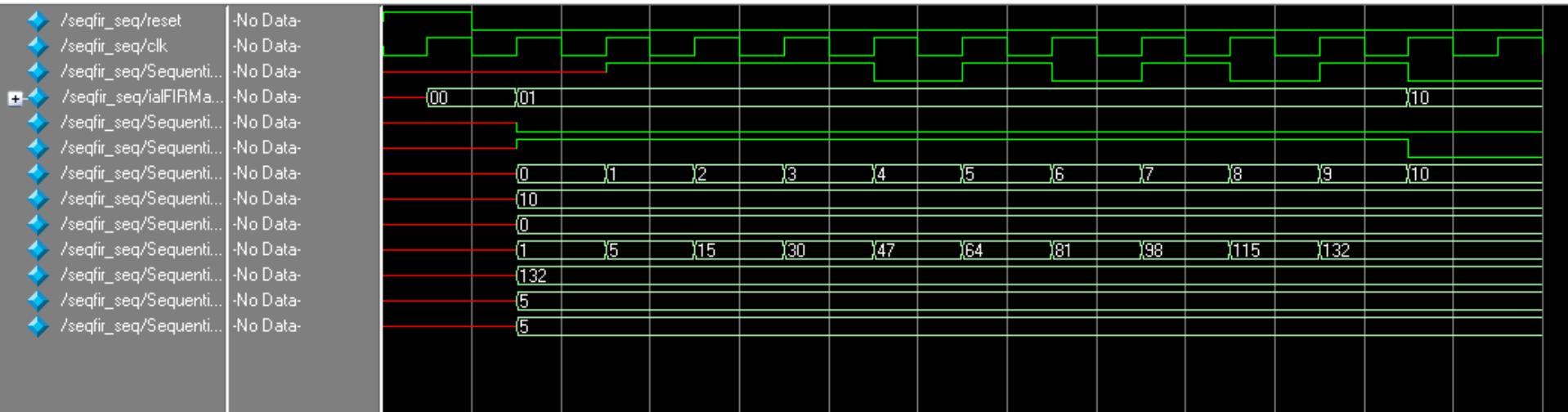
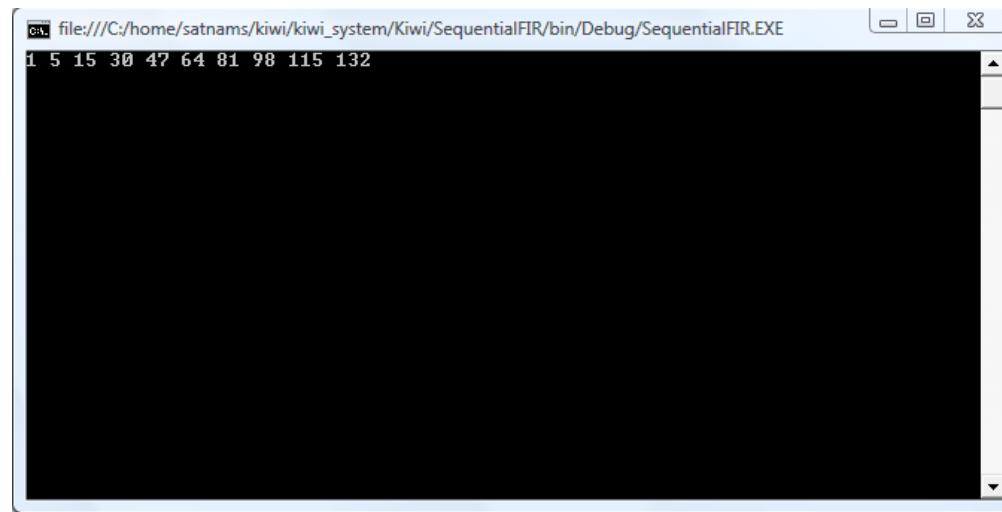
```
public static void Behaviour()
{
    Thread ProducerThread = new Thread(new ThreadStart(Producer));
    ProducerThread.Start();

    Thread ConsumerThread = new Thread(new ThreadStart(Consumer));
    ConsumerThread.Start();
}
```

Filter Example

$$y_t = \sum_{k=0}^{N-1} a_k x_{t-k}$$





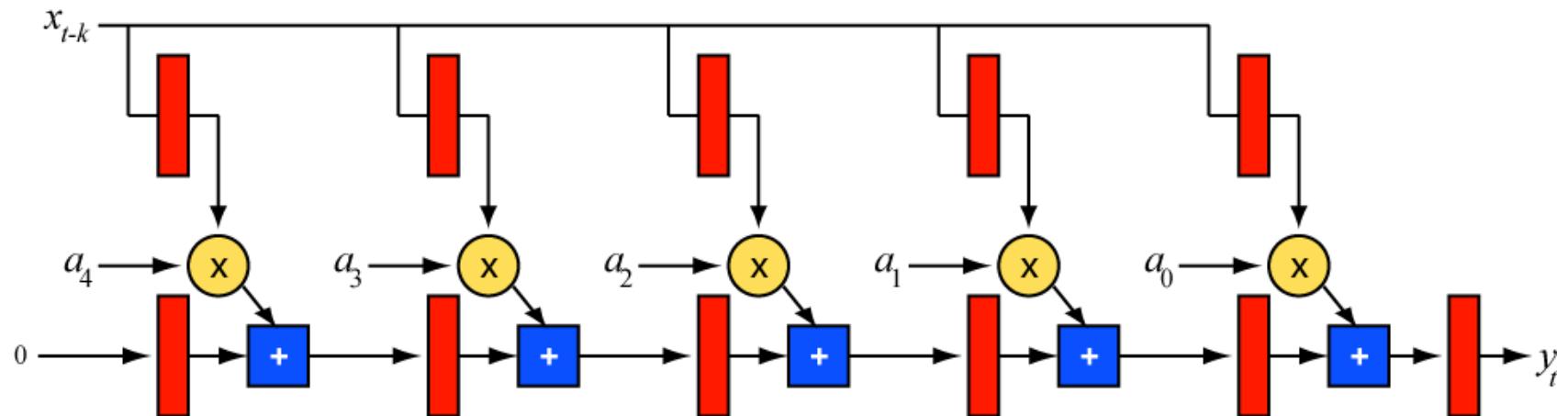
```

public static int[] SequentialFIRFunction(int[] weights, int[] input)
{
    int[] window = new int[size];
    int[] result = new int[input.Length];
    // Clear to window of x values to all zero.
    for (int w = 0; w < size; w++)
        window[w] = 0;
    // For each sample...
    for (int i = 0; i < input.Length; i++)
    {
        // Shift in the new x value
        for (int j = size - 1; j > 0; j--)
            window[j] = window[j - 1];
        window[0] = input[i];
        // Compute the result value
        int sum = 0;
        for (int z = 0; z < size; z++)
            sum += weights[z] * window[z];
        result[i] = sum;
    }
    return result;
}

```

$$y_t = \sum_{k=0}^{N-1} a_k x_{t-k}$$

Transposed Filter

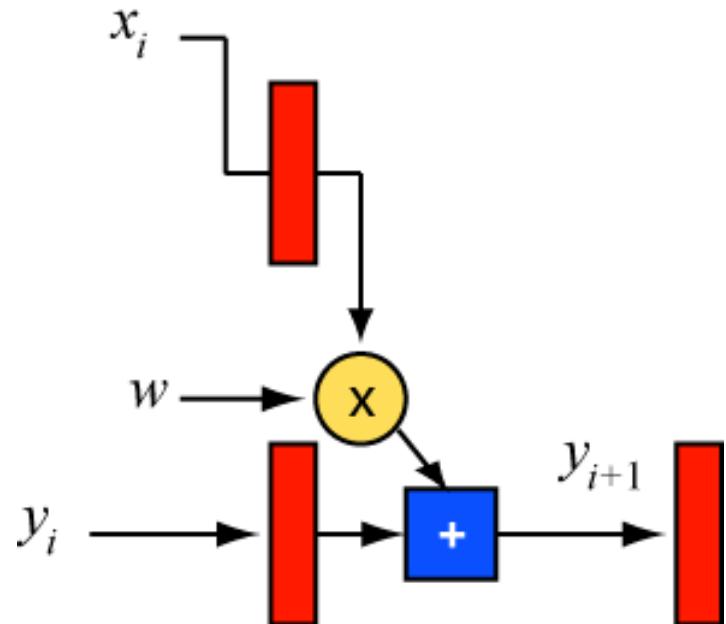


$$y_t = \sum_{k=0}^{N-1} a_k x_{t-k}$$

```

static void Tap(int i, byte w,
    Kiwi.Channel<byte> xIn,
    Kiwi.Channel<int> yIn,
    Kiwi.Channel<int> yout)
{
    byte x;
    int y;
    while (true)
    {
        y = yIn.Read();
        x = xIn.Read();
        yout.Write(x * w + y);
    }
}

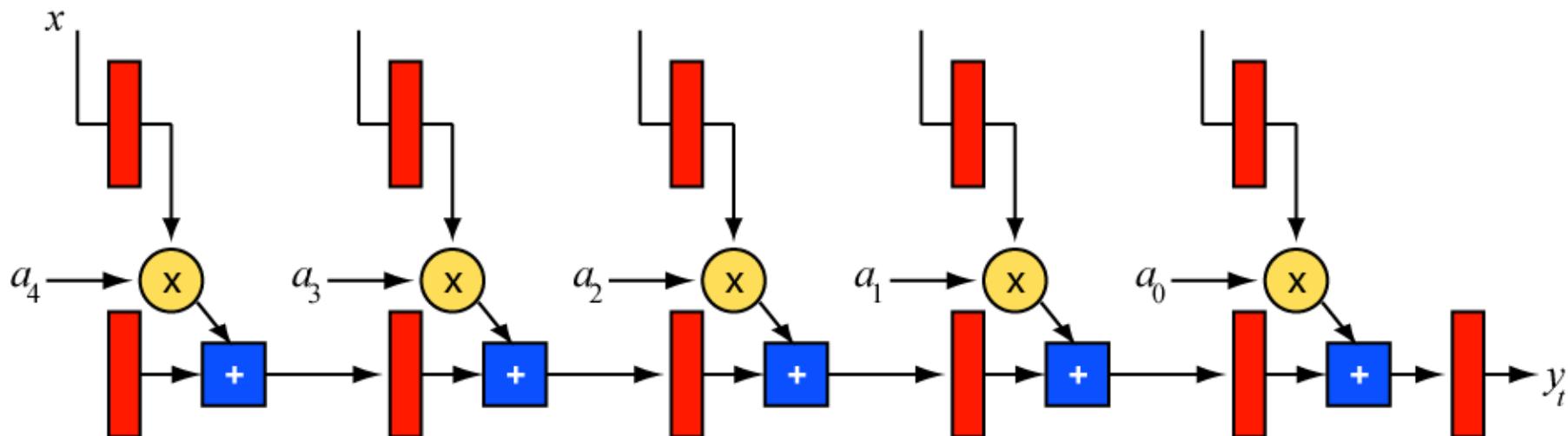
```



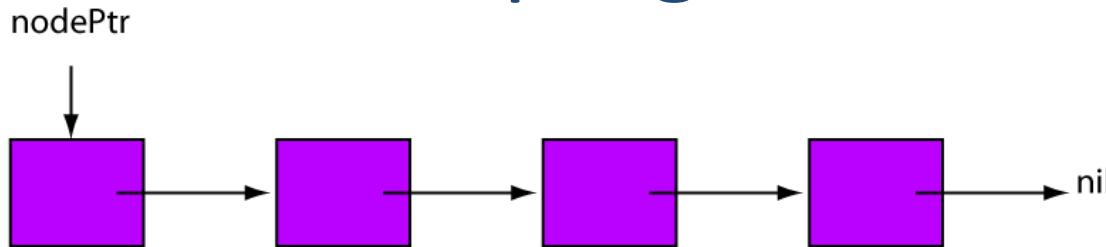
```

// Connect up the taps for a transposed filter
for (int i = 0; i < size; i++)
{
    int j = i; // Quiz: why do we need the local j?
    Thread tapThread = new Thread(delegate() { Tap(j, weights[j],
                                                Xchannels[j],
                                                Ychannels[j],
                                                Ychannels[j+1]); });
    tapThread.Start();
}

```

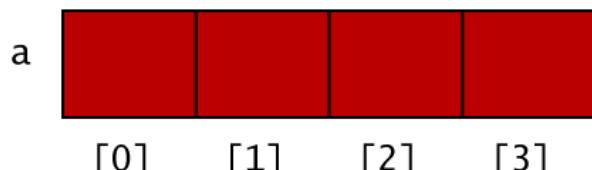


2. Synthesis of dynamic data structures in C programs



```
while (nodePtr != null)
{   ProcessNode(nodePtr);
    nodePtr = nodePtr->next;
}
```

use shape analysis tool to prove program invariant $\text{Is}(k, \text{nodePtr}, \text{null})$ in separation logic
or prove $\text{Is}(k, \text{nodePtr}, \text{null}) \&\& k < 4$ i.e. we use at most 4 cells in the circuit



```
for (int i=0; i<4; i++)
    ProcessNode(a[i]);
```





| Program | Bound | ALUTs | Registers | Block Mem | Blocks | Speed |
|----------|---------------------|--------|-----------|-----------|--------|--------|
| merge | $8 * n_1 + 8 * n_2$ | 5,157 | 4,694 | 8,192 | 2 | 90MHz |
| prio | $8 * n$ | 5,859 | 4,598 | 4,096 | 1 | 83MHz |
| packet | $8 * n + 8$ | 9,413 | 9,158 | 8,192 | 2 | 76MHz |
| huffman | $48 * n_1 - 20$ | 20,678 | 11,116 | 12,288 | 3 | 76MHz |
| bst_dict | $24 * n_1$ | 5,786 | 5,660 | 8,192 | 2 | 125MHz |

```

void prio(int n,in _signal i,out _signal o) {
    LINK *tmp,*c,*buffer;
    assume( n>0 );
    while (1) {
        buffer = NULL;
        // Build up an n-sized sorted buffer
        for (int k=0;k<n;k++) {
            buffer = sorted _insert(input(i),buffer);
        }
        // Send the sorted list to the output and
        // deallocate the buffer as we walk it
        c=buffer;
        while(c!=NULL) {
            output(o,c->data);
            tmp = c;
            c = c->next;
            free(tmp);
        }
    }
}

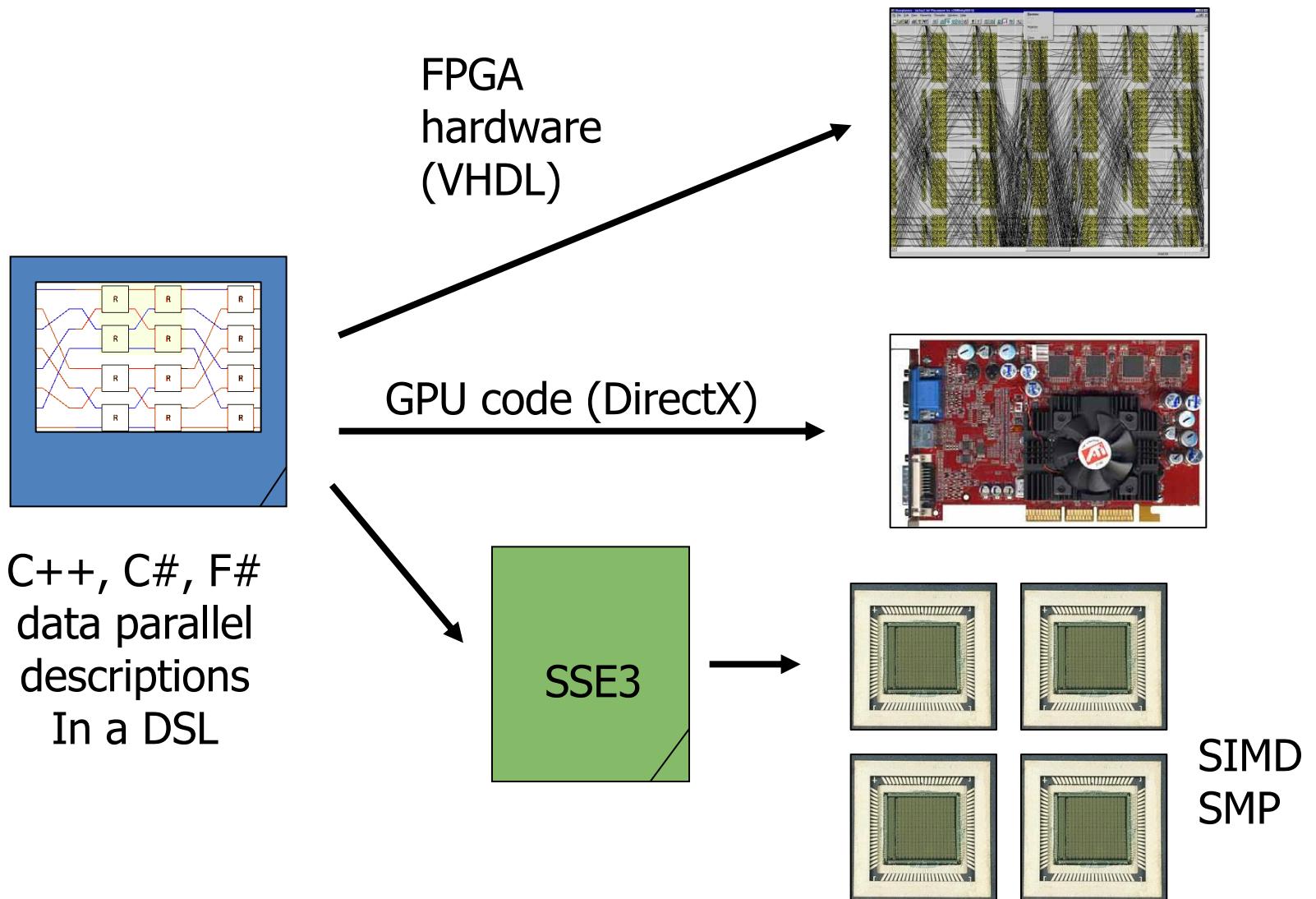
```

```

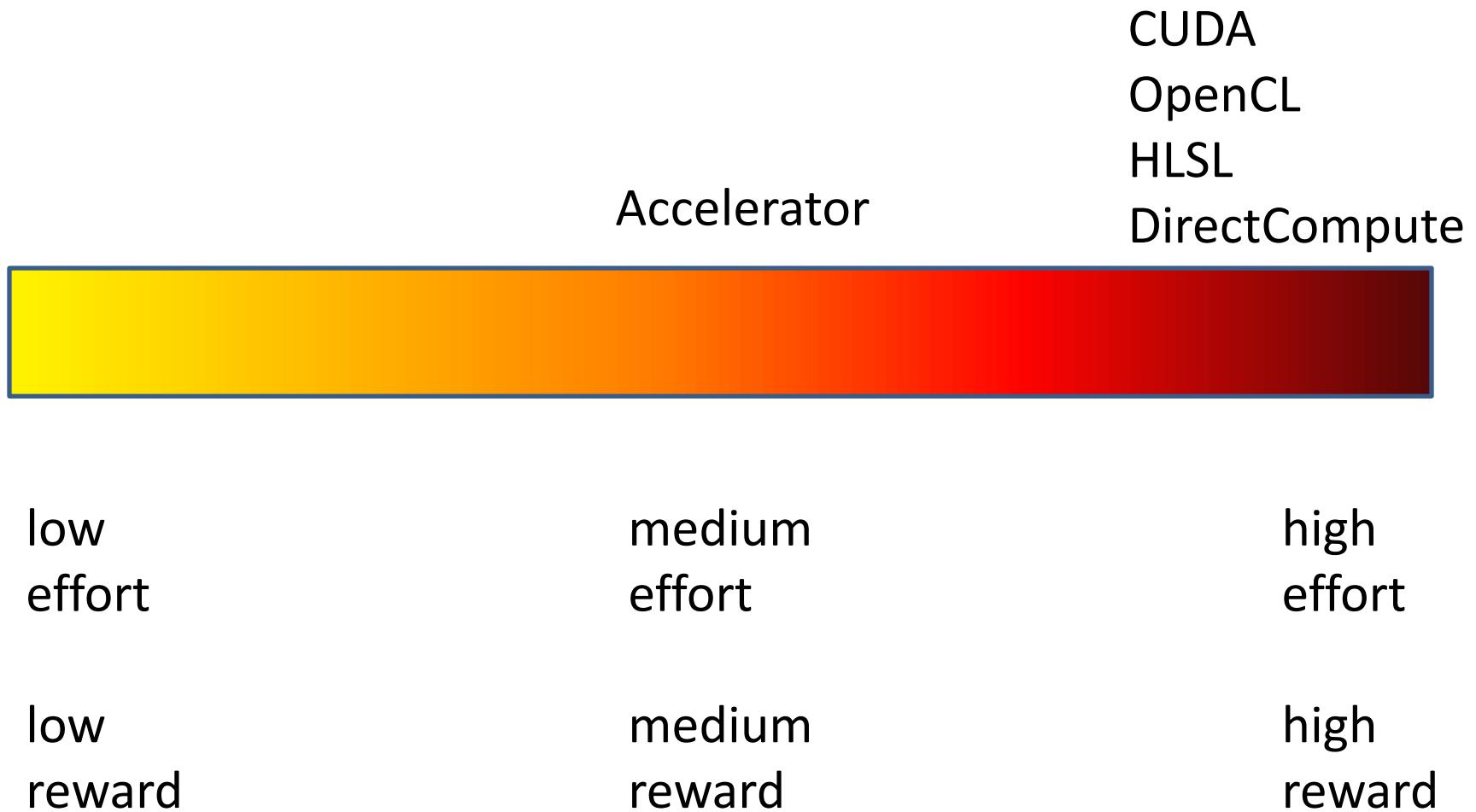
LINK * sorted _insert(int data, LINK *l) {
    LINK * elem = l;
    LINE * prev = NULL;
    LINK * x = (LINK*)malloc(sizeof(LINK));
    assert(x!=NULL);
    x->data = data;
    while (elem != NULL) {
        if (elem->data >= x->data) {
            x->next = elem;
            if (prev == NULL) { l = x; return l; }
            prev->next = x;
            return l;
        }
        prev = elem;
        elem = elem->next;
    }
    x->next = elem;
    if (prev == NULL) { l = x; return l; }
    prev->next = x;
    return l;
}

```

3. Accelerator



Effort vs. Reward



```
using System;
using Microsoft.ParallelArrays;

namespace AddArraysPointwise
{
    class AddArraysPointwiseDX9
    {
        static void Main(string[] args)
        {
            var x = new FloatParallelArray (new[] {1.0F, 2, 3, 4, 5});
            var y = new FloatParallelArray (new[] {6.0F, 7, 8, 9, 10});
            var dx9Target = new DX9Target();
            var z = x + y;
            foreach (var i in dx9Target.ToArray1D (z))
                Console.WriteLine(i);
        }
    }
}
```

```
using System;
using Microsoft.ParallelArrays;

namespace AddArraysPointwiseMulticore
{
    class AddArraysPointwiseMulticore
    {
        static void Main(string[] args)
        {
            var x = new FloatParallelArray (new[] {1.0F, 2, 3, 4, 5});
            var y = new FloatParallelArray (new[] {6.0F, 7, 8, 9, 10});
            var multicoreTarget = new X64MulticoreTarget();
            var z = x + y;
            foreach (var i in multicoreTarget.ToArray1D (z))
                Console.Write(i + " ");
            Console.WriteLine();
        }
    }
}
```

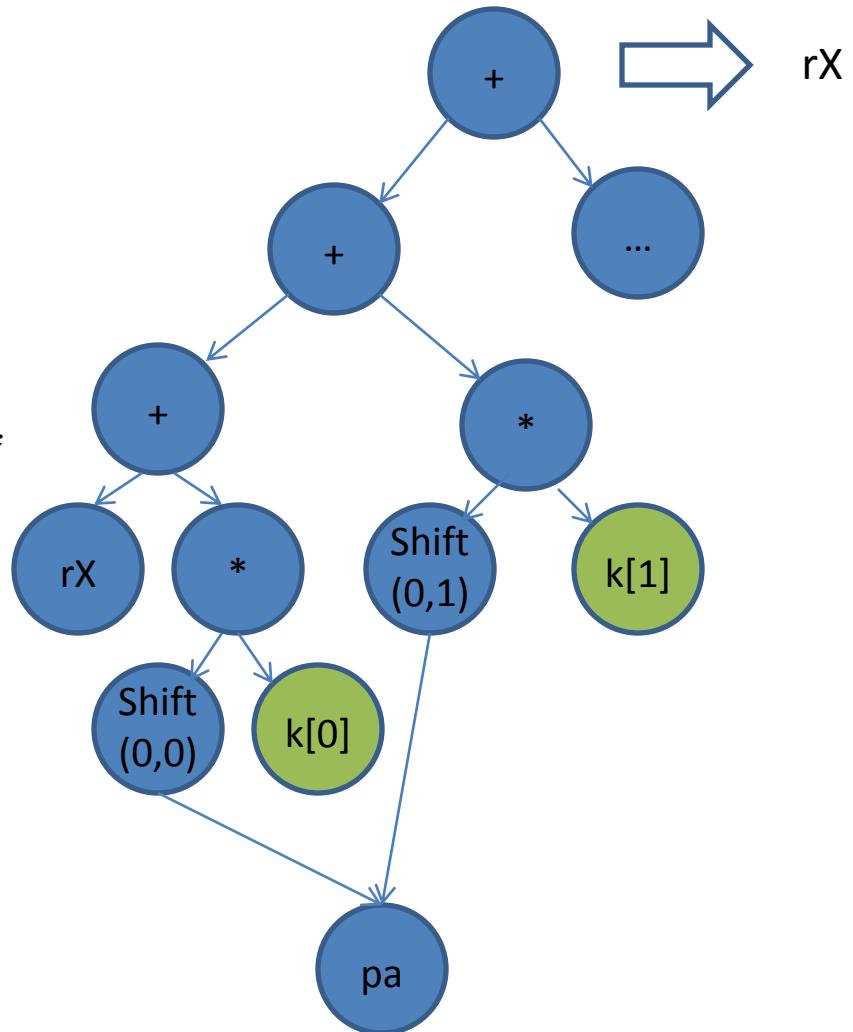
```
using System;
using Microsoft.ParallelArrays;

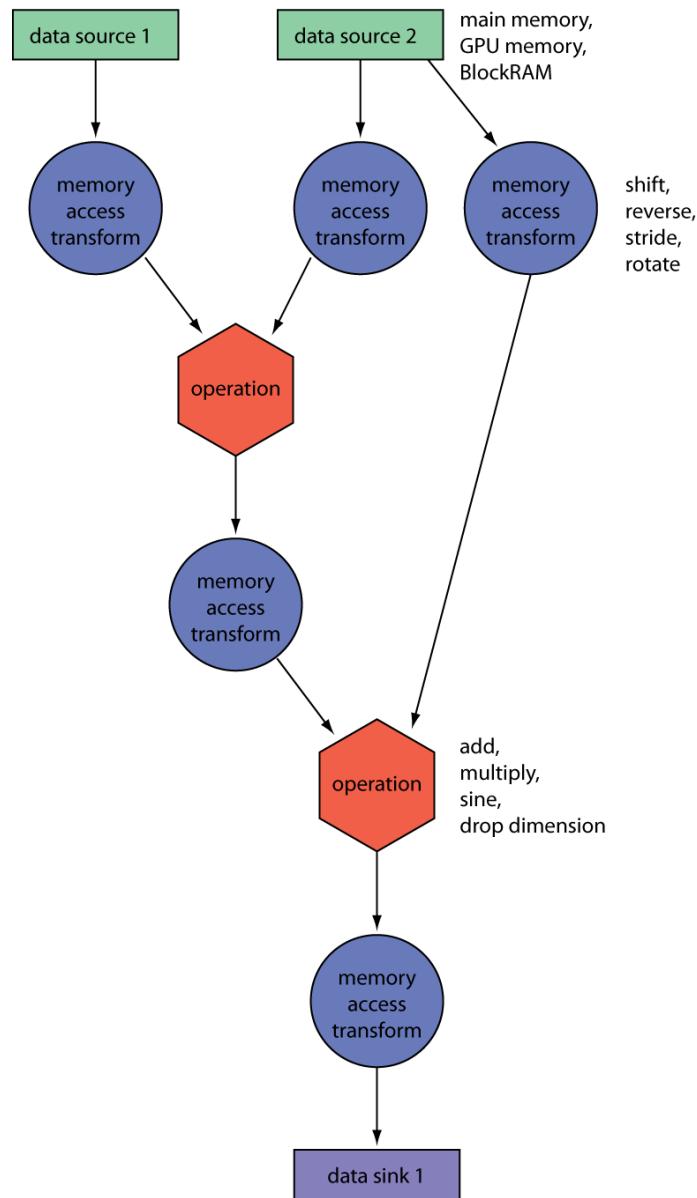
namespace AddArraysPointwiseFPGA
{
    class AddArraysPointwiseMulticore
    {
        static void Main(string[] args)
        {
            var x = new FloatParallelArray (new[] {1.0F, 2, 3, 4, 5});
            var y = new FloatParallelArray (new[] {6.0F, 7, 8, 9, 10});
            var fpgaTarget = new FPGATarget();
            var z = x + y;
            fpgaTarget.ToArray1D (z) ;
        }
    }
}
```

```
open System
open Microsoft.ParallelArrays
let main(args) =
    let x = new FloatParallelArray (Array.map float32 [|1; 2; 3; 4; 5|])
    let y = new FloatParallelArray (Array.map float32 [|6; 7; 8; 9; 10|])
    let z = x + y
    use dx9Target = new DX9Target()
    let zv = dx9Target.ToArray1D(z)
    printf "%A\n" zv
    0
```

Expression Graphs

```
FPA pa = new FPA(bitmap);  
  
// Convolve in X direction  
FPA rX = new FPA(0, pa.Shape);  
  
for (int i = 0; i < kernel.Length; i++)  
{  
    rX += PA.Shift(pa, 0, i) * kernel[i];  
}
```





x

| | | | | | | | |
|---|---|---|---|---|---|---|---|
| 7 | 2 | 5 | 9 | 3 | 8 | 6 | 4 |
| 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 |

a

| | | | | |
|---|---|---|---|---|
| 3 | 4 | 7 | 5 | 2 |
| 4 | 3 | 2 | 1 | 0 |

a * x

| | | | | |
|---|----|----|----|----|
| 6 | 20 | 63 | 15 | 16 |
| 4 | 3 | 2 | 1 | 0 |

y = sum (a *x)

| | | | | | | | |
|---|---|---|---|---|-----|---|---|
| | | | | | 120 | | |
| 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 |

```
public static int[] SequentialFIRFunction(int[] weights, int[] input)
{
    int[] window = new int[size];
    int[] result = new int[input.Length];
    // Clear to window of x values to all zero.
    for (int w = 0; w < size; w++)
        window[w] = 0;
    // For each sample...
    for (int i = 0; i < input.Length; i++)
    {
        // Shift in the new x value
        for (int j = size - 1; j > 0; j--)
            window[j] = window[j - 1];
        window[0] = input[i];
        // Compute the result value
        int sum = 0;
        for (int z = 0; z < size; z++)
            sum += weights[z] * window[z];
        result[i] = sum;
    }
    return result;
}
```

$$y_t = \sum_{k=0}^{N-1} a_k x_{t-k}$$

$$y = [y[0], y[1], y[2], y[3], y[4], y[5], y[6], y[7]]$$

$$\begin{aligned}y[0] &= a[0]x[0] + \textcolor{blue}{a[1]x[-1]} + a[2]x[-2] + a[3]x[-3] + a[4]x[-4] \\y[1] &= a[0]x[1] + \textcolor{blue}{a[1]x[0]} + a[2]x[-1] + a[3]x[-2] + a[4]x[-3] \\y[2] &= a[0]x[2] + \textcolor{blue}{a[1]x[1]} + a[2]x[0] + a[3]x[-1] + a[4]x[-2] \\y[3] &= a[0]x[3] + \textcolor{blue}{a[1]x[2]} + a[2]x[1] + a[3]x[0] + a[4]x[-1] \\y[4] &= a[0]x[4] + \textcolor{blue}{a[1]x[3]} + a[2]x[2] + a[3]x[1] + a[4]x[0] \\y[5] &= a[0]x[5] + \textcolor{blue}{a[1]x[4]} + a[2]x[3] + a[3]x[2] + a[4]x[1] \\y[6] &= a[0]x[6] + \textcolor{blue}{a[1]x[5]} + a[2]x[4] + a[3]x[3] + a[4]x[2] \\y[7] &= a[0]x[7] + \textcolor{blue}{a[1]x[6]} + a[2]x[5] + a[3]x[4] + a[4]x[3]\end{aligned}$$

$$\begin{aligned}y &= [y[0], y[1], y[2], y[3], y[4], y[5], y[6], y[7]] \\&= \textcolor{green}{a[0]} * [x[0], x[1], x[2], x[3], x[4], x[5], x[6], x[7]] + \\&\quad \textcolor{green}{a[1]} * [x[-1], x[0], x[1], x[2], x[3], x[4], x[5], x[6]] + \\&\quad \textcolor{green}{a[2]} * [x[-2], x[-1], x[0], x[1], x[2], x[3], x[4], x[5]] + \\&\quad \textcolor{green}{a[3]} * [x[-3], x[-2], x[-1], x[0], x[1], x[2], x[3], x[4]] + \\&\quad \textcolor{green}{a[4]} * [x[-4], x[-3], x[-2], x[-1], x[0], x[1], x[2], x[3]]\end{aligned}$$

$\text{shift}(x, 0) = [7, 2, 5, 9, 3, 8, 6, 4] = x$

$\text{shift}(x, -1) = [7, 7, 2, 5, 9, 3, 8, 6]$

$\text{shift}(x, -2) = [7, 7, 7, 2, 5, 9, 3, 8]$

| | | | | | |
|---|---|---|---|---|---|
| 0 | 0 | 1 | 2 | 3 | 4 |
|---|---|---|---|---|---|

shift -1

| | | | | | |
|---|---|---|---|---|---|
| 0 | 1 | 2 | 3 | 4 | 5 |
|---|---|---|---|---|---|

x

| | | | | | |
|---|---|---|---|---|---|
| 1 | 2 | 3 | 4 | 5 | 5 |
|---|---|---|---|---|---|

shift + 1

$$\begin{aligned}
 y &= [y[0], y[1], y[2], y[3], y[4], y[5], y[6], y[7]] \\
 &= a[0] * [x[0], x[1], x[2], x[3], x[4], x[5], x[6], x[7]] + \\
 &\quad a[1] * [x[-1], x[0], x[1], x[2], x[3], x[4], x[5], x[6]] + \\
 &\quad a[2] * [x[-2], x[-1], x[0], x[1], x[2], x[3], x[4], x[5]] + \\
 &\quad a[3] * [x[-3], x[-2], x[-1], x[0], x[1], x[2], x[3], x[4]] + \\
 &\quad a[4] * [x[-4], x[-3], x[-2], x[-1], x[0], x[1], x[2], x[3]]
 \end{aligned}$$

$$\begin{aligned}
 y = & a[0] * \text{shift}(x, 0) + \\
 & a[1] * \text{shift}(x, -1) + \\
 & a[2] * \text{shift}(x, -2) + \\
 & a[3] * \text{shift}(x, -3) + \\
 & a[4] * \text{shift}(x, -4)
 \end{aligned}$$

shift (x, 0)

shift (x, -1)

shift (x, -2)

shift (x, -3)

shift (x, -4)

| | | | | | | | |
|---|---|---|---|---|---|---|---|
| 7 | 2 | 5 | 9 | 3 | 8 | 6 | 4 |
| 7 | 7 | 2 | 5 | 9 | 3 | 8 | 6 |
| 7 | 7 | 7 | 2 | 5 | 9 | 3 | 8 |
| 7 | 7 | 7 | 7 | 2 | 5 | 9 | 3 |
| 7 | 7 | 7 | 7 | 7 | 2 | 5 | 9 |

→ a[0] * shift (x, 0)

→ a[1] * shift (x, -1)

→ a[2] * shift (x, -2)

→ a[3] * shift (x, -3)

→ a[4] * shift (x, -4)

| | | | | | | | | |
|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 14 | 4 | 10 | 18 | 6 | 16 | 12 | 8 | |
| 35 | 35 | 10 | 25 | 45 | 15 | 40 | 30 | |
| 49 | 49 | 49 | 14 | 35 | 63 | 21 | 56 | |
| 28 | 28 | 28 | 28 | 8 | 20 | 36 | 12 | |
| 21 | 21 | 21 | 21 | 21 | 6 | 15 | 27 | |
| | | | | | | | | |
| ↓ | ↓ | ↓ | ↓ | ↓ | ↓ | ↓ | ↓ | |
| + | + | + | + | + | + | + | + | |
| y = | 147 | 137 | 118 | 106 | 115 | 120 | 124 | 133 |



```
using Microsoft.ParallelArrays;
using A = Microsoft.ParallelArrays.ParallelArrays;
namespace AcceleratorSamples
{
    public class Convolver
    {
        for (int i = 0; i < a.Length; i++)
            ypar += a[i] * A.Shift(xpar, -i);
        var n = a.Length;
        var ypar = new FloatParallelArray(0.0f, new [] { n });
        for (int i = 0; i < a.Length; i++)
            ypar += a[i] * A.Shift(xpar, -i);
        float[] result = computeTarget.ToArray1D(ypar);
        return result;
    }
}
```

```
using Microsoft.ParallelArrays;
using A = Microsoft.ParallelArrays.ParallelArrays;
namespace AcceleratorSamples
{
    public class Convolver
    {
        public static float[,] Convolver1D_2DInput
            (Target computeTarget, float[] a, float[,] x)

var shiftBy = new [] {0, 0} ;
for (var i = 0; i < a.Length; i++)
{
    shiftBy[1] = -i;
    ypar += a[i] * A.Shift(xpar, shiftBy);
}
    ypar += a[i] * A.Shift(xpar, shiftBy);
}
var result = computeTarget.ToArray2D(ypar);
return result;
}
}
```

| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|------------------|---|----|--|----|----|----|----|---|---|---|---|---|---|--|--|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| shift (x, 0, 0) | <table border="1"><tr><td>7</td><td>2</td><td>5</td><td>9</td></tr><tr><td>2</td><td>8</td><td>7</td><td>4</td></tr><tr><td>8</td><td>9</td><td>3</td><td>5</td></tr></table> | 7 | 2 | 5 | 9 | 2 | 8 | 7 | 4 | 8 | 9 | 3 | 5 | $\rightarrow a[0] * \text{shift}(x, 0, 0)$ 2 | <table border="1"><tr><td>14</td><td>4</td><td>10</td><td>18</td><td>6</td><td>16</td><td>12</td><td>8</td></tr><tr><td>4</td><td>16</td><td>14</td><td>8</td><td>16</td><td>18</td><td>6</td><td>10</td></tr></table> | 14 | 4 | 10 | 18 | 6 | 16 | 12 | 8 | 4 | 16 | 14 | 8 | 16 | 18 | 6 | 10 |
| 7 | 2 | 5 | 9 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 2 | 8 | 7 | 4 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 8 | 9 | 3 | 5 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 14 | 4 | 10 | 18 | 6 | 16 | 12 | 8 | | | | | | | | | | | | | | | | | | | | | | | | |
| 4 | 16 | 14 | 8 | 16 | 18 | 6 | 10 | | | | | | | | | | | | | | | | | | | | | | | | |
| shift (x, -1, 0) | <table border="1"><tr><td>7</td><td>2</td><td>5</td><td>9</td></tr><tr><td>7</td><td>2</td><td>5</td><td>9</td></tr><tr><td>3</td><td>8</td><td>6</td><td>4</td></tr></table> | 7 | 2 | 5 | 9 | 7 | 2 | 5 | 9 | 3 | 8 | 6 | 4 | $\rightarrow a[1] * \text{shift}(x, -1, 0)$ 5 | <table border="1"><tr><td>35</td><td>10</td><td>25</td><td>45</td><td>15</td><td>40</td><td>30</td><td>20</td></tr><tr><td>35</td><td>10</td><td>25</td><td>45</td><td>15</td><td>40</td><td>30</td><td>20</td></tr></table> | 35 | 10 | 25 | 45 | 15 | 40 | 30 | 20 | 35 | 10 | 25 | 45 | 15 | 40 | 30 | 20 |
| 7 | 2 | 5 | 9 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 7 | 2 | 5 | 9 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 3 | 8 | 6 | 4 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 35 | 10 | 25 | 45 | 15 | 40 | 30 | 20 | | | | | | | | | | | | | | | | | | | | | | | | |
| 35 | 10 | 25 | 45 | 15 | 40 | 30 | 20 | | | | | | | | | | | | | | | | | | | | | | | | |
| shift (x, -2, 0) | <table border="1"><tr><td>7</td><td>2</td><td>5</td><td>9</td></tr><tr><td>7</td><td>2</td><td>5</td><td>9</td></tr><tr><td>3</td><td>8</td><td>6</td><td>4</td></tr></table> | 7 | 2 | 5 | 9 | 7 | 2 | 5 | 9 | 3 | 8 | 6 | 4 | $\rightarrow a[2] * \text{shift}(x, -2, 0)$ 7 | <table border="1"><tr><td>49</td><td>14</td><td>35</td><td>63</td><td>21</td><td>56</td><td>42</td><td>28</td></tr><tr><td>49</td><td>14</td><td>35</td><td>63</td><td>21</td><td>56</td><td>42</td><td>28</td></tr></table> | 49 | 14 | 35 | 63 | 21 | 56 | 42 | 28 | 49 | 14 | 35 | 63 | 21 | 56 | 42 | 28 |
| 7 | 2 | 5 | 9 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 7 | 2 | 5 | 9 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 3 | 8 | 6 | 4 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 49 | 14 | 35 | 63 | 21 | 56 | 42 | 28 | | | | | | | | | | | | | | | | | | | | | | | | |
| 49 | 14 | 35 | 63 | 21 | 56 | 42 | 28 | | | | | | | | | | | | | | | | | | | | | | | | |
| shift (x, -3, 0) | <table border="1"><tr><td>7</td><td>2</td><td>5</td><td>9</td></tr><tr><td>7</td><td>2</td><td>5</td><td>9</td></tr><tr><td>3</td><td>8</td><td>6</td><td>4</td></tr></table> | 7 | 2 | 5 | 9 | 7 | 2 | 5 | 9 | 3 | 8 | 6 | 4 | $\rightarrow a[3] * \text{shift}(x, -3, 0)$ 4 | <table border="1"><tr><td>28</td><td>8</td><td>20</td><td>36</td><td>12</td><td>32</td><td>24</td><td>16</td></tr><tr><td>28</td><td>8</td><td>20</td><td>36</td><td>12</td><td>32</td><td>24</td><td>16</td></tr></table> | 28 | 8 | 20 | 36 | 12 | 32 | 24 | 16 | 28 | 8 | 20 | 36 | 12 | 32 | 24 | 16 |
| 7 | 2 | 5 | 9 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 7 | 2 | 5 | 9 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 3 | 8 | 6 | 4 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 28 | 8 | 20 | 36 | 12 | 32 | 24 | 16 | | | | | | | | | | | | | | | | | | | | | | | | |
| 28 | 8 | 20 | 36 | 12 | 32 | 24 | 16 | | | | | | | | | | | | | | | | | | | | | | | | |
| shift (x, -4, 0) | <table border="1"><tr><td>7</td><td>2</td><td>5</td><td>9</td></tr><tr><td>7</td><td>2</td><td>5</td><td>9</td></tr><tr><td>3</td><td>8</td><td>6</td><td>4</td></tr></table> | 7 | 2 | 5 | 9 | 7 | 2 | 5 | 9 | 3 | 8 | 6 | 4 | $\rightarrow a[4] * \text{shift}(x, -4, 0)$ 3 | <table border="1"><tr><td>21</td><td>6</td><td>15</td><td>27</td><td>9</td><td>24</td><td>18</td><td>12</td></tr><tr><td>21</td><td>6</td><td>15</td><td>27</td><td>9</td><td>24</td><td>18</td><td>12</td></tr></table> | 21 | 6 | 15 | 27 | 9 | 24 | 18 | 12 | 21 | 6 | 15 | 27 | 9 | 24 | 18 | 12 |
| 7 | 2 | 5 | 9 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 7 | 2 | 5 | 9 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 3 | 8 | 6 | 4 | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 21 | 6 | 15 | 27 | 9 | 24 | 18 | 12 | | | | | | | | | | | | | | | | | | | | | | | | |
| 21 | 6 | 15 | 27 | 9 | 24 | 18 | 12 | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | $y[0] = \begin{array}{cccccccc} \downarrow & \downarrow \\ + & + & + & + & + & + & + & + \end{array}$ $y[1] = \begin{array}{cccccccc} 147 & 42 & 105 & 189 & 63 & 168 & 126 & 84 \\ 137 & 54 & 109 & 179 & 73 & 170 & 120 & 86 \end{array}$ | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

```
using System;
using Microsoft.ParallelArrays;
namespace AcceleratorSamples
{
    public class Convolver2D
    {
        static FloatParallelArray convolve(Func<int, int[]> shifts, float[] kernel,
                                            int i, FloatParallelArray a)
        {

static FloatParallelArray convolveXY(float[] kernel,
                                      FloatParallelArray input)
{
    FloatParallelArray convolveX
        = convolve(i => new [] { -i, 0 }, kernel,
                    kernel.Length - 1, input);
    return convolve(i => new [] { 0, -i }, kernel,
                    kernel.Length - 1, convolveX);
}
return a + convolve(shifts, kernel, i - 1, a),
}

var inputArray = new ParallelArrayList(inputData);
var result = dx9Target.ToArray2D(convolveXY (testKernel, inputArray));
for (var row = 0; row < inputSize; row++)
{
    for (var col = 0; col < inputSize; col++)
        Console.Write("{0} ", result[row, col]);
    Console.WriteLine();
}
}
```

```
using System;
using System.Linq;
using Microsoft.ParallelArrays;
namespace AcceleratorSamples
{
    static FloatParallelArray convolve(this FloatParallelArray a,
                                         Func<int, int[]> shifts,
                                         float[] kernel)
    { return kernel
        .Select((k, i) => k * ParallelArrays.Shift(a, shifts(i)))
        .Aggregate((a1, a2) => a1 + a2);
    }

    static FloatParallelArray convolveXY(this FloatParallelArray input,
                                         float[] kernel)
    { return input
        .convolve(i => new[] { -i, 0 }, kernel)
        .convolve(i => new[] { 0, -i }, kernel);
    }

    for (int col = 0; col < inputSize; col++)
        Console.Write("{0} ", result[row, col]);
    Console.WriteLine();
}
}
```

```
FPA ConvolveXY(Target &tgt, int height, int width, int filterSize, float filter[], FPA input, float *resultArray)
{
    // Convolve in X (row) direction.
    size_t dims[] = {height, width};
    FPA smoothX = FPA(0, dims, 2);
    intptr_t counts[] = {0, 0};
    int filterHalf = filterSize/2;
    float scale;
    for (int i = -filterHalf; i <= filterHalf; i++)
    {
        counts[0] = i;
        scale = filter[i + filterHalf];
        smoothX += Shift(input, counts, 2) * scale;
    }

    // Convolve in Y (col) direction.
    counts[0] = 0;
    FPA result = FPA(0, dims, 2);
    for (int i = -filterHalf; i <= filterHalf; i++)
    {
        counts[1] = i;
        scale = filter[filterHalf + i];
        result += Shift(smoothX, counts, 2) * scale;
    }
    tgt.ToArray(result, resultArray, height, width, width * sizeof(float));
    return smoothX;
};
```

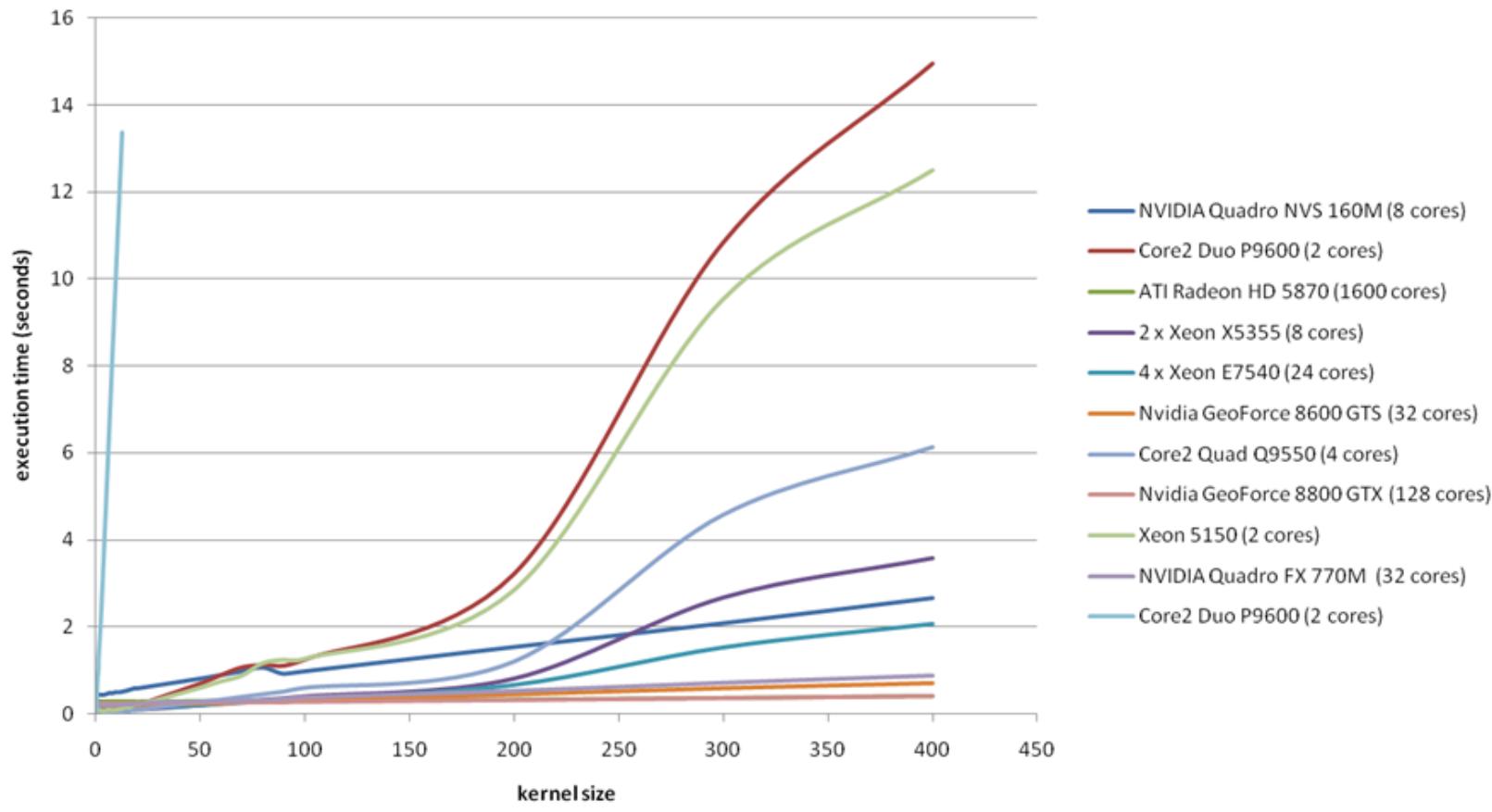
```

open System
open Microsoft.ParallelArrays
[<EntryPoint>]
let main(args) =
    // Declare a filter kernel for the convolution
    let testKernel = Array.map float32 [| 2; 5; 7; 4; 3 |]
    // Specify the size of each dimension of the input array
    let inputSize = 10
    // Create a pseudo-random number generator
    . . .

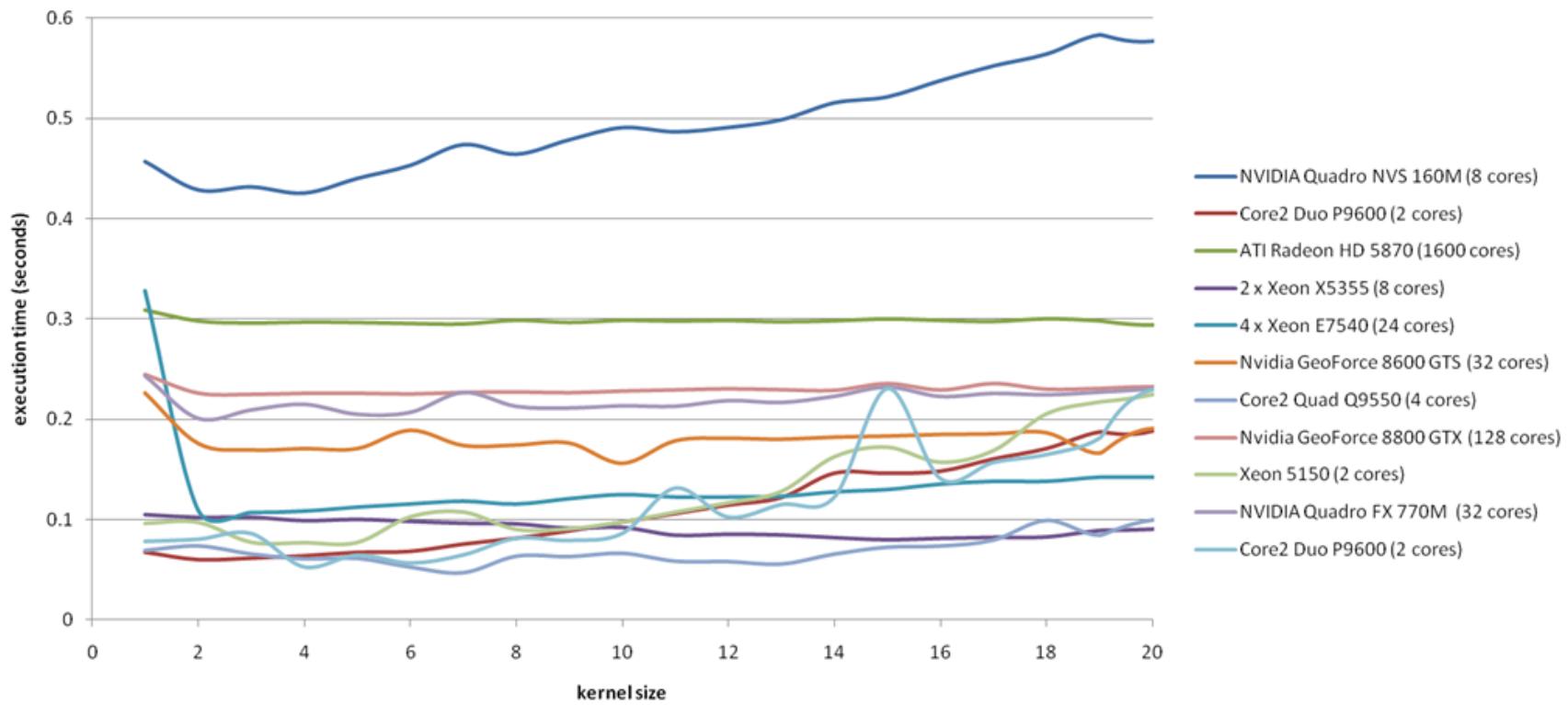
let convolveXY kernel input
= // First convolve in the X direction and then in Y
    let convolveX = convolve (fun i -> [| -i; 0 |]) kernel
                                (kernel.Length - 1) input
    let convolveY = convolve (fun i -> [| 0; -i |]) kernel
                                (kernel.Length - 1) convolveX
convolveY
    . . .
        e + convolve shifts kernel (i-1) a
// Declare a 2D convolver
let convolveXY kernel input
= // First convolve in the X direction and then in the Y direction
    let convolveX = convolve (fun i -> [| -i; 0 |]) kernel (kernel.Length - 1) input
    let convolveY = convolve (fun i -> [| 0; -i |]) kernel (kernel.Length - 1) convolveX
    convolveY
// Create a DX9 target and use it to convolve the test input
use dx9Target = new DX9Target()
let convolveDX9 = dx9Target.ToArray2D (convolveXY testKernel testArray)
printfn "DX9: -> \r\n%A" convolveDX9
0

```

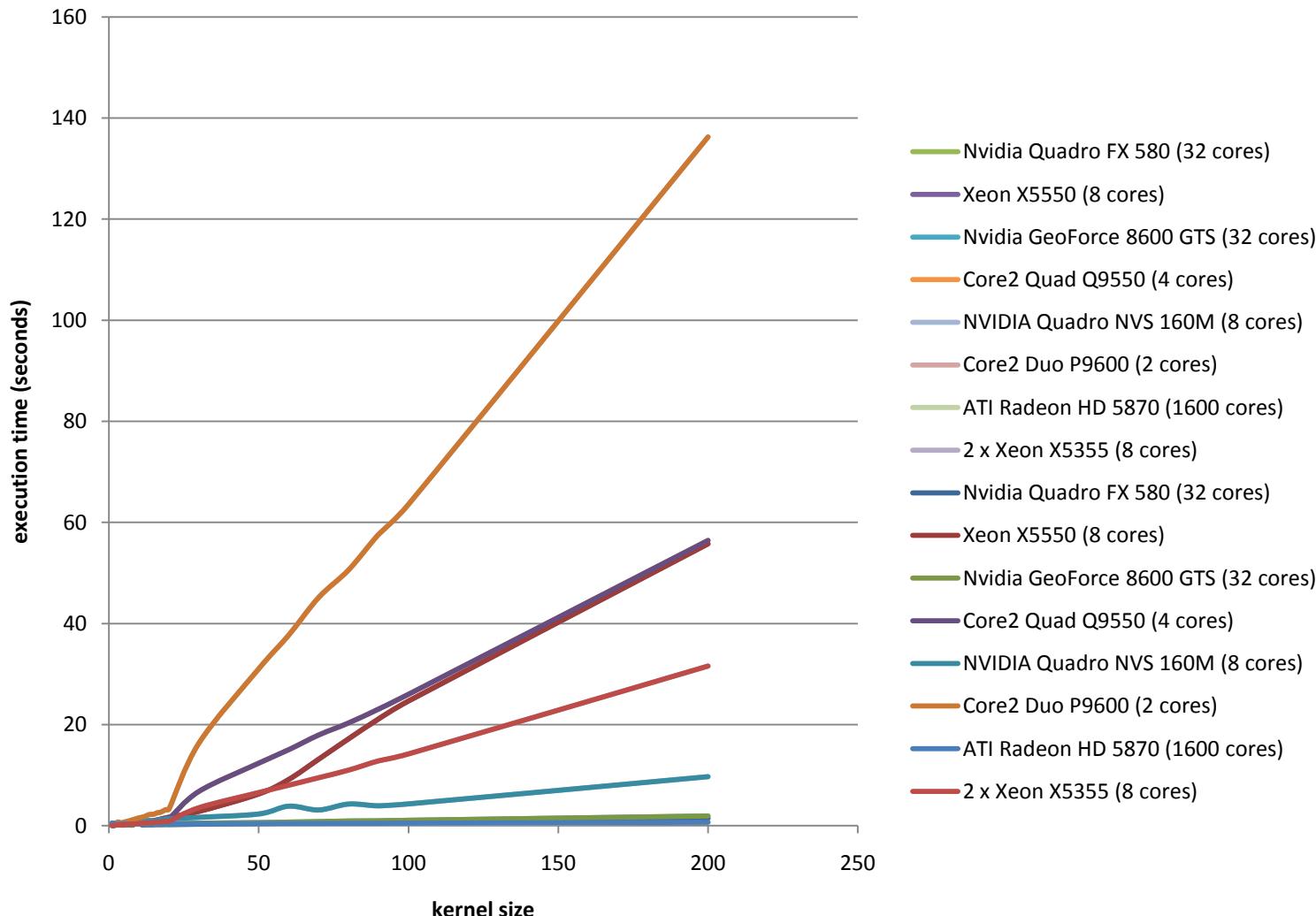
Convolver 1D 4000x4000 Benchmark



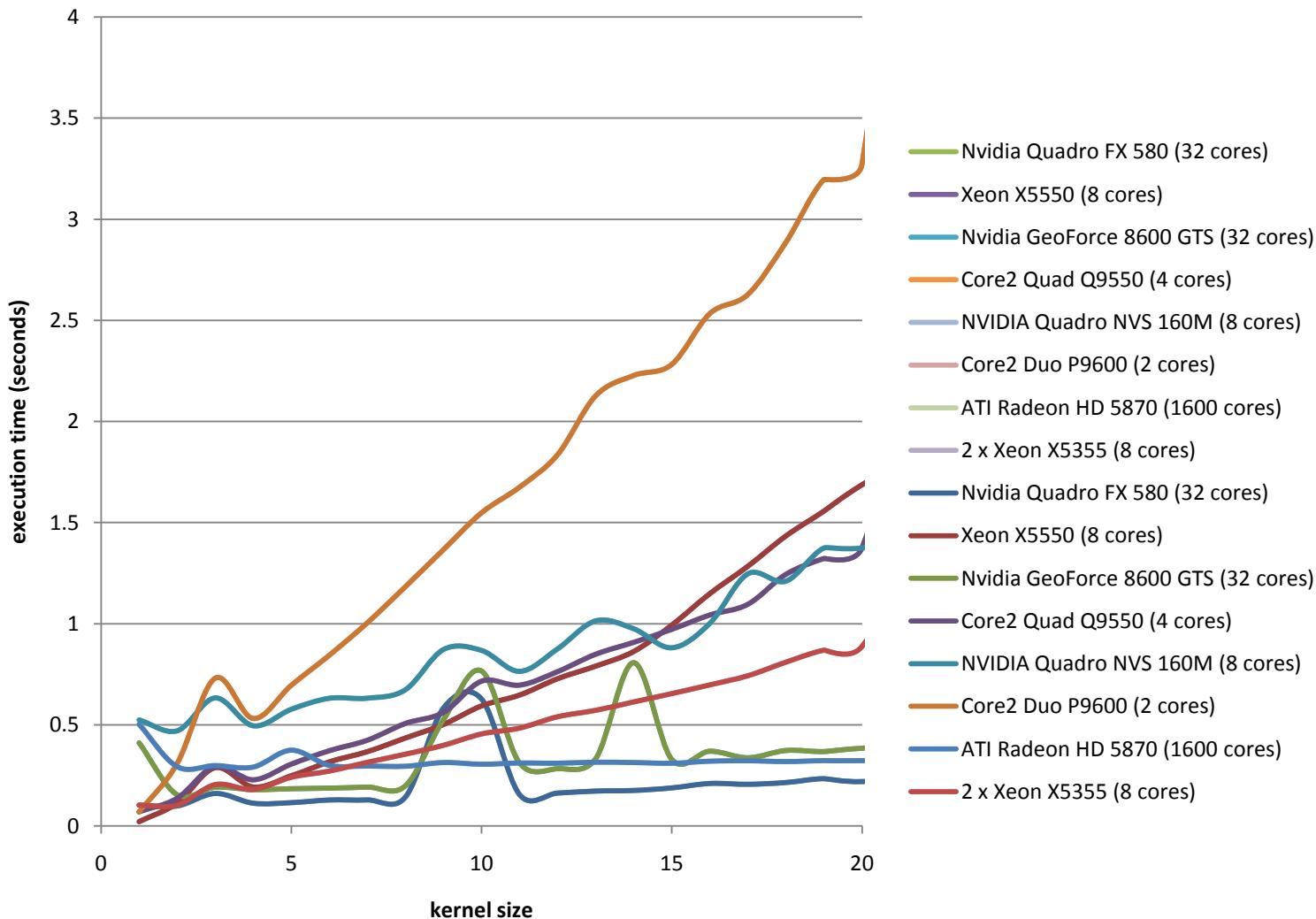
Convolver 1D 4000x4000 Benchmark



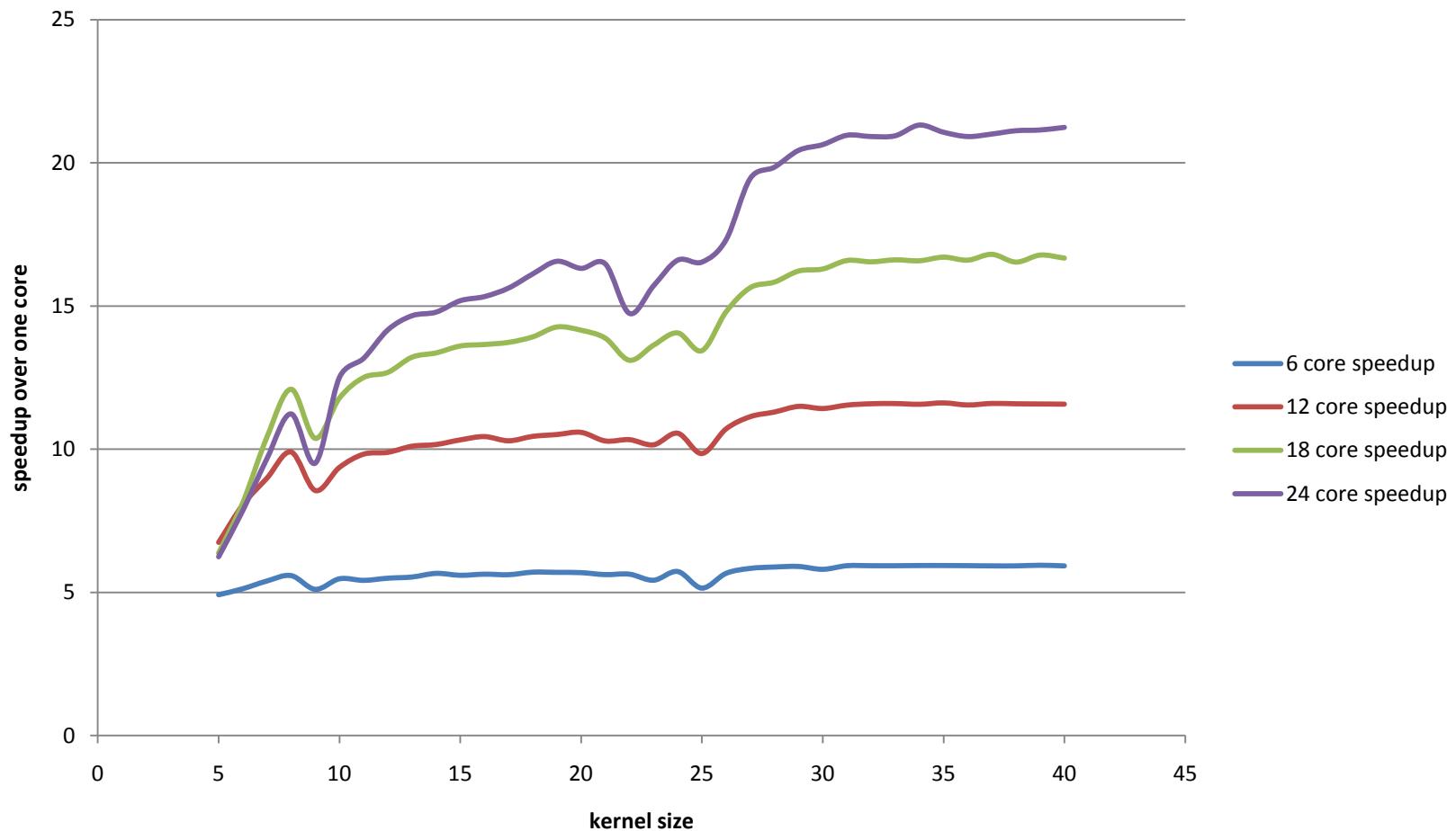
Convolver 2D 4000x4000 Benchmark

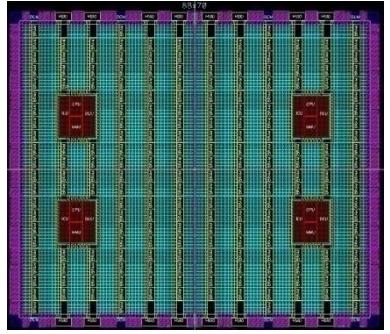


Convolver 2D 4000x4000 Benchmark



**x64 multicore target benchmark for 2D convolver
(24 core server Xeon E7540)**



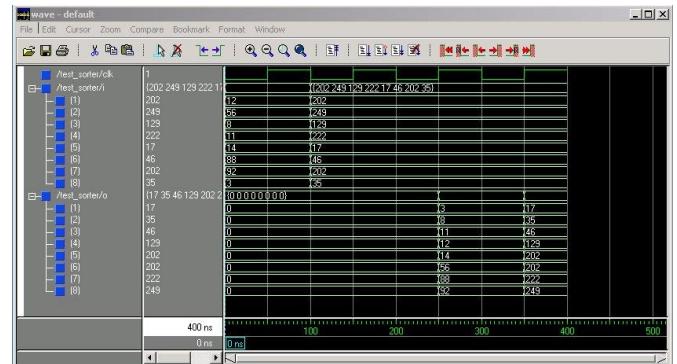


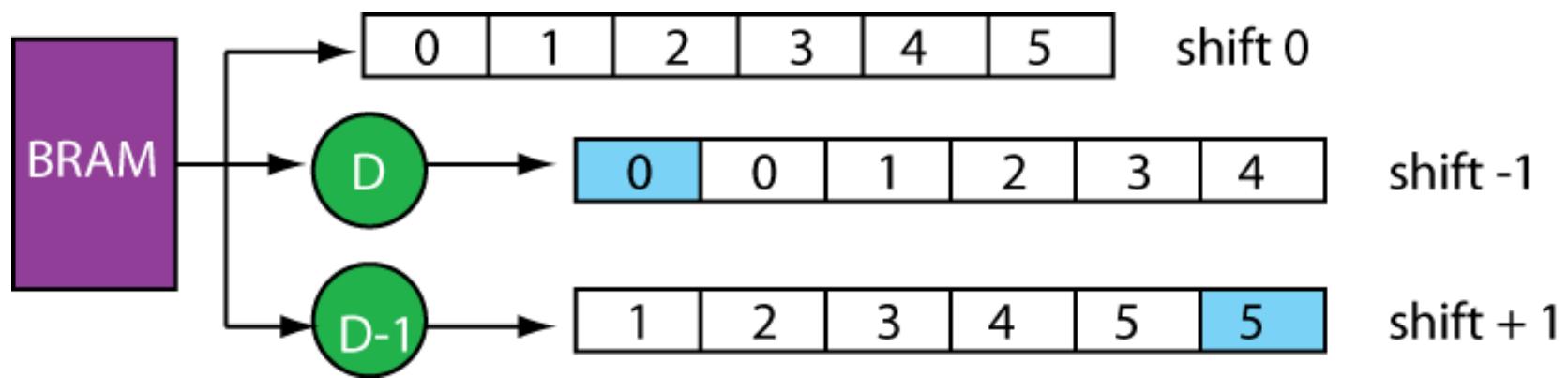
opportunity

Verilog

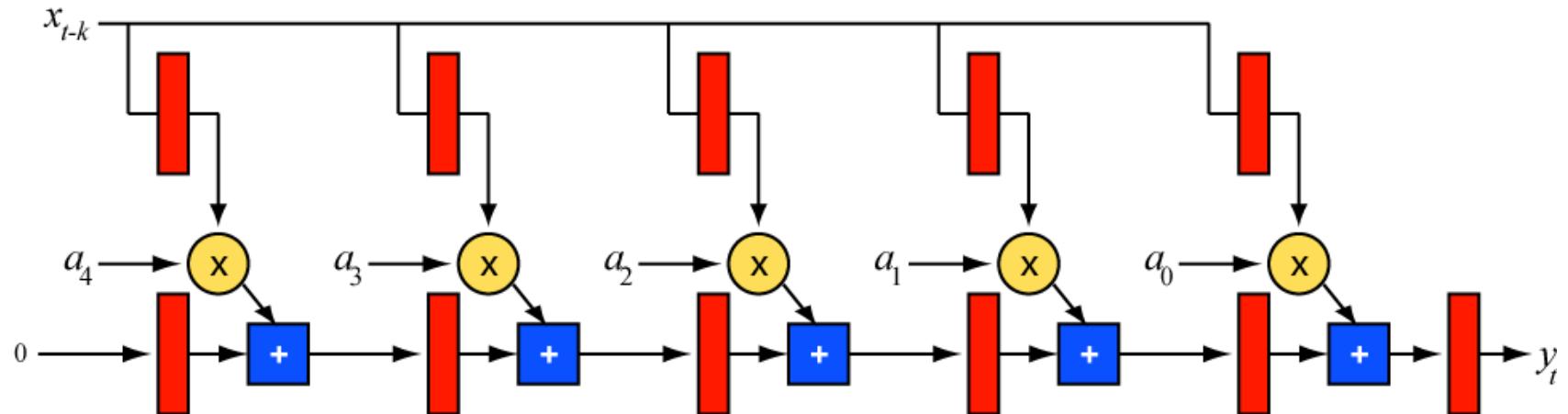
challenge

scientific computing
data mining
search
image processing
financial analytics

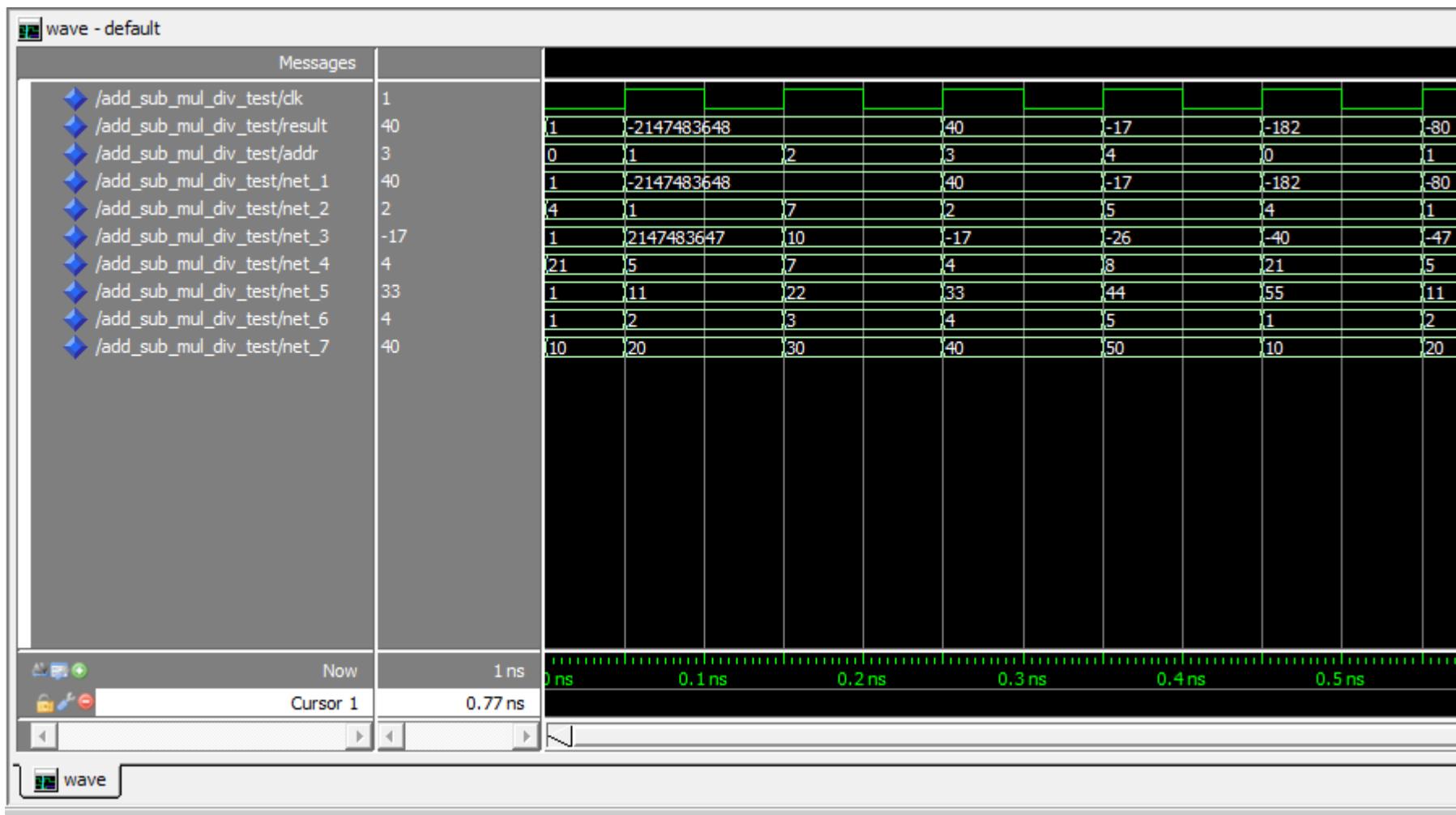


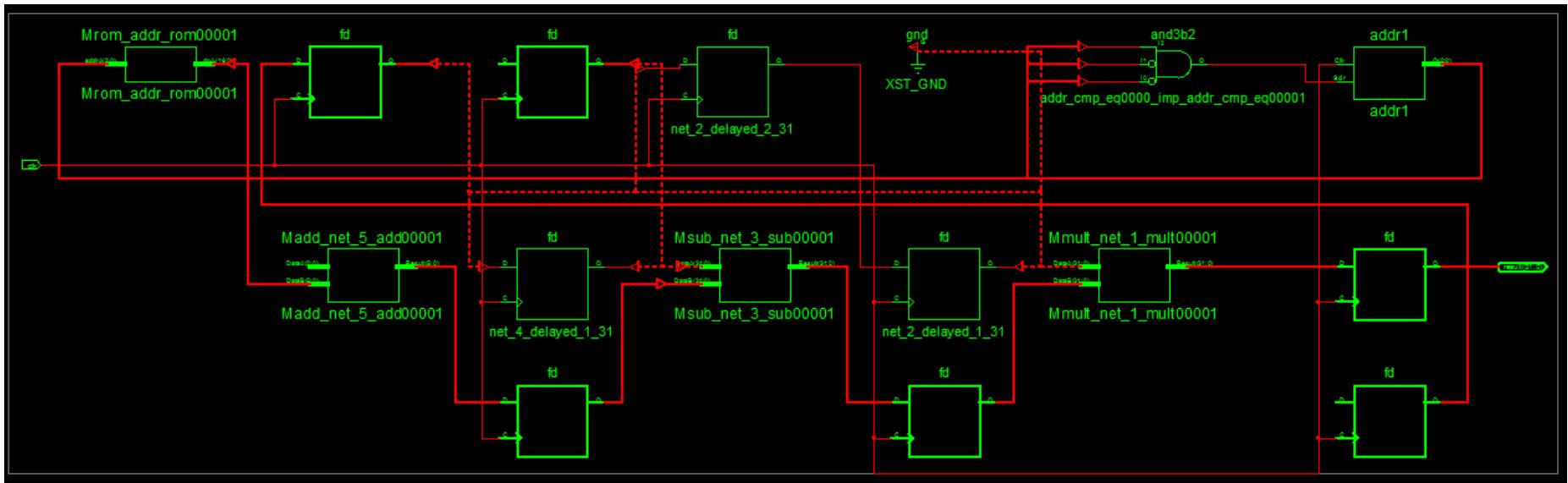


Convolver



$$y_t = \sum_{k=0}^{N-1} a_k x_{t-k}$$



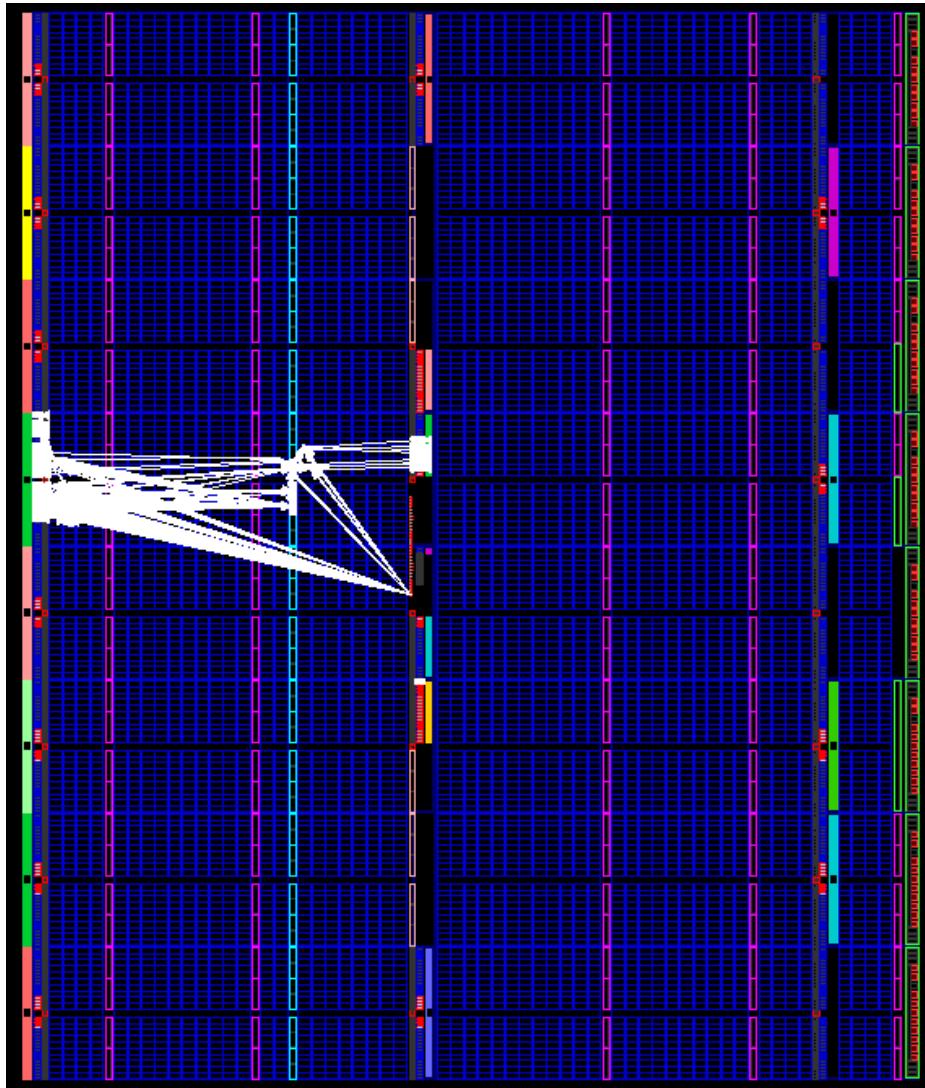


8.249ns max delay

3 x DSP48Es

63 slice registers

24 slice LUTs



ChipScope Pro Analyzer [convolver_chipscope]

File View JTAG Chain Device Trigger Setup Waveform Window Help

Project: convolver_chipscope

DEV:3 MyDevice3 (System_ACE_CF)
 DEV:4 MyDevice4 (XC5VLX50T)
 System Monitor Console
 UNIT:0 MyILA0 (ILA)
 Trigger Setup
 Waveform
 Listing

Signals: DEV: 4 UNIT: 0

- Data Port
 - /net_53
 - /net_66_add0000
 - /Result
- Trigger Ports
 - TriggerPort0

Trigger Setup - DEV:4 MyDevice4 (XC5VLX50T) UNIT:0 MyILA0 (ILA)

| Match Unit | Function | Value | Radix | Counter | |
|-----------------|----------|-------|-------|---------|----------|
| M0:TriggerPort0 | $=$ | | F | Bin | disabled |

Add Active Trigger Condition Name Trigger Condition Equation
 M0

Type: Window Windows: 1 Depth: 1024 Position: 0

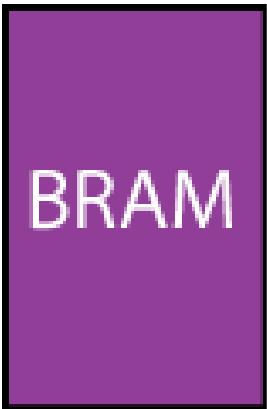
Storage Qualification: All Data

Waveform - DEV:4 MyDevice4 (XC5VLX50T) UNIT:0 MyILA0 (ILA)

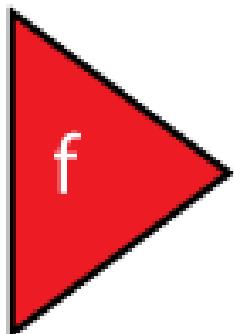
| Bus/Signal | X | O | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 |
|------------|------|------|---|------|---|------|------|----|-----|-----|-----|-----|------|-----|-----|-----|----|----|
| /net_53 | 6318 | 6318 | | 6318 | | 3952 | 1846 | 91 | 263 | 506 | 749 | 992 | 1144 | 942 | 699 | 668 | | |

COMMAND: set_match_function 4 0 0 0 3 1 F
 COMMAND: set_trigger_condition 4 0 3 1 5555
 COMMAND: set_storage_condition 4 0 FFFF
 COMMAND: run 4 0
 COMMAND: upload 4 0
 INFO - Device 4 Unit 0: Waiting for core to be armed

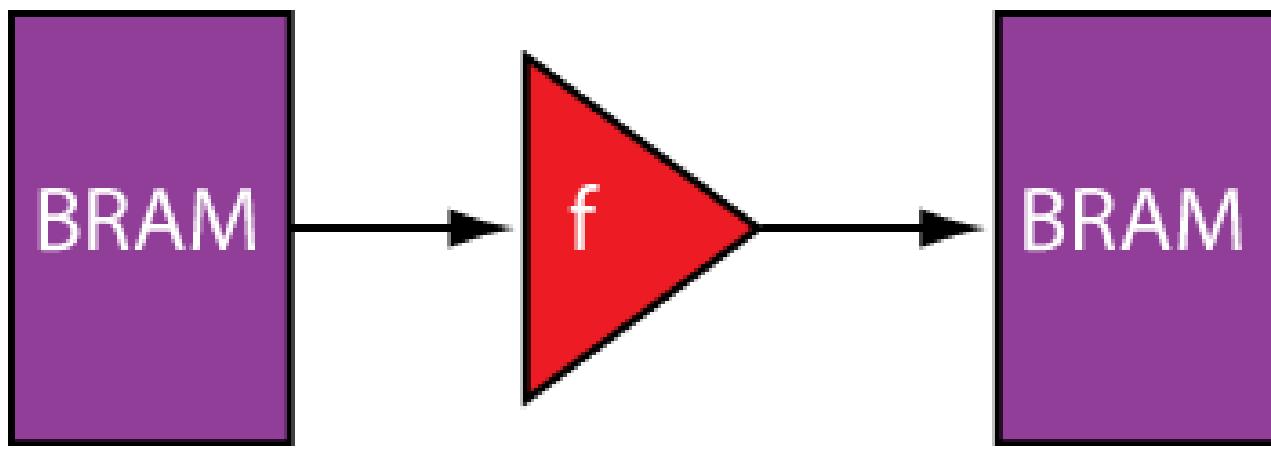
Upload DONE

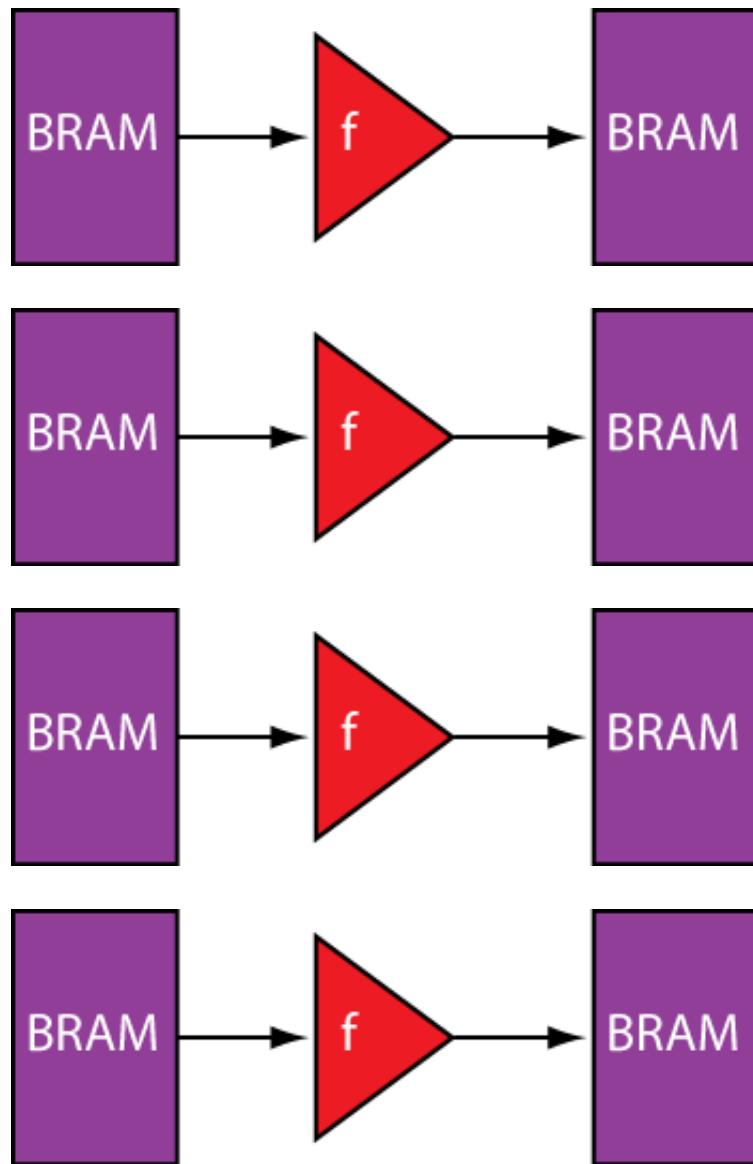


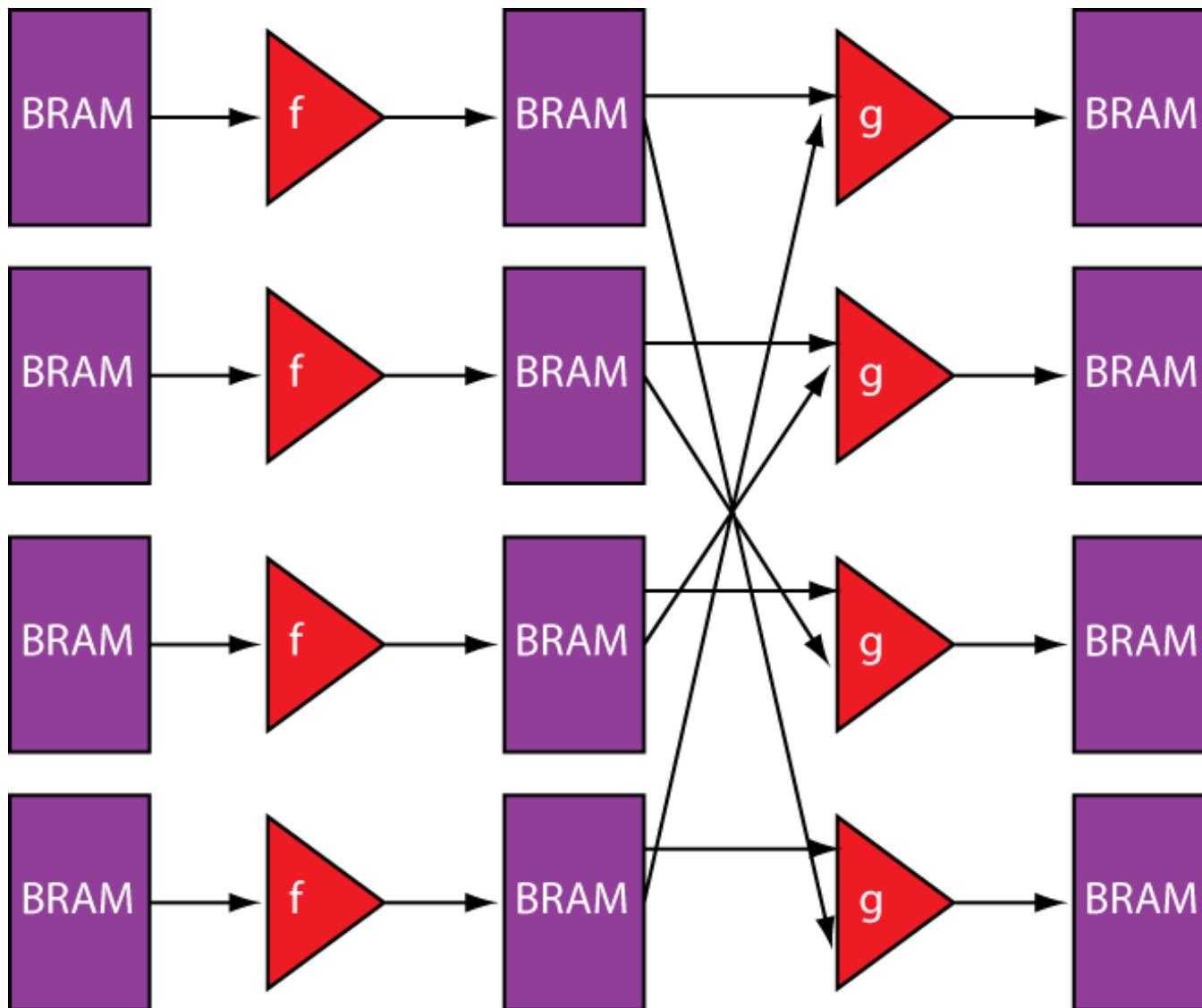
36Kbits
38,304
dual-ported

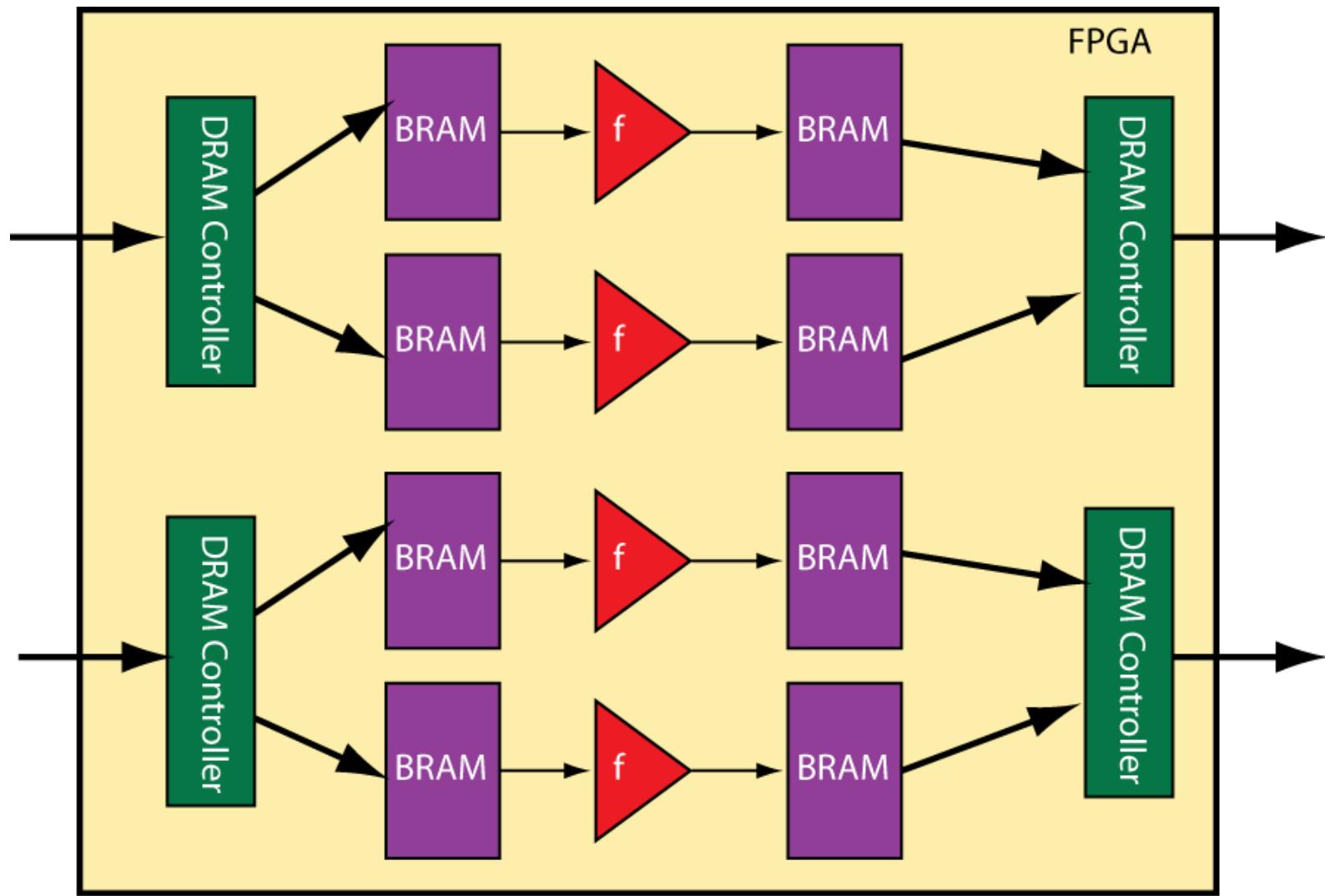


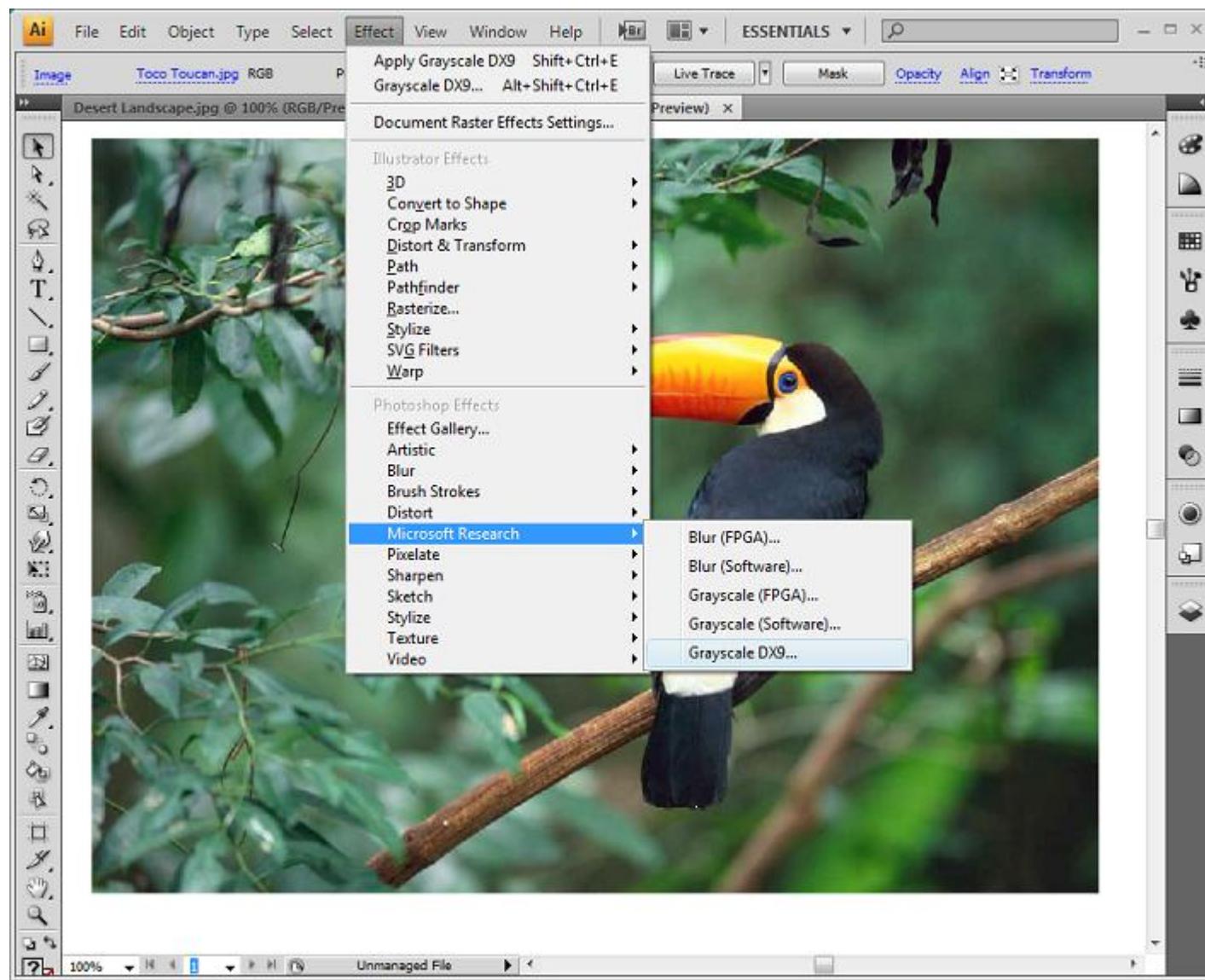
FPGA basic logic (LUTS)
DSP blocks (current max 2016)

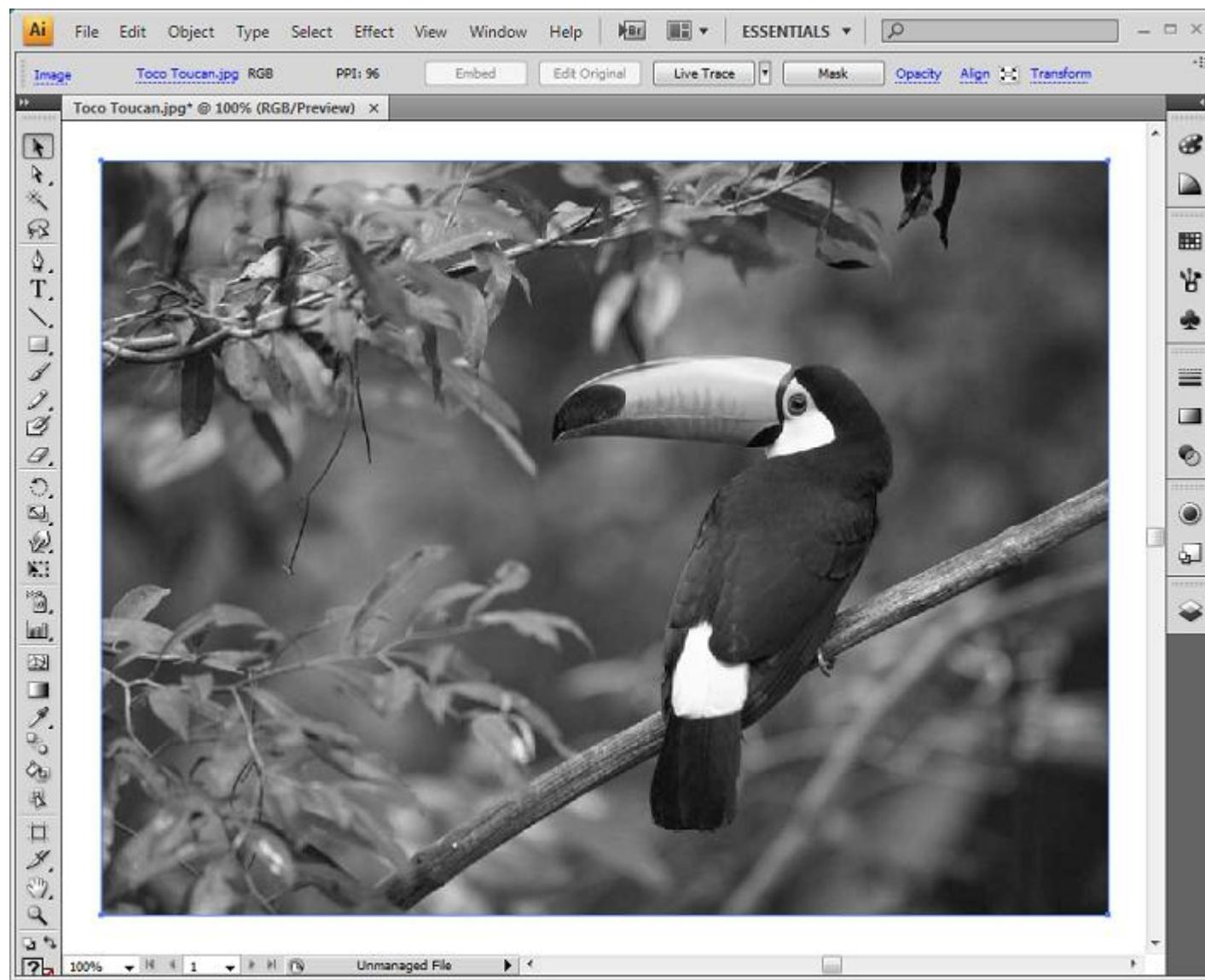












```

// Compute grayscale
Target &tgt = CreateDX9Target();
float* grayF = (float*) malloc(sizeof(float) * pixels) ;
FPA red = FPA(redF, rectHeight, rectWidth) ;
FPA green = FPA(greenF, rectHeight, rectWidth);
FPA blue = FPA(blueF, rectHeight, rectWidth);
FPA sum = Add (77 * red, Add (151 * green, 28 * blue)) ;
FPA gray = Divide (sum, 256) ;
tgt.ToArray(gray, grayF, rectHeight, rectWidth, rectWidth * sizeof(float));
// Update Photoshop image buffer
pixel = (uint8*)data;
for(int32 pixelY = 0; pixelY < rectHeight; pixelY++)
{
    for(int32 pixelX = 0; pixelX < rectWidth; pixelX++)
    {
        uint8 gray = (uint8) grayF[pixelX+pixelY*rectWidth] ;
        pixel[0] = (uint8)gray ;
        pixel[1] = (uint8)gray ;
        pixel[2] = (uint8)gray ;
        pixel = pixel + 3 ;
        bigPixel++;
        fPixel++;
        dissolve++;
        if (maskPixel != NULL)
            maskPixel++;
    }
    pixel += (dataRowBytes - 3*rectWidth);
    bigPixel += (dataRowBytes / 2 - 3*rectWidth);
    fPixel += (dataRowBytes / 4 - 3*rectWidth);
    if (maskPixel != NULL)
        maskPixel += (maskRowBytes - rectWidth);
}

```

CUDA

```
//Compute and store results
__syncthreads();
#pragma unroll
for(int i = ROWS_HALO_STEPS;
    i < ROWS_HALO_STEPS + ROWS_RESULT_STEPS; i++) {
    float sum = 0;

    #pragma unroll
    for(int j = -KERNEL_RADIUS; j <= KERNEL_RADIUS; j++)
        sum += c_Kernel[KERNEL_RADIUS - j] *
s_Data[threadIdx.y][threadIdx.x + i * ROWS_BLOCKDIM_X + j];

    d_Dst[i * ROWS_BLOCKDIM_X] = sum;
}
```



Satnam Singh's MSDN Blog : GPGPU and x64 Multicore Programming with Accelerator from F# - Windows Internet Explorer

http://blogs.msdn.com/satnam_singh/archive/2009/12/15/gpgpu-and-x64-multicore-programming-with-accelerator-from- DLL utilities

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Satnam Singh's MSDN Blog

GPGPU and x64 Multicore Programming with Accelerator from F# ★★★★★

Microsoft recently released a preview of the [Accelerator V2](#) GPU and x64 multicore programming system on Microsoft Connect. This system provides a civilized level of abstraction for writing data-parallel programs that execute on GPUs and multicore processors. An experimental FPGA target is under development.

Even on my low end graphics card I get pretty impressive performance results for the 2D convolver that is described in this blog. All 8 cores of my 64-bit Windows 7 workstation are also effectively exercised by the x64 multicore target, which exploits SIMD processor instructions and multithreading. I won't say anything about performance in this blog post since what I want to focus on is how to use Accelerator from the [F#](#) functional programming language. We will work backwards by starting off with a complete implementation of a two dimensional convolver. Step by step we show how this convolver is expressed using Accelerator from F#.

First here is the beautiful implementation of a two dimensional convolver. The rest of this post explains why this code works.

```
open System
open Microsoft.ParallelArrays
[<EntryPoint>]
let main(args) =
    // Declare a filter kernel for the convolution
    let testkernel = Array.map float32 [| 2; 5; 7; 4; 3 |]

    // Specify the size of each dimension of the input array
    let inputSize = 10

    // Create a pseudo-random number generator
    let random = Random (42)

    // Declare a psuedo-input data array
    let testData = Array2D.init inputSize inputSize (fun i j -> float32 (random.NextDouble()) * float (random.Next(1, 100)))

    // Create an Accelerator float parallel array for the F# input array
    use testarray = new FloatParallelArray(testData)

    // Declare a function to convolve in the X or Y direction
    let rec convolve (shifts : int -> int []) (kernel : float32 []) i (a : FloatParallelArray)
        = let e = kernel.[i] * ParallelArrays.Shift(a, shifts i)
          if i = 0 then
            e
          else
            e + convolve shifts kernel (i-1) a

    // Declare a 2D convolver
    let convolveXY kernel input
        = // First convolve in the X direction and then in the Y direction
          let convolveX = convolve (fun i -> [| -1; 0 |]) kernel (kernel.Length - 1) input
          let convolveY = convolve (fun i -> [| 0; -1 |]) kernel (kernel.Length - 1) convolveX
          convolveY
```

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Done Internet | Protected Mode: On 100%

Search for “Microsoft Accelerator V2”

The screenshot shows a Windows Internet Explorer window displaying the Microsoft Connect website. The URL in the address bar is <https://connect.microsoft.com/acceleratorv2?wa=wsignin1.0>. The page content is about the MSR Accelerator v2 preview build, highlighting its availability for download and detailing its features. The sidebar on the left includes links for MSR Accelerator v2 Home, Downloads, Feedback, and Newsgroups.

MSR Accelerator v2

The MSR Accelerator v2 preview build is available for download.

What is Accelerator?

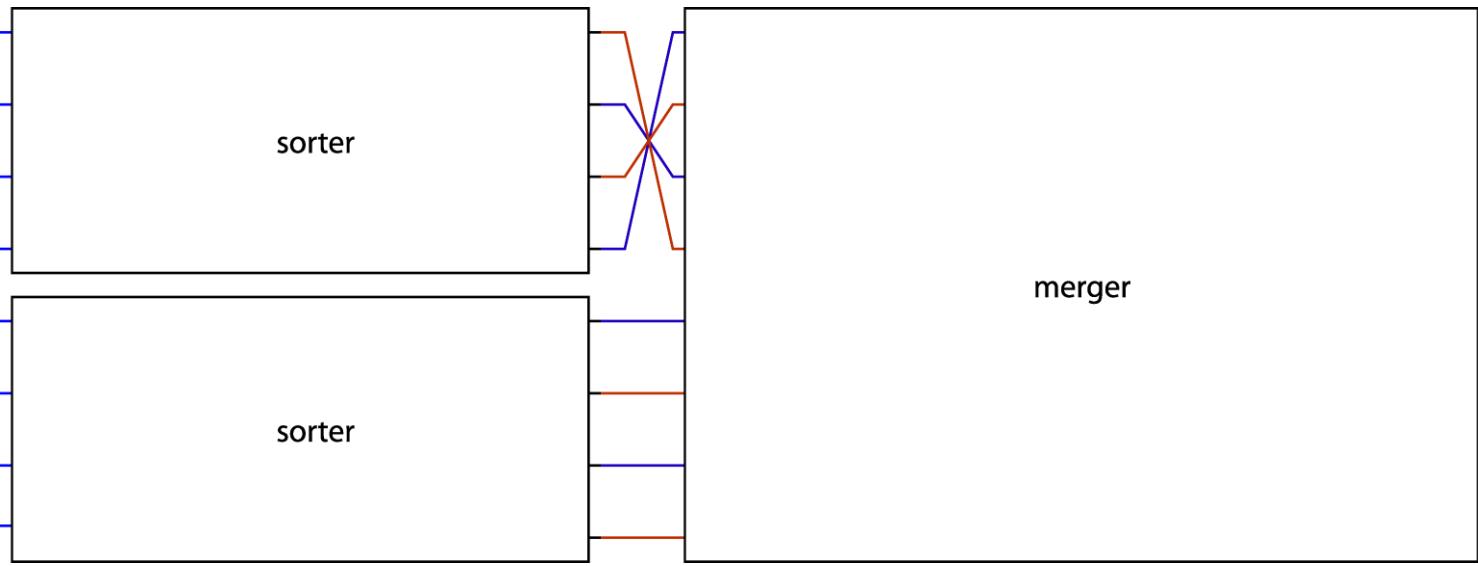
Accelerator is a high-level data parallel array-processing library which uses the parallel processing capabilities of a GPU or multi-core CPU to accelerate execution. Accelerator v1 was released to the MSR Web site in October 2006 and has been periodically updated since then. Accelerator v2 is an MSR incubation project whose goal is to validate the architecture and API approach with high quality engineering sufficient to gather real-world usage data.

What's in Accelerator v2?

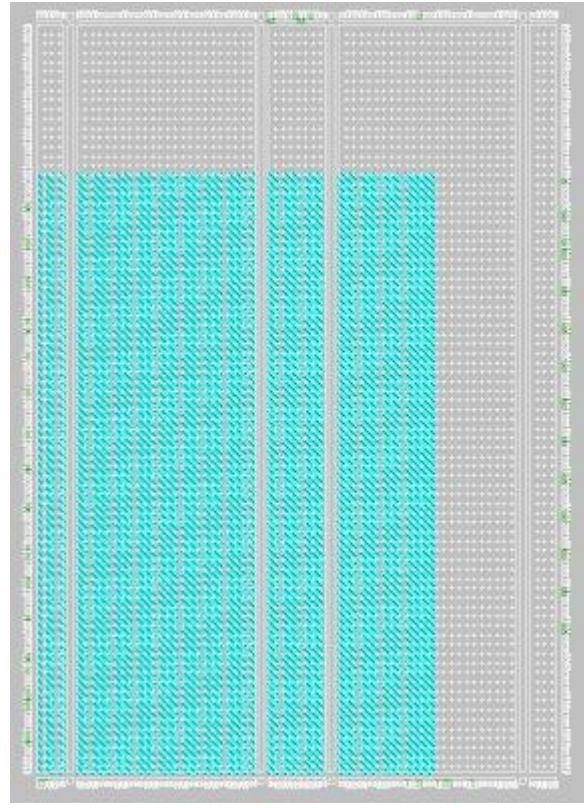
Accelerator v2 builds on Accelerator v1's programming model and adds features that were commonly requested by Accelerator v1 users. New functionality includes:

- Accelerator v2 is written as a native-code C++ library with a managed API wrapper
- Execution on x64 multicore CPUs and DX9 GPUs
- Extensible HW target interface enabling support for execution on new devices
- Ability to execute on multiple devices within a single Accelerator instance

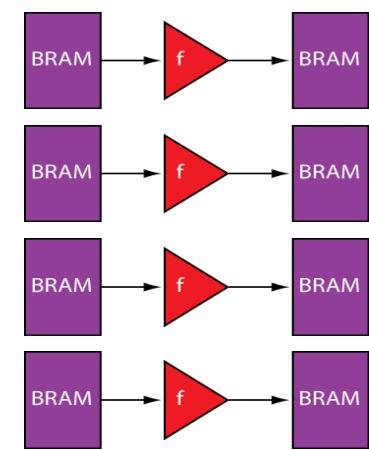
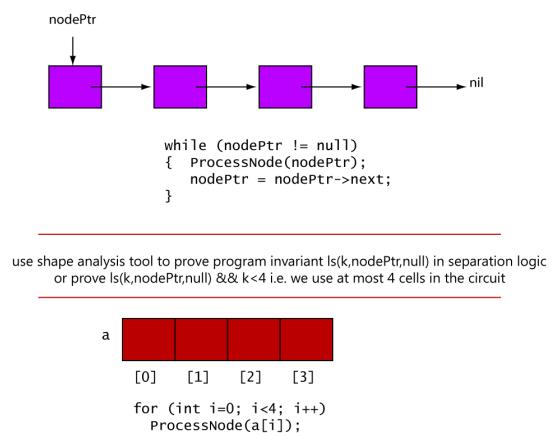
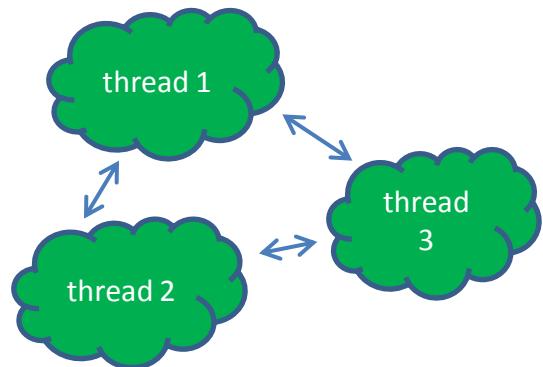
If you have a question or feedback about the MSR Accelerator v2 preview build, please contact us via one of the



```
let rec bsort n =
  match n with
  | 1 -> sort2
  | n -> two (bsort (n-1)) >-> sndList rev >-> bfly sort2 n
```

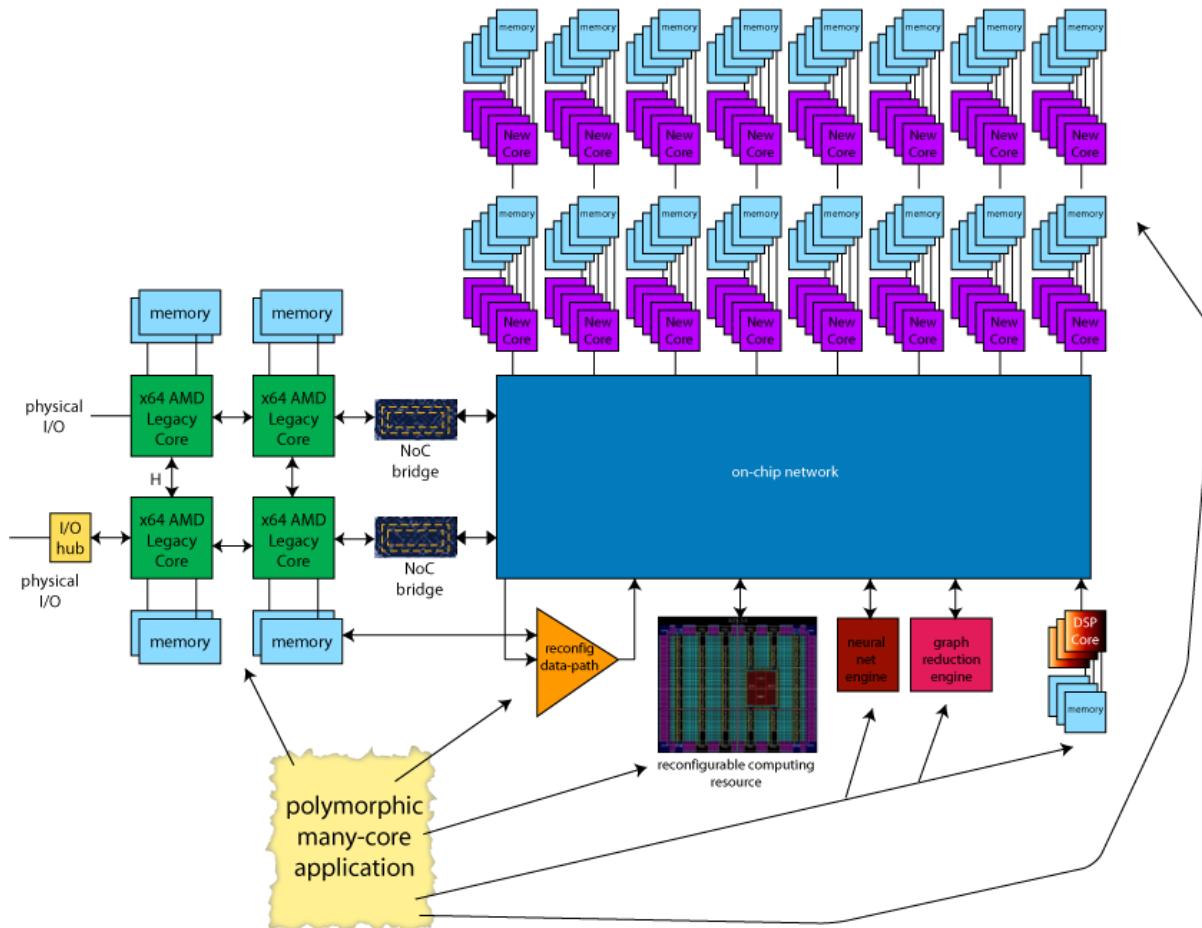








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Satnam Singh



SENIOR RESEARCHER

I currently work on two research topics:

- **[Alchemy: Transumtation of Programs into Circuits.](#)**

- The compilation of data-parallel programs written in C++ and .NET languages like C# into FPGA circuits.
- The [Kiwi](#) project with David Greaves which synthesizes circuits from high level circuit models written using regular multi-threaded code in languages like C#.
- Synthesis of C programs that manipulate dynamic data structures in the heap.
- Synthesis of data-parallel programs in C++ and C# written with using the Microsoft [Accelerator](#) V2 library into FPGAs for co-processing.
- High level techniques for designing low level circuits. I have re-implemented my [Lava](#) system in C# and F# and I have made HLINQ which is a circuit generator for LINQ queries.
- Evaluation and experimentation with alternative hardware description

Some Recent Talks

- C-to-Gates Synthesis of Dynamic Data Structures. Practical Synthesis for Concurrent Systems (PSY) 2009, Grenoble (CAV workshop). 28 June 2009.
- Three Approaches to High Level Synthesis. IBM Zurich Research Lab. 17 June 2009.
- GPU Programming with Microsoft Accelerator. Oxford University. 5 June 2009.
- Pointer Synthesis++. UK Design Forum '09. 8 May 2009.
- An Overview of Parallel and Concurrent Programming in Haskell. University of Leeds. 30 April 2009.
- Kiwi: Synthesis of FPGA Circuits from