Inconsistencies in Specification of Intel TDX Remote Attestation

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Need of science of security in an emerging and important domain

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- CCC: more marketing than scientific^{1,2} (highlights only)

 $^{^{1} \}hbox{Confidential Computing Consortium, W hitepaper feedback from Muhammad Usama Sardar, Issue $\#77$, 2020}$

²Sardar and Fetzer, Confidential Computing and Related Technologies : A Review, 2021 (🗇 🕨 4 🚊 🕨 4 🚊 💉 🔾 🤄

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- Complexity is the best friend of Intel!

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Outline

- Introduction
- 2 Formal Security Analysis Approach
- 3 TDX
 - Discrepancies Identified
 - Formal Specification
 - Automated Verification
- 4 Summary

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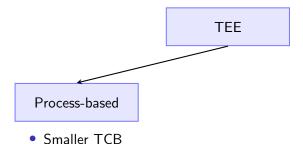
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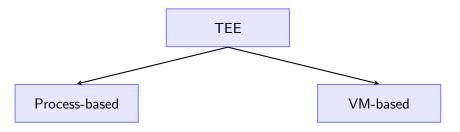
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- "Any attack that could compromise the attestation of a TEE instance could lead to a workload or data being compromised in turn."

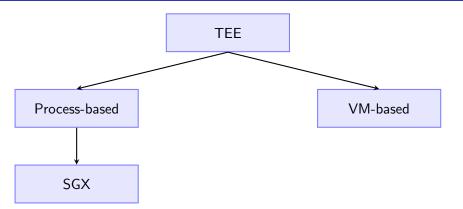
³Confidential Computing Consortium, A Technical Analysis of Confidential Computing, v1.1, 2021

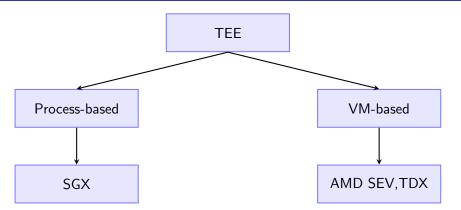
⁴Confidential Computing Consortium, A Technical Analysis of Confidential Computing, v1.15/2021 🚊 🕟 🧸 🗦 🔻 🔗 🔍

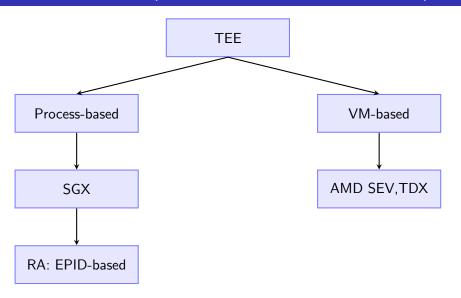


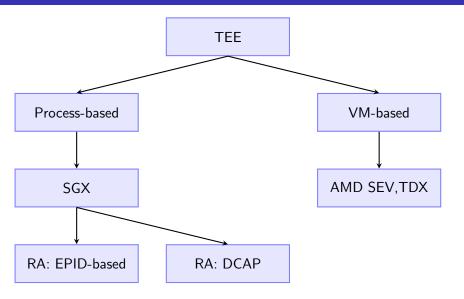


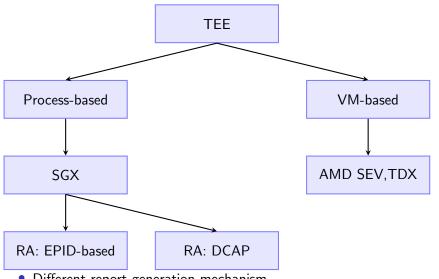
• Ease of use











- Different report generation mechanism
- Runtime TD measurements

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More automation vs. user interaction

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- Faster⁶

 $^{^5}$ Blanchet, "CryptoVerif: A computationally-sound security protocol verifier", 2017

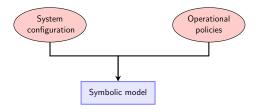
⁶Lafourcade and Puys, "Performance Evaluations of Cryptographic Protocols Verification Tools Dealing with Algebraic Properties", 2016

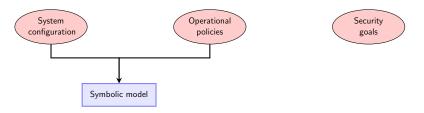
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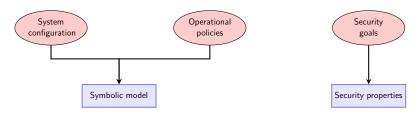


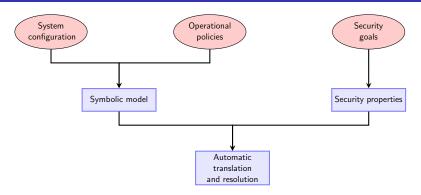


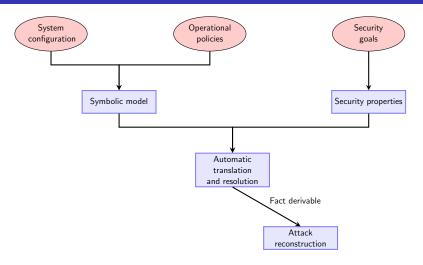
Operational policies

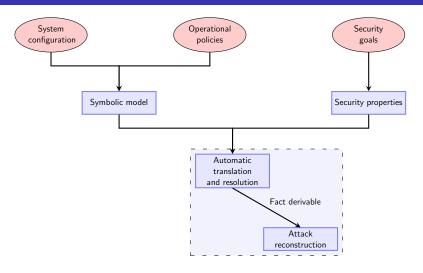




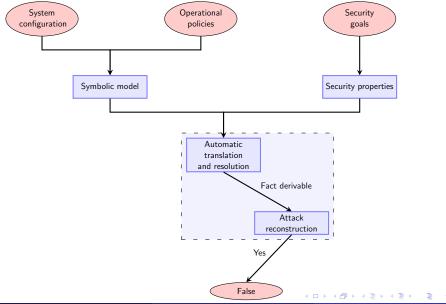




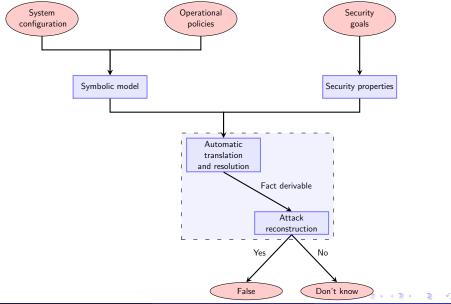




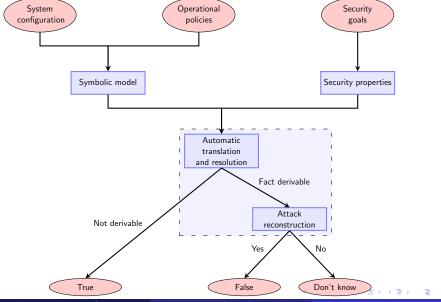
Workflow of the Analysis Approach



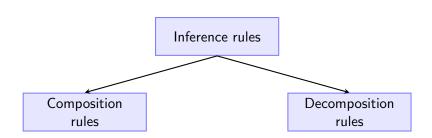
Workflow of the Analysis Approach



Workflow of the Analysis Approach



Inference System



Composition rules

- Composition rules
 - pair $\frac{x}{\langle x,y \rangle}$ att $(x) \bigwedge att(y) \to att(\langle x,y \rangle)$

- Composition rules
 - pair $\frac{x}{\langle x, y \rangle}$ $att(x) \wedge att(y) \rightarrow att(\langle x, y \rangle)$ hash $\frac{m}{h(m)}$ $att(m) \rightarrow att(h(m))$

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Contributions

• Identification of discrepancies including inconsistent information

⁷Blanchet et al., "Modeling and verifying security protocols with the applied pi calculus and ProVerif", 2016

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- Precise specification of TD attestation protocol in ProVerif⁷

11 / 25

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- Precise specification of TD attestation protocol in ProVerif⁷
- Automated verification of confidentiality and authentication properties in ProVerif

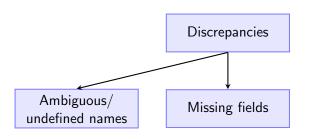
Discrepancies Identified

Ambiguous/
undefined names

• SEAMINFO vs. TEE_TCB_INFO (e.g., p.2-8)⁸

⁸Intel, Intel (R) Trust Domain CPU Architectural Extensions, 2020

Discrepancies Identified

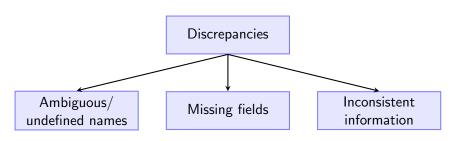


MROWNERCONFIG missing in TDINFO (Fig. 10.1, p.85)⁹

⁸Intel, Intel ® Trust Domain CPU Architectural Extensions, 2020

⁹ Intel, Architecture Specification: Intel® Trust Domain Extensions (Intel® TDX):Module, 2020 € → ⟨ € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | € → | €

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⁸Intel, Intel ® Trust Domain CPU Architectural Extensions, 2020

Inconsistent Information: Example 1^{10}

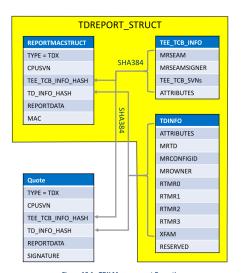


Figure 10.1: TDX Measurement Reporting

¹⁰ Intel, Architecture Specification: Intel® Trust Domain Extensions (Intel® TDX):Module;2020 🚊 🛌 🗦 🔻 💆 🥠 🔍

Inconsistent Information: Example 1¹¹

 $tmp_seamreport.REPORTMACSTRUCT.TEE_TCB_INFO_HASH = SHA384(tmp_seamreport.TEE_TCB_INFO);$

Table 2-3. TEE_TCB_INFO Structure

Name	Offset (Bytes)	Size (Bytes)	Description
VALID	0	8	Indicates TEE_TCB_INFO fields which are valid. 1 in the i-th significant bit reflects that the 8 bytes starting at offset (8 * i) are valid. 0 in the i-th significant bit reflects that either 8 bytes starting at offset (8 * i) is not populated or reserved, and is set to zero.
TEE_TCB_SVN	8	16	TEE_TCB_SVN array.
MRSEAM	24	48	Measurement of the Intel TDX module.
MRSIGNERSEAM	72	48	Measurement of TDX module signer if valid.
ATTRIBUTES	120	8	Additional configuration ATTRIBUTES if valid.
RESERVED	128	111	Must be zero.

¹¹Intel, Intel (R) Trust Domain CPU Architectural Extensions, 2020

Inconsistent Information: Example 2¹²

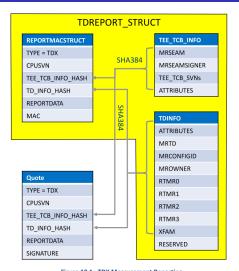


Figure 10.1: TDX Measurement Reporting

RESERVED is not a part of hash!

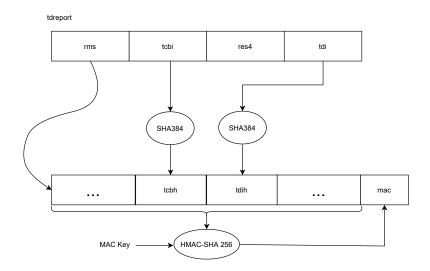
12 Intel, Architecture Specification: Intel® Trust Domain Extensions (Intel® TDX):Module, 2020 📱 🛌 📳 💈 🥙 🤇

Inconsistent Information: Example 2¹³

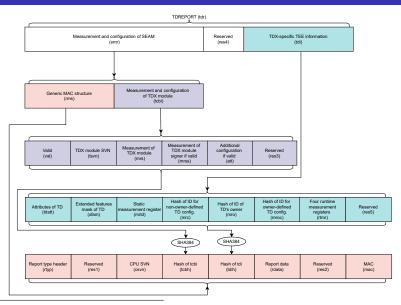
Software verifying a TEE report structure (for TDX, this includes TEE_TCB_INFO_STRUCT and TDINFO_STRUCT) should first confirm that its REPORTMACSTRUCT.TEE_TCB_INFO_HASH equals the hash of the TEE_TCB_INFO_STRUCT (if applicable) and that REPORTMACSTRUCT.TEE_INFO_HASH equals the hash of the TDINFO_STRUCT. Then, software uses

¹³ Intel, Architecture Specification: Intel® Trust Domain Extensions (Intel® TDX): Module 2020 📱 🕟 🔻 📱 🗸 🔊 🔍

TD Report Structures (Simplified view)



TD Report Structures¹⁴

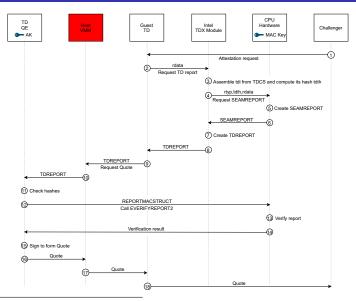


¹⁴ Sardar, Musaev, and Fetzer, "Demystifying Attestation in Intel Trust Domain Extensions via Formal Verification", 2021 🔈 🤉 🖎

Simplified View of Protocol

- Local attestation \rightarrow Symmetric crypto \rightarrow MAC
- ullet Remote attestation o Asymmetric crypto o Digital signatures

TDX Attestation Flow for Quote Generation 15



¹⁵Sardar, Musaev, and Fetzer, "Demystifying Attestation in Intel Trust Domain Extensions via Formal Verification", 2021 🔈 🤉 🕒

Automated Verification

- Validation: reachability of all parts of code
- Confidentiality: reachability property
- Authentication properties, e.g.,
 x ≡ ⟨rtyp, res1, csvn, tcbh, tdih, rdata, res2⟩

```
\forall x.

\exists mac, tcbi.

event(QuoteVerified(x)) \Rightarrow event(CPUsentSMR(x, mac, tcbi))
```

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 - Equivalence properties

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 - Mutual authentication
 - Freshness
 - Equivalence properties
 - Tamarin for comparison

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 - may lead to design and implementation flaws
- Reported to Intel and being updated by Intel
- Works in progress (comments most welcome: also by email)
 - Model: PCE and cert chain, verifier end
 - Properties:
 - Mutual authentication
 - Freshness
 - Equivalence properties
 - Tamarin for comparison
- Shameless plug: we are hiring PhDs, post-docs ([muhammad_usama.sardar,christof.fetzer]@tu-dresden.de)

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