

# *Automatic theorem proving and SMT*

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Microsoft Research  
HCSS, May 8, 2013

# Outline

- Symbolic methods and SMT
- Z3 – what's new?
- Validating Network Access Restrictions
- Satisfiability of Horn Clauses

# Symbolic methods and SMT

# Satisfiability Modulo Theories (SMT)

**Is formula  $\varphi$  satisfiable  
modulo theory  $T$  ?**

SMT solvers have  
specialized algorithms for  $T$



# Satisfiability Modulo Theories (SMT)

$x+2=y \Rightarrow f(\text{select}(\text{store}(a,x,3),y-2))=f(y-x+1)$

Array Theory

Arithmetic

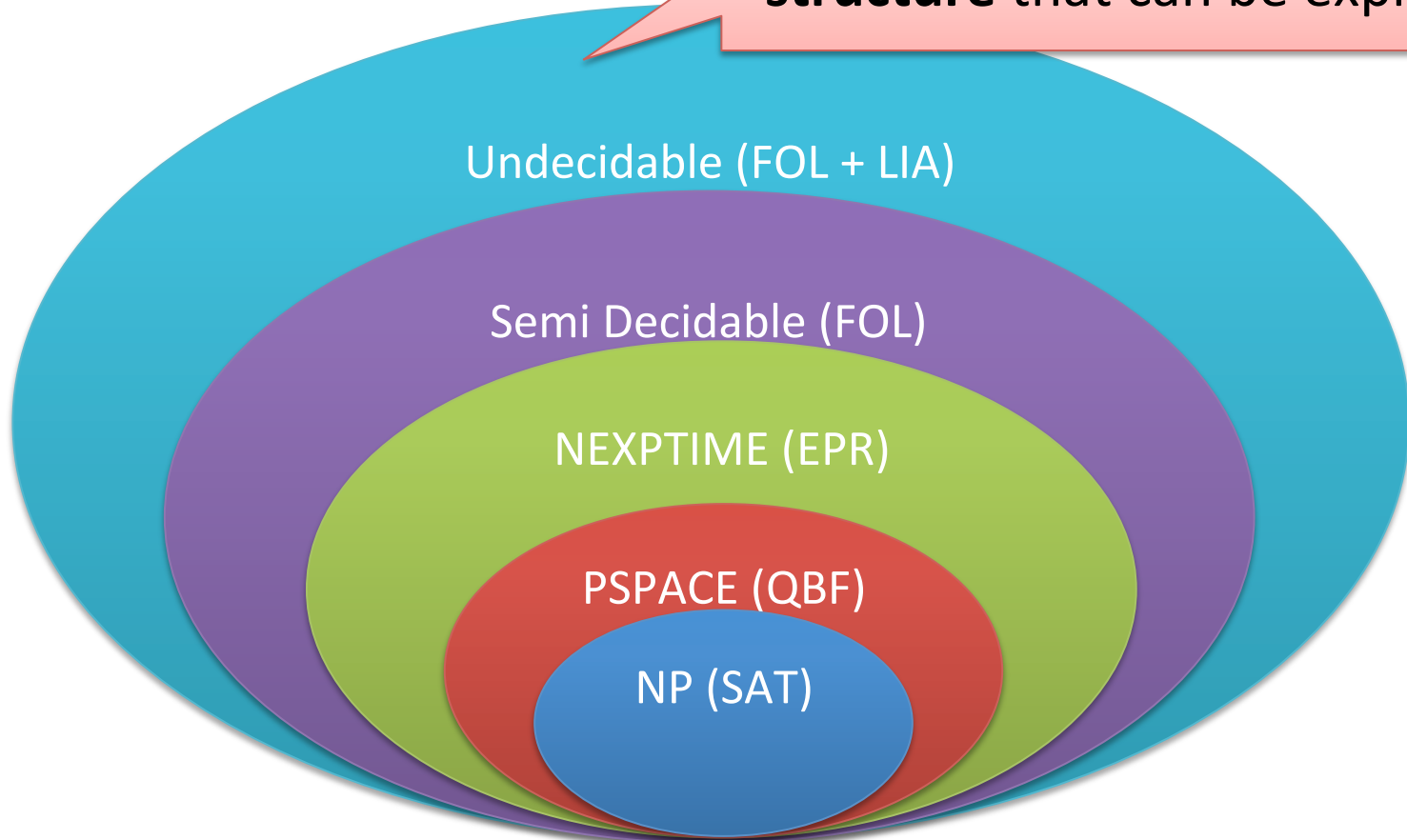
Uninterpreted  
Functions

$\text{select}(\text{store}(a,i,v),i)=v$   
 $i \neq j \Rightarrow \text{select}(\text{store}(a,i,v),j)=\text{select}(a,j)$

# Symbolic Reasoning

High computational Complexity

Practical problems often have **structure** that can be exploited.



# Some Microsoft Tools using Z3

SAGE



HAVOC



The Spec# Programming System

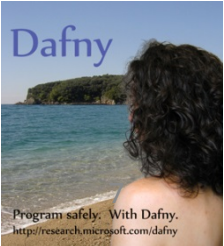
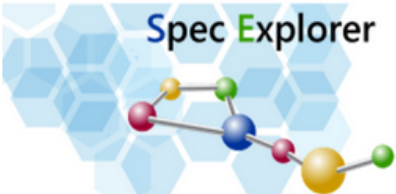
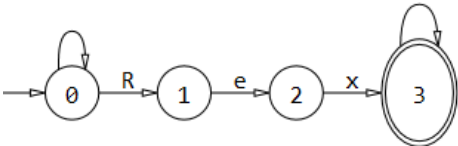


SLAM

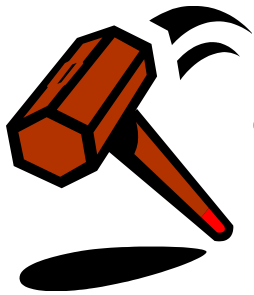
```
if(!node->x); i ++ v1; if(!procs.end()*node){
```

TERMINATOR

SymDiff

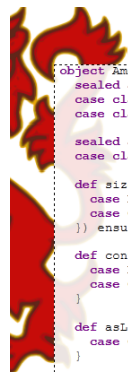


# Other Cool tools using Z3



Sledge  
Hammer

Liquid Types



LeonOnline

```
object AmortizedQueue {
  sealed abstract class List
  case class Cons(head : Int, tail : List) extends List
  case class Nil() extends List

  sealed abstract class AbsQueue
  case class Queue(front : List, rear : List) extends AbsQueue

  def size(list : List) : Int = (list match {
    case Nil() => 0
    case Cons(_, xs) => 1 + size(xs)
  }) ensuring(_ >= 0)

  def content(l: List) : Set[Int] = l match {
    case Nil() => Set.empty[Int]
    case Cons(x, xs) => Set(x) ++ content(xs)
  }

  def asList(queue : AbsQueue) : List = queue match {
    case Queue(front, rear) => concat(front, reverse(rear))
  }

  def concat(l1 : List, l2 : List) : List = (l1 match {
    case Nil() => l2
    case Cons(x, xs) => Cons(x, concat(xs, l2))
  }) ensuring (res => size(res) == size(l1) + size(l2) && content(res) == content(l1) ++ content(l2))

  def isAmortized(queue : AbsQueue) : Boolean = queue match {
    case Queue(front, rear) => size(front) >= size(rear)
  }
}

Verify !
```

concat	postcond.	valid	Z3-f	0.011
reverse	postcond.	valid	Z3-f	0.028
amortizedQueue	postcond.	valid	Z3-f	0.004
enqueue	postcond.	valid	Z3-f	0.003
isAmortized	postcond.	valid	Z3-f	0.003

up at  
copy  
in this



PUG

ESBMC

Scala<sup>Z3</sup>

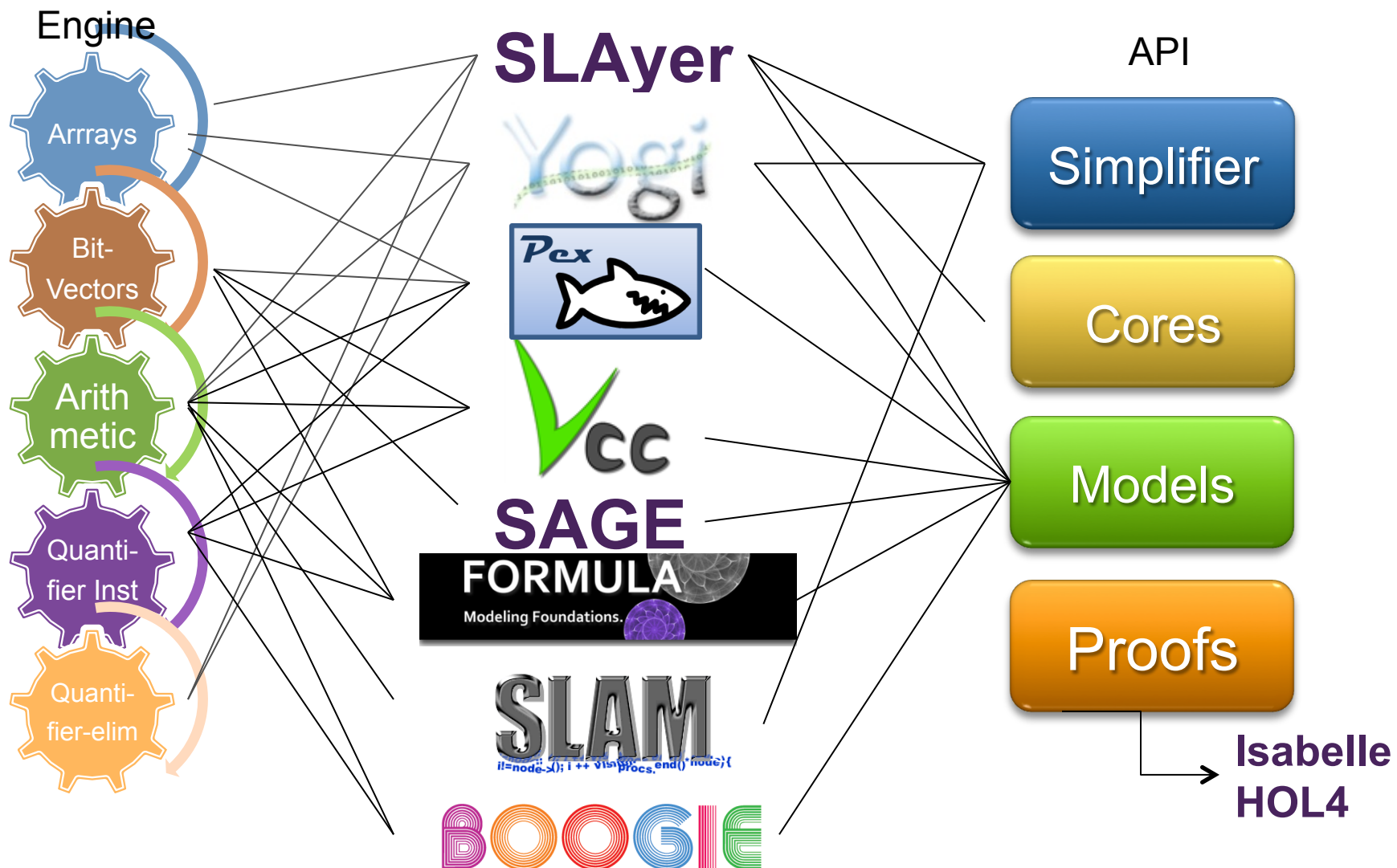
MetiTarski



Jeves

KeYmaera

# Feature Usage



# Z3: Little Engines of Proof



<http://research.microsoft.com/projects/z3>

# What people say about Z3

**Z3** is **powerful**: *Thank you for your advise and your powerful Z3 !*

**Z3** is a **crown jewel**. 

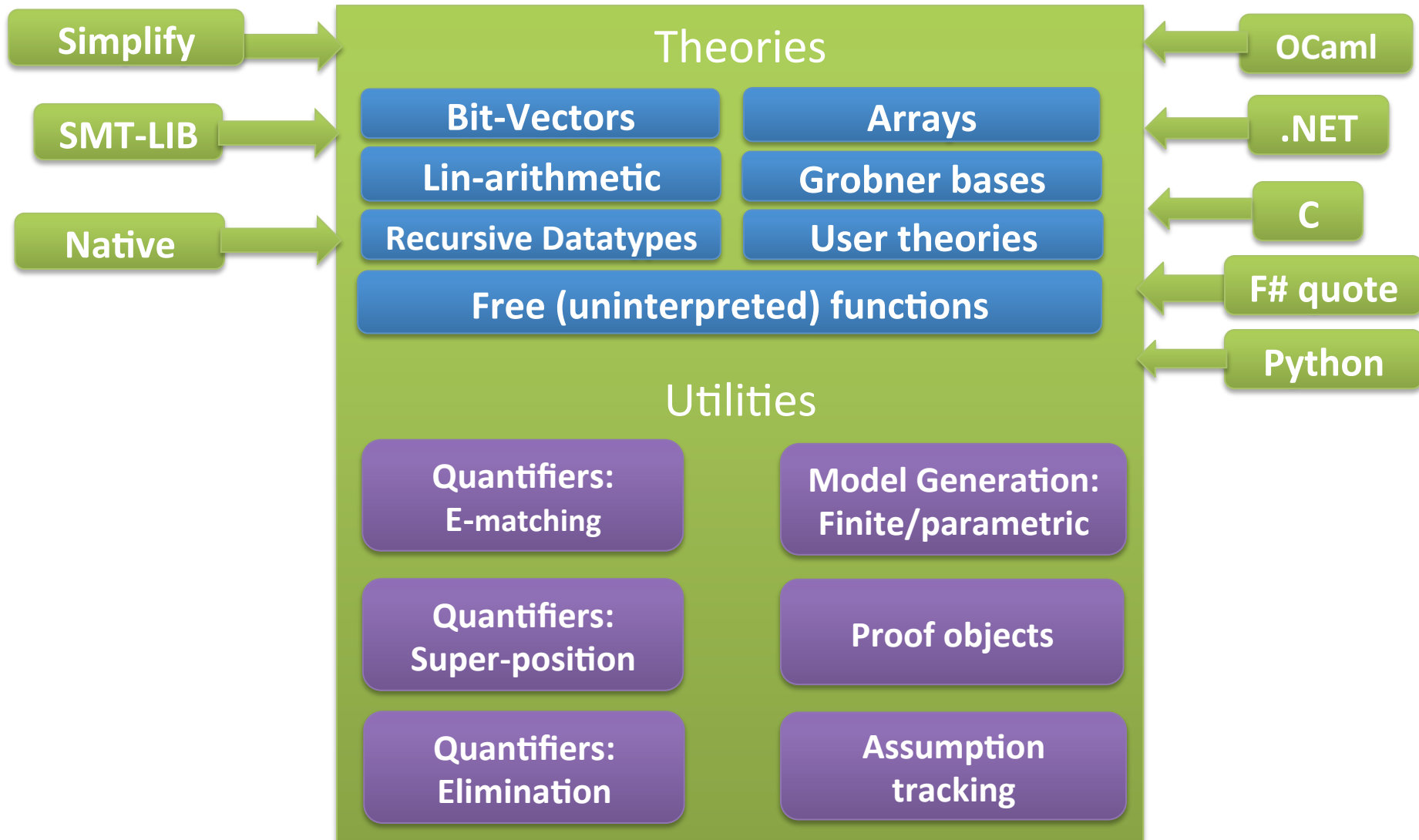
**Z3** is **not just a car**: *Der neue Z3 is höllisch schnell (und ich meine kein Auto).*

**Z3** is **smarter** than the speaker: *I just meant that I am part of the PC to which you sent email so writing me that you sent email to the PC is ... well, redundant ☺. Even Z3 should be able to derive it. [Andrei Voronkov]*

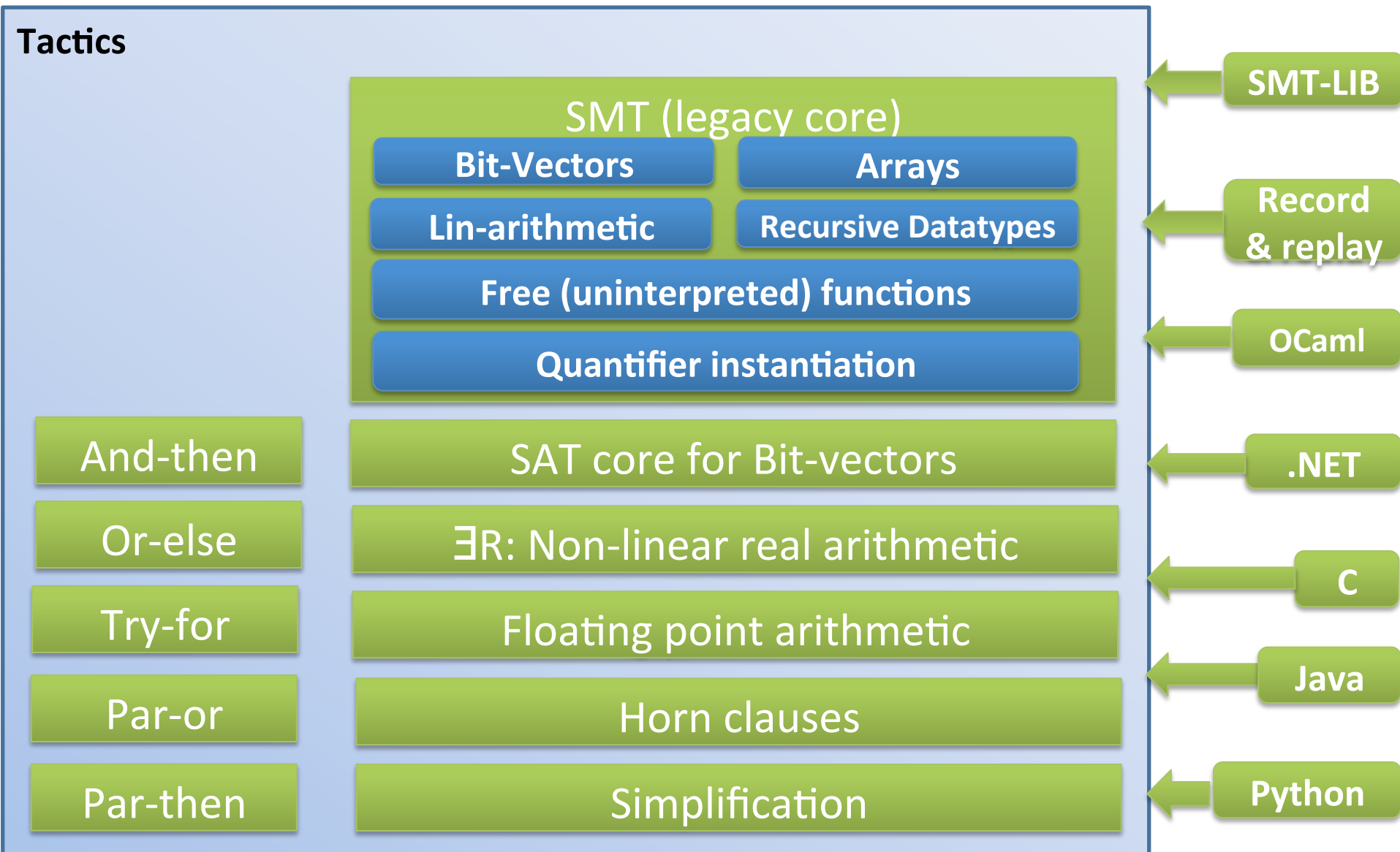
**Z3** – What's new?



# Z3 architecture - original



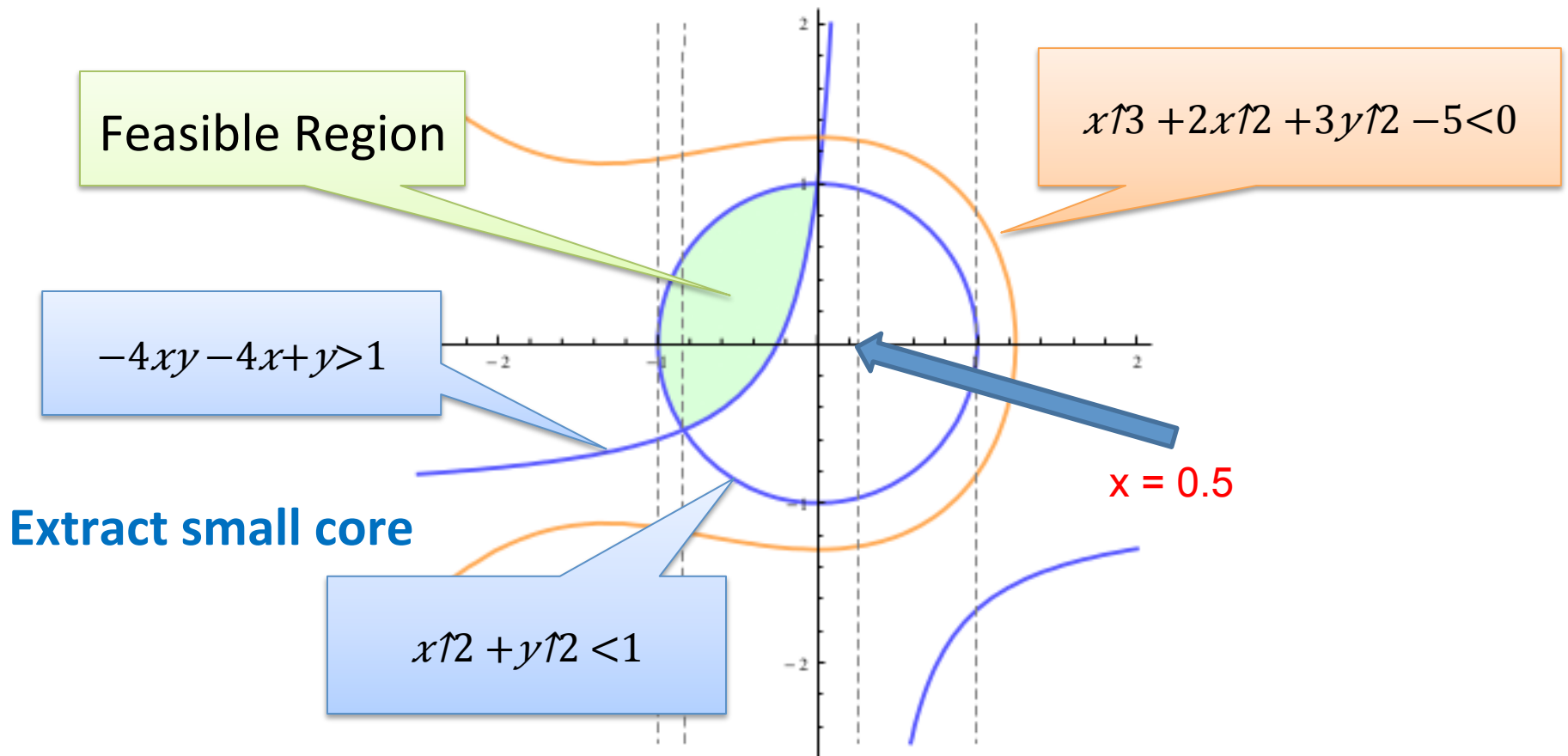
# Z3 architecture - new



# Z3

## Solving $\exists R$ Efficiently

A key idea: Use partial solution to guide the search



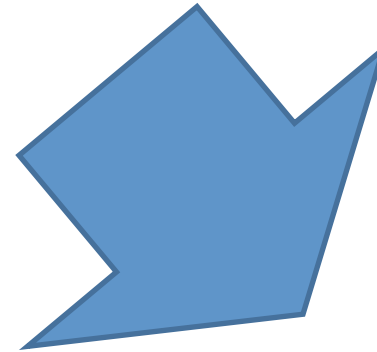
# Z3

## Horn Clause Satisfiability

**mc(x) = x-10** if **x > 100**

**mc(x) = mc(mc(x+11))** if **x ≤ 100**

**assert (x ≤ 101 ⇒ mc(x) = 91)**



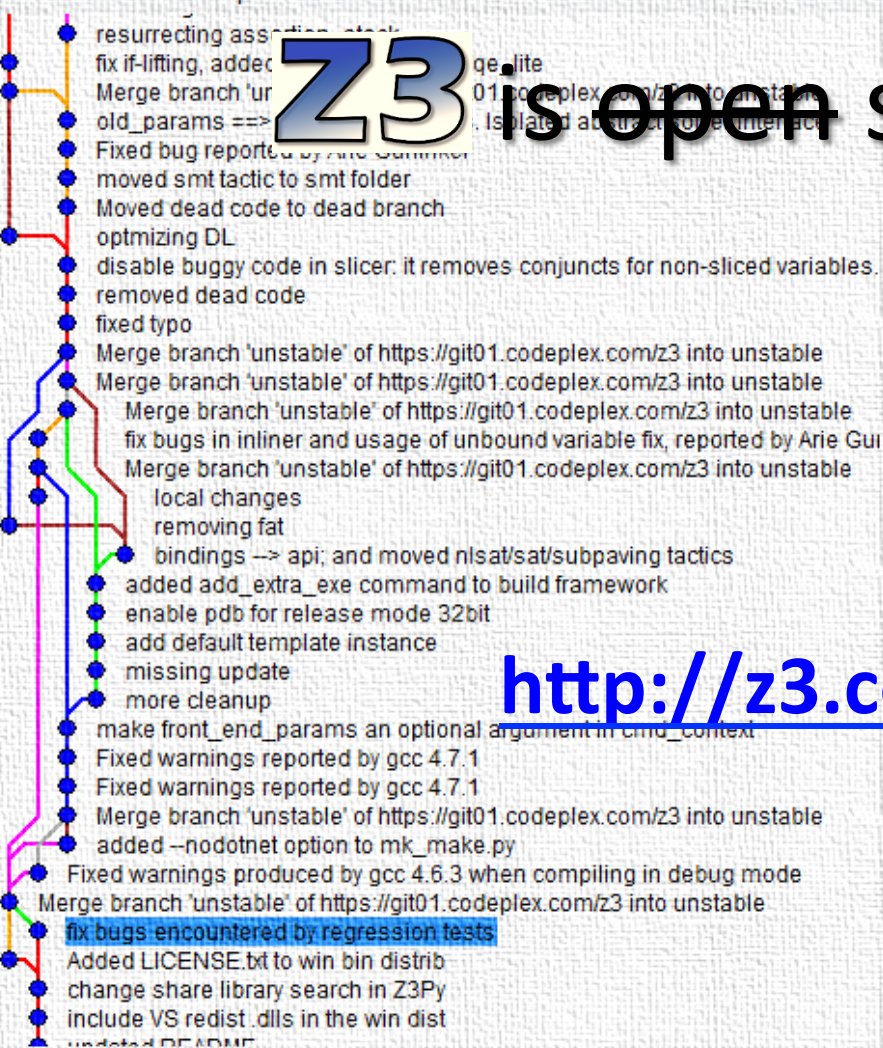
$\forall X. X > 100 \rightarrow mc(X, X-10)$

$\forall X, Y, R. X \leq 100 \wedge mc(X+11, Y) \wedge mc(Y, R) \rightarrow mc(X, R)$

$\forall X, R. mc(X, R) \wedge X \leq 101 \rightarrow R = 91$



File Edit View Help



# Z3 is open shared source

<http://z3.codeplex.com/>

Leonardo de Moura <leonardo@microsoft.com>	2012-11-01 21:15:45
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-11-04 08:40:16
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-11-01 22:06:16
Leonardo de Moura <leonardo@microsoft.com>	2012-11-01 20:28:14
Leonardo de Moura <leonardo@microsoft.com>	2012-11-01 19:28:26
Leonardo de Moura <leonardo@microsoft.com>	2012-11-01 17:48:54
Leonardo de Moura <leonardo@microsoft.com>	2012-11-01 17:40:20
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-11-01 22:06:10
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-11-01 05:29:28
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 23:58:21
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 23:36:18
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 23:22:00
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-10-31 22:35:45
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-10-31 22:25:42
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-10-31 22:23:24
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-10-31 19:37:10
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-10-31 19:37:05
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 23:21:22
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 22:24:39
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 22:14:37
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 22:09:05
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 20:16:43
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 20:02:14
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 19:54:59
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 18:43:46
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 09:16:26
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 09:05:38
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 08:48:23
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 02:47:37
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 08:43:00
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 01:42:36
Nikolaj Bjorner <nbjorner@microsoft.com>	2012-10-31 01:13:27
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 01:42:05
Leonardo de Moura <leonardo@microsoft.com>	2012-10-31 00:09:12
Leonardo de Moura <leonardo@microsoft.com>	2012-10-30 23:17:02
Leonardo de Moura <leonardo@microsoft.com>	2012-10-30 22:22:07

SHA1 ID: f44631ce73e9e1d1f97416ca08dbb3618fc930cb

Row 53 / 375

Find next prev commit containing:

Search

Diff Old version New version Lines of context: 3 Ignore space change Line diff

author: Nikolaj Bjorner <nbjorner@microsoft.com> 2012-10-31 01:13:27  
 committer: Nikolaj Bjorner <nbjorner@microsoft.com> 2012-10-31 01:13:27  
 parent: [ec907a470518bf9e46f2ddd18ea1b1161db74dda](#) (change share library search in Z3Py)

Patch Tree

Comments

src/ast/ast.cpp  
 src/muz\_qe/dl\_util.cpp  
 src/muz\_qe/pdr\_context.cpp



# Z3 source is (evil) subverting 8/



Google

שתף את הדברים הנכונים בדיק עם האנשים הנכונים.

הצטרף ל-Google+

Jan Wildeboer



Munich, Germany גר ב- 📍

הצג את הפרופיל המלא 📄



3 באוק 2012 - גלוי לכולם Jan Wildeboer



Microsoft trying to subvert Open Source. Not the first time: <http://research.microsoft.com/en-us/um/people/leonardo/blog/2012/10/02/open-z3.html>

"Z3 is now open source. The source code is available under the MSR-LA license. This is the same license used to distribute the Z3 binaries. The source code can be used for non-commercial purposes. "

Note the "non-commercial". This is not open source with such a limitation. It fits the political agenda of Microsoft however, where they have told politicians again and again the they think Open Source means non-commercial. But it simply isn't.

Open Source means Open Source. And that means no limitations on use and reuse.

« Open Sourcing Z3

Leonardo de Moura' Homepage



# Validation

## **SecGuru: Automatic Validation of Network Connectivity Restrictions**



Microsoft  
**Research**

Karthick Jayaraman, Charlie Kaufman,  
and Ramanathan Venkatapathy

Nikolaj Bjørner

# Network Policies: Complexity, Challenge and Opportunity

## Several devices, vendors, formats

- Net filters
- Firewalls
- Routers

## Challenge in the field

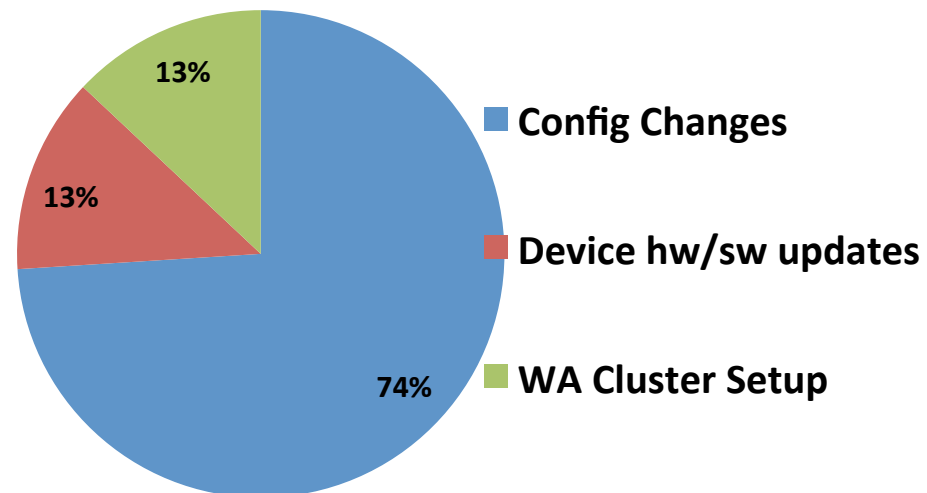
- Do devices enforce policy?
- Ripple effect of policy changes

## Arcane

- Low-level configuration files
- Mostly manual effort
- Kept working by  
*“Masters of Complexity”*

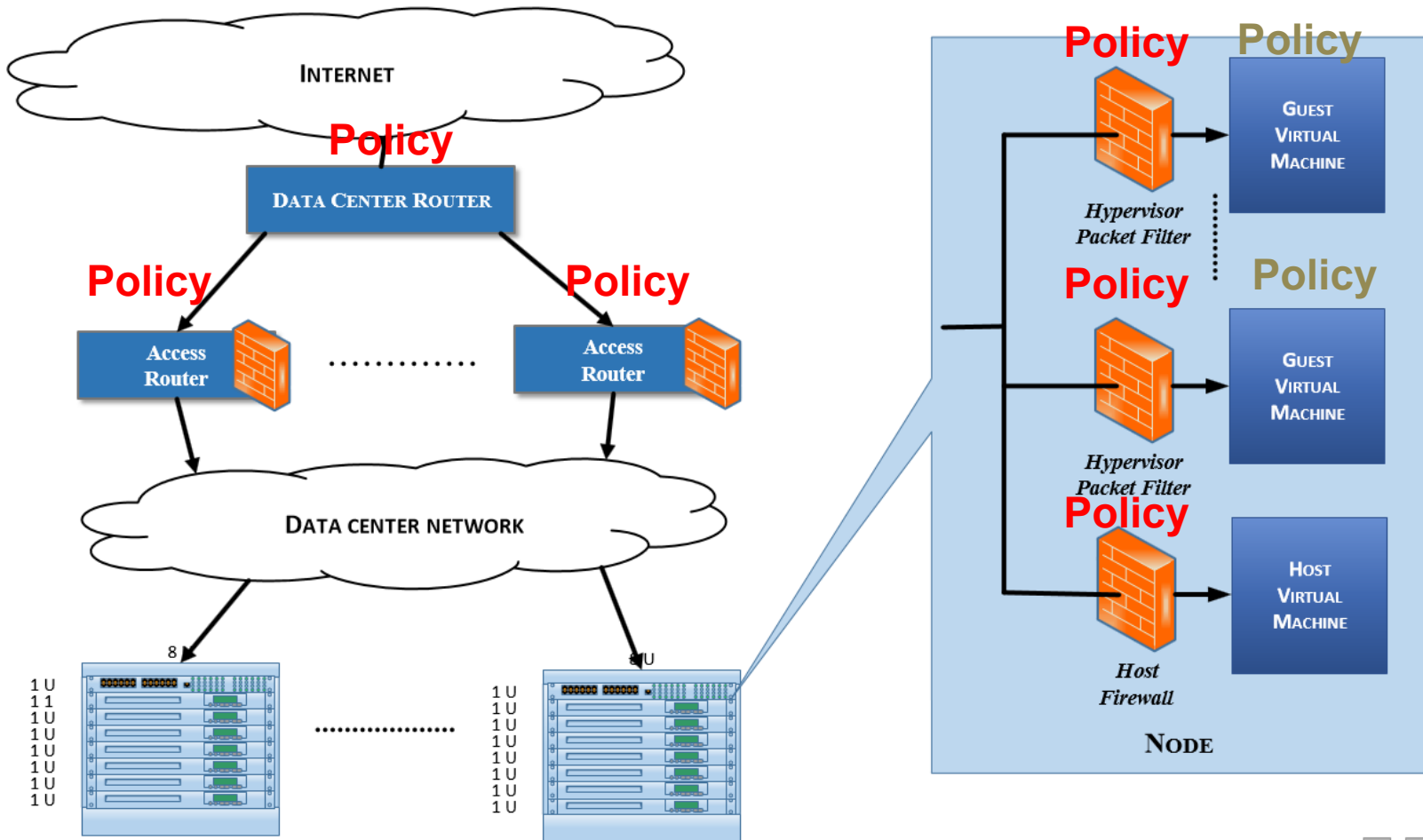
**Human errors > 4 x DOS attacks**

**Human Errors by Activity**





# A Data-center Architecture



# A Data-center Architecture

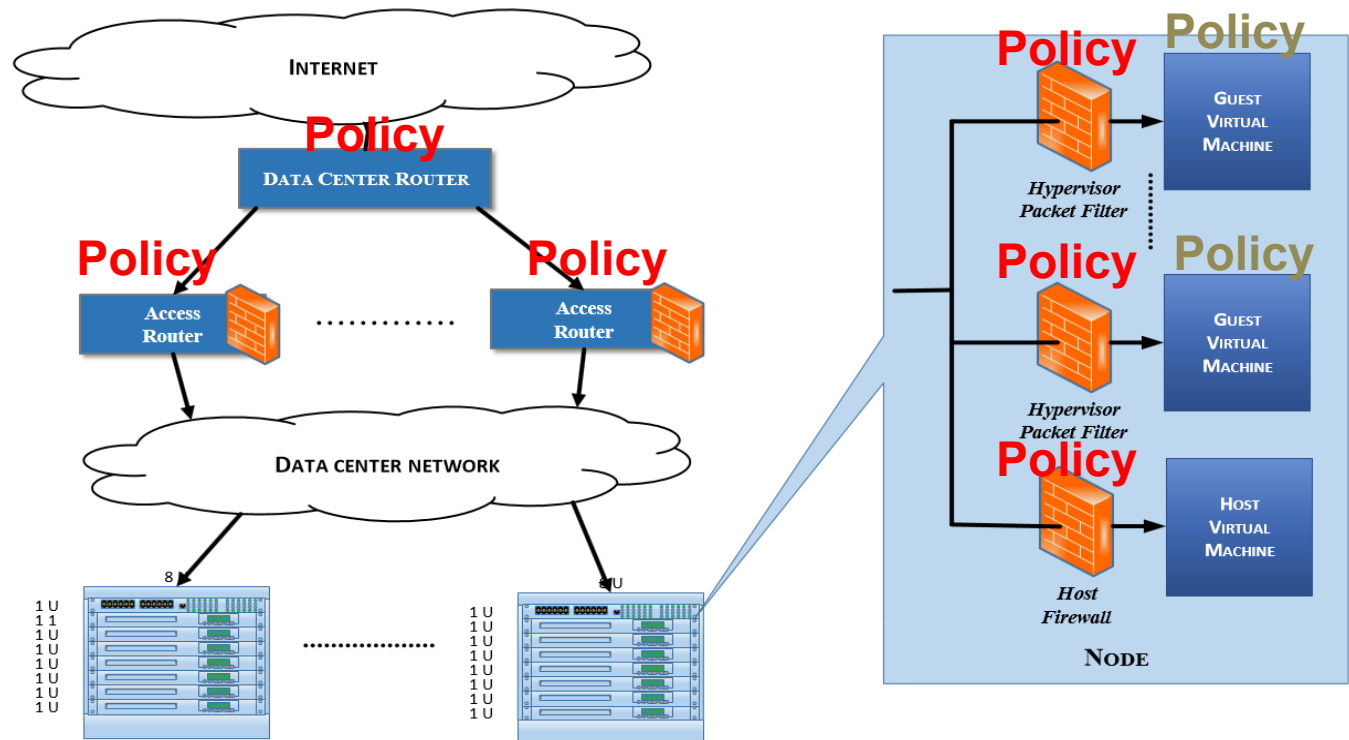
Defense in Depth = Crypto + Policies

- outside IP can be spoofed

Efficient and Flexible Defense by Policy only

- inside IP cannot be spoofed

Policies (Access Control Lists) matter



# Network Policies

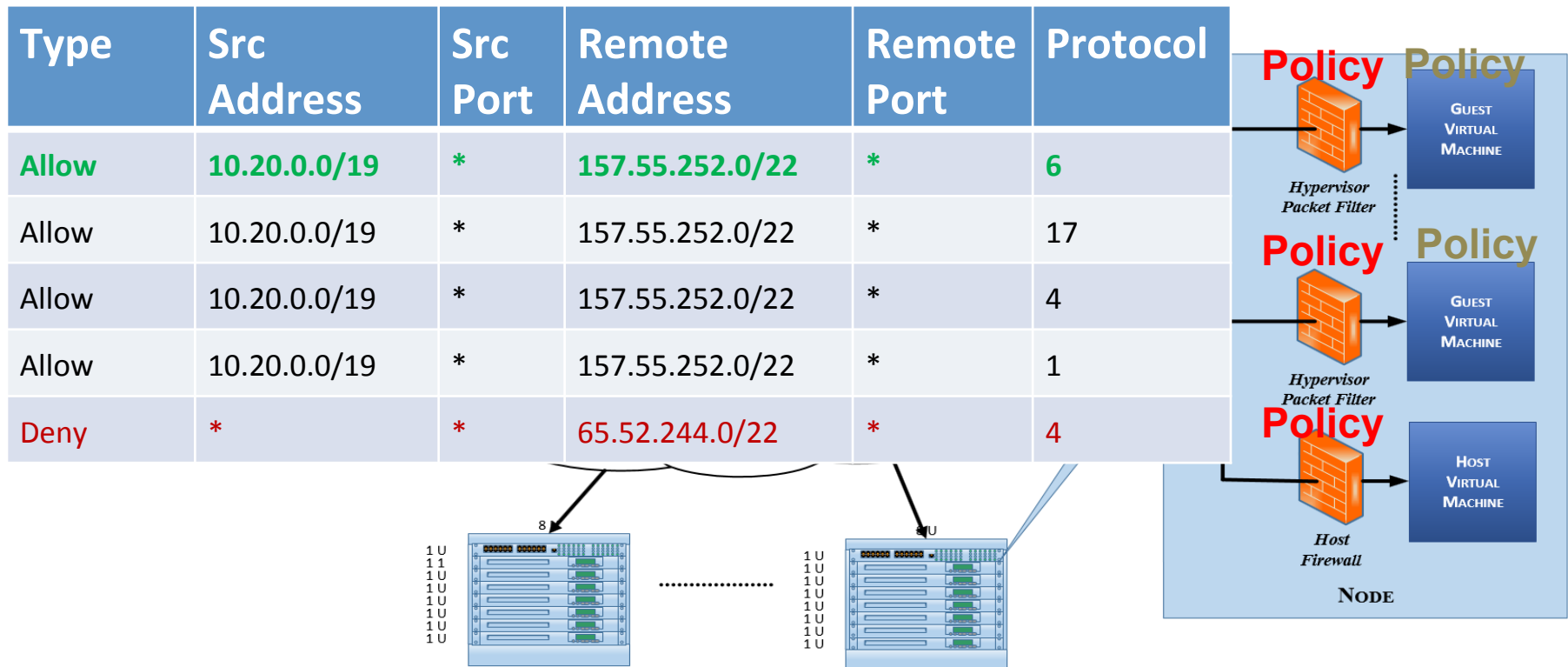
## What they look like in routers

Defense in Depth = Crypto + Policies

- outside IP can be spoofed

Efficient and Flexible Defense by Policy only

- inside IP cannot be spoofed



# A Need for Automating Policy Configuration

## Constantly growing

*New clusters* – “same” policy, but different addresses

## Constantly changing

*Hardware* – capacity, semantics

*New Services* – selectively exposed

*Patches* – to *live site incidents*

**Bare metal** low-level format of routers are also used for policy configurations

# Towards automation: Checking Policy Configuration with **SecGuru**

## Constantly growing

*New clusters – enforce and check that  
new policies are instances  
of a master template (the intent)*

## Constantly changing

*Hardware – capacity, semantics*

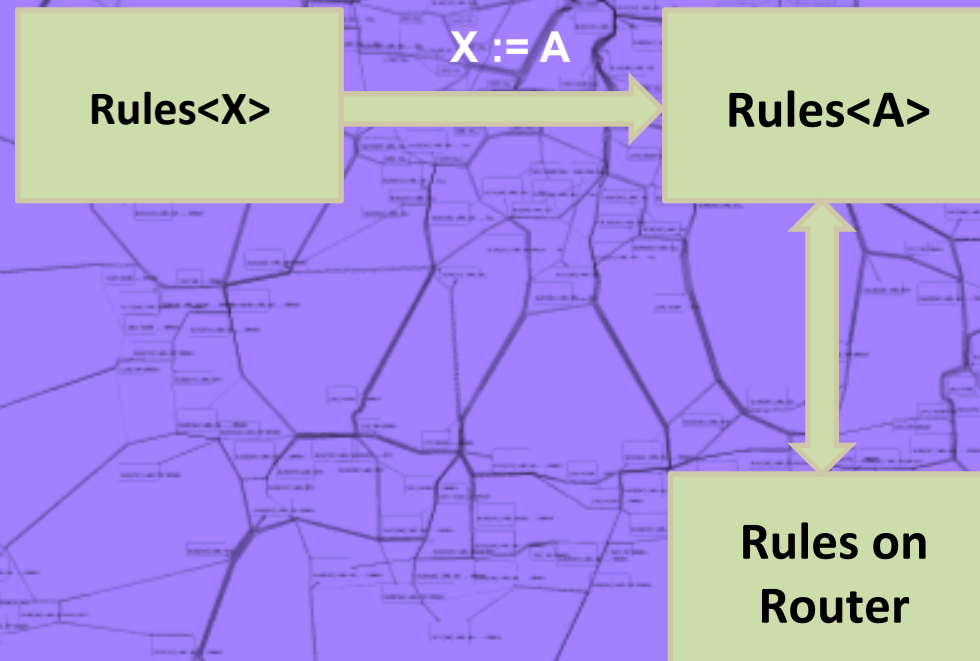
*New Services – check effect of new rules*

*Patches – check for regressions*



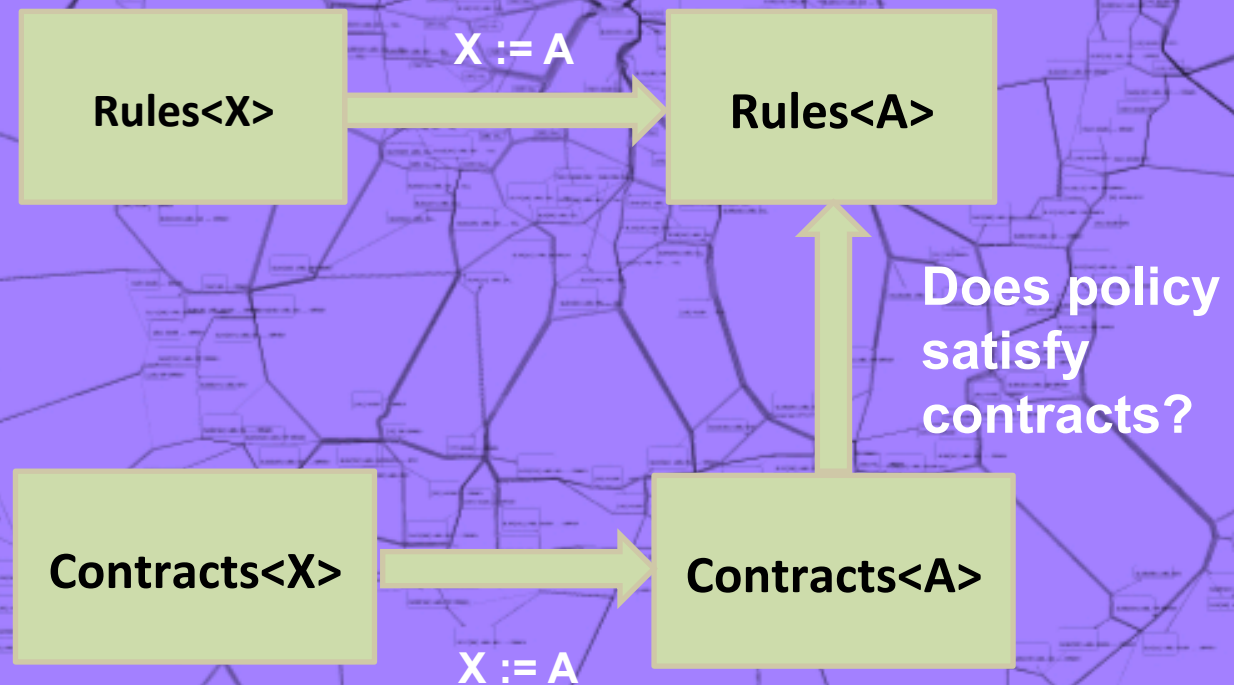
# Towards automation: Checking Policy Configuration with **SecGuru**

*Enforce and check that policies are instances of a master template (the intent)*



# Towards automation: Checking Policy Configuration with **SecGuru**

*Enforce and check that policies satisfy contracts*



# Policies as Bit-Vector Formulas

Type	Src Address	Src Port	Remote Address	Remote Port	Protocol
Allow	10.20.0.0/19	*	157.55.252.0/22	*	6
Allow	10.20.0.0/19	*	157.55.252.0/22	*	17
Allow	10.20.0.0/19	*	157.55.252.0/22	*	4
Allow	10.20.0.0/19	*	157.55.252.0/22	*	1
Deny	*	*	65.52.244.0/22	*	4

IP, Port, and Protocol: **bit vectors**  
 Policy: **Bit-vector logic**

**Allow:**  $(10.20.0.0 \leq s$   
 $r < 10.20.31.255)$   
 $\wedge (157.55.252.0 \leq dstIp$   
 $< 157.55.252.255) \wedge$

**Policy:**  $(V_i \uparrow \dots \uparrow Allo$   
 $w \downarrow i) \wedge (\wedge j \uparrow \dots \uparrow \neg Den$



# Policies as Bit-Vector Formulas

Type	Src Address	Src Port	Remote Address	Remote Port	Protocol
Allow	10.20.0.0/19	*	157.55.252.0/22	*	6
Allow	10.20.0.0/19	*	157.55.252.0/22	*	17
Allow	10.20.0.0/19	*	157.55.252.0/22	*	4
Allow	10.20.0.0/19	*	157.55.252.0/22	*	1
Deny	*	*	65.52.244.0/22	*	4

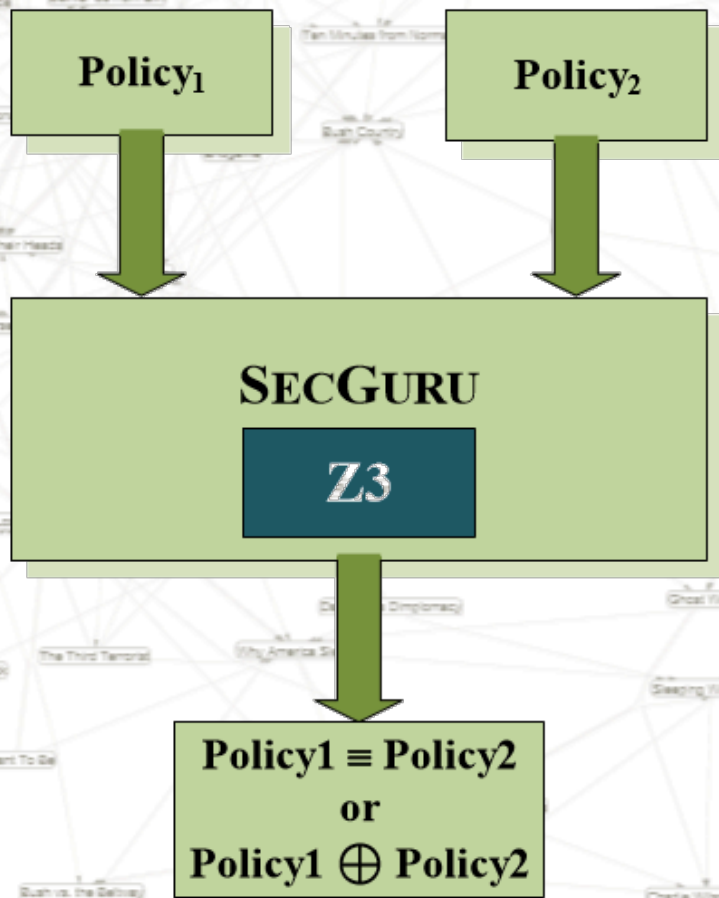
IP, Port, and Protocol: **bit vectors**  
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**Allow:  $(10.20.0.0 \leq s$   
 $rclp\ 10.20.31.255)$   
 $\wedge (157.55.252.0 \leq dstIp$   
 $< 157.55.252.255) \wedge$**

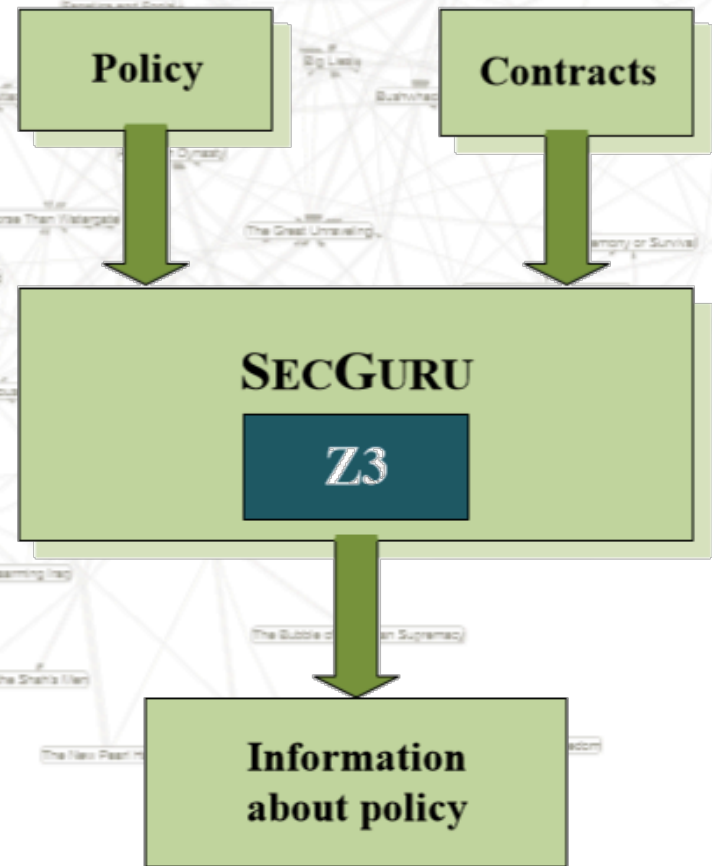
*Policy* $\downarrow 0 = false$   
*Policy* $\downarrow i+1 = if$   
*IsAllow* $\downarrow i$

# Two uses of SecGuru

## Change-Impact Analysis



## Contract Validation



# Two uses of SecGuru

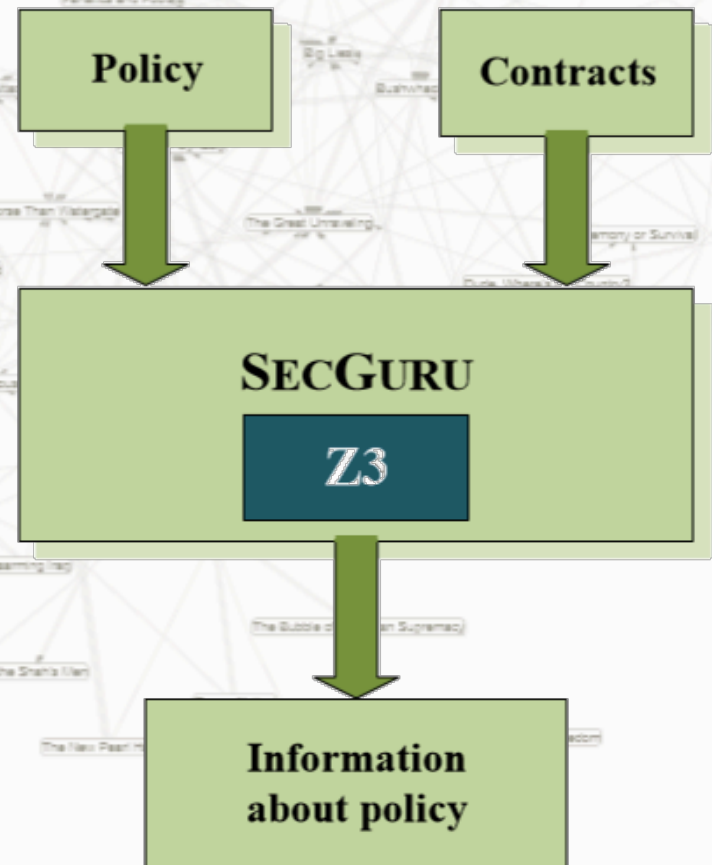
*Does policy permit outgoing traffic to **some** address in 65.52.244.0/22?*

*Check Satisfiability of*

*Does policy permit connections to **all** the remote addresses in the range 65.52.244.0/22?*

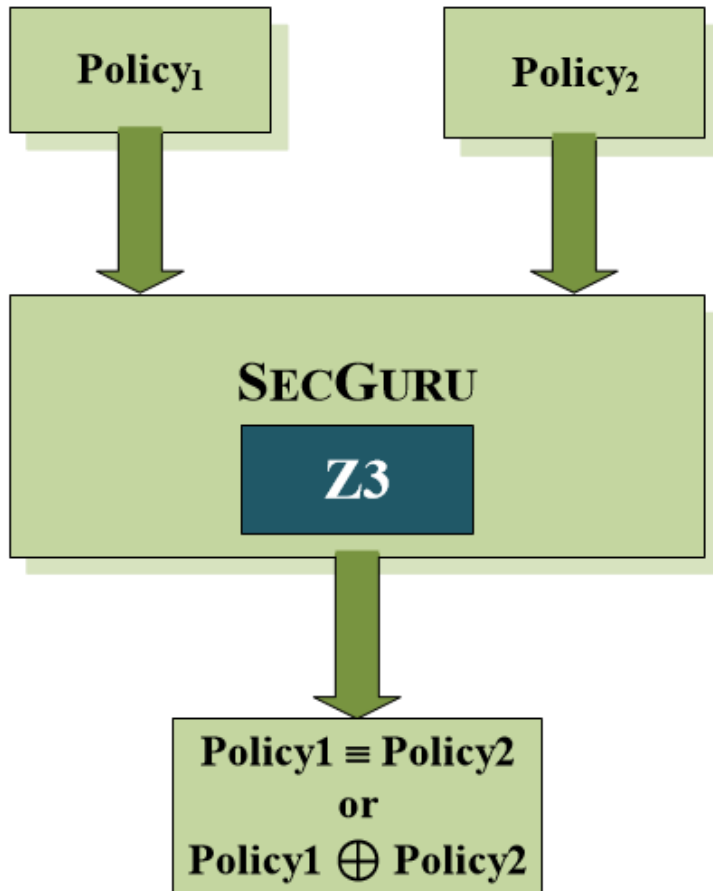
*Check Unsatisfiability of*

## Contract Validation



# Semantic Diff with SecGuru

## Change-Impact Analysis



Semantic diff between policies

Is  $Policy\downarrow 1 \equiv Policy\downarrow 2$  ?

If not, print  $Policy\downarrow 1 \oplus Policy\downarrow 2$

Traffic accepted by  $P\downarrow 1$ , but not  $P\downarrow 2$ . Models for  $P\downarrow 1 \wedge \neg P\downarrow 2$

Traffic accepted by  $P\downarrow 2$ , but not  $P\downarrow 1$ .

Models for  $P\downarrow 2 \wedge \neg P\downarrow 1$



# Semantic Diff with SecGuru

## Change-Impact Analysis

Semantic diff between policies

Policy<sub>1</sub>



Pol

Pol

```
Traffic accepted by policy1, but not policy2
S.no, SrcIP, SrcPort, DstIP, DstPort, Protocol, TCP Flags, ICMP Type,
1, 10.25.252.0-10.25.252.255; , *, , 239.0.0.0-239.255.255.255; , *, , IP, ,
2, 10.62.12.0-10.62.12.255; , *, , 168.63.161.0-168.63.161.15; , 80; , TCP, ,
3, 10.62.12.0-10.62.12.255; , *, , 168.63.161.0-168.63.161.15; , 443; , TCP, ,
4, 10.62.12.0-10.62.12.255; , *, , 168.63.161.0-168.63.161.15; , 8080; , TCP, ,
5, 10.146.192.0-10.146.192.255; , *, , 239.0.0.0-239.255.255.255; , *, , IP, ,
6, 210.126.9.11-210.126.9.12; , *, , 168.63.161.0-168.63.161.15; , 80; , TCP, ,
7, 210.126.9.11-210.126.9.12; , *, , 168.63.161.0-168.63.161.15; , 443; , TC, ,
8, 210.126.9.11-210.126.9.12; , *, , 168.63.161.0-168.63.161.15; , 8080; , T, ,
9, 216.52.28.197; , *, , 168.63.161.0-168.63.161.15; , 80; , TCP, SYN, ,
10, 216.52.28.197; , *, , 168.63.161.0-168.63.161.15; , 443; , TCP, SYN, ,
```

```
Traffic accepted by policy2, but not policy1
S.no, SrcIP, SrcPort, DstIP, DstPort, Protocol, TCP Flags, ICMP Type,
1, 0.0.0.0-10.19.255.255; , *, , 168.63.161.0-168.63.161.15; , 19999; , TCP, ,
2, 1.202.140.226-1.202.140.227; , *, , 168.63.161.0-168.63.161.15; , 0-8549; , TCP, ,
3, 1.202.140.226-1.202.140.227; , *, , 168.63.161.0-168.63.161.15; , 8552-8; , TCP, ,
4, 1.202.140.226-1.202.140.227; , *, , 168.63.161.0-168.63.161.15; , 8571-6; , TCP, ,
5, 10.7.32.0-10.7.35.255; , *, , 168.63.161.0-168.63.161.15; , 0-8549; , TCP, ,
6, 10.7.32.0-10.7.35.255; , *, , 168.63.161.0-168.63.161.15; , 8552-8569; , ,
7, 10.7.32.0-10.7.35.255; , *, , 168.63.161.0-168.63.161.15; , 8571-65535; , ,
8, 10.7.51.0-10.7.51.31; , *, , 168.63.161.0-168.63.161.15; , 0-79; , TCP, S,
9, 10.7.51.0-10.7.51.31; , *, , 168.63.161.0-168.63.161.15; , 81-442; , TCP, ,
10, 10.7.51.0-10.7.51.31; , *, , 168.63.161.0-168.63.161.15; , 444-8079; , T,
```

# All-BVSAT: A compact model enumeration

Really naïve model enumeration:

- To generate the  $(k+1)st$  model, negate all the  $k$  models seen so far

$$- (V_i \uparrow Allow \downarrow i) \wedge (\wedge_j \uparrow \neg Den$$

$$y \downarrow j) \wedge (\wedge_k \uparrow \neg Model \downarrow k) \quad \dots$$

$2^{32+16+32+16}$  models

Smarter model enumeration in SecGuru using All-BVSAT (idea):

- Find initial  $srcIp \downarrow 0, srcPort \downarrow 0 \models \mathbf{Policy} \downarrow 1 \wedge \neg \mathbf{Polic}$   
 $y \downarrow 2$

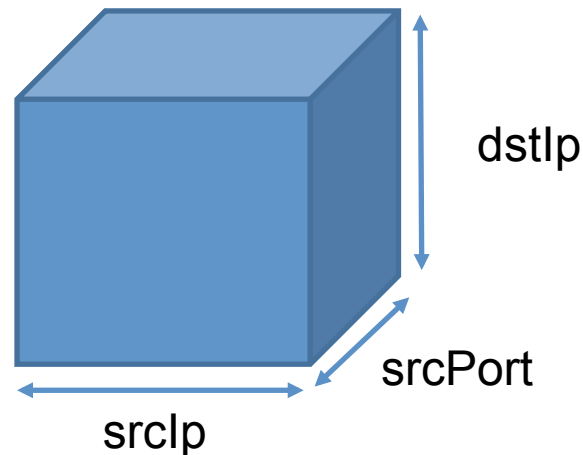
Maximize bounds  $lo | srcIp \leq srcIp \leq hi | srcIp$ .

# All-BVSAT: A compact model enumeration

Maximize bounds:

$$\begin{aligned} & \blacksquare \blacksquare lo \downarrow srcIp \leq srcIp \leq hi \downarrow srcIp \wedge l \\ & o \downarrow srcPort \leq srcPort \leq hi \downarrow srcPort \wedge lo \downarrow dstIp \\ & \leq dstIp \leq hi \downarrow dstIp \ \&F \ \& \mathbf{Policy} \downarrow \mathbf{1} \wedge \neg \mathbf{Polic} \\ & \mathbf{y} \downarrow \mathbf{2} \end{aligned}$$

Result is a *cube*:

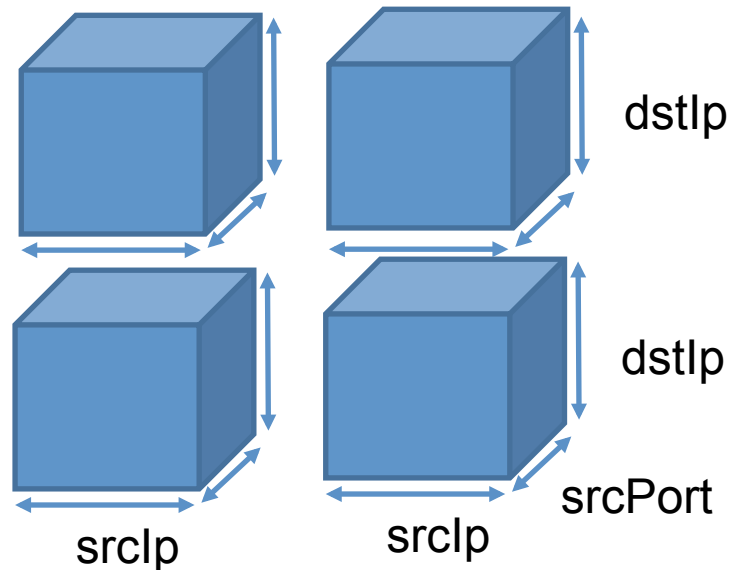


# All-BVSAT: A compact model enumeration

More succinct: Maximize *multiple* bounds

$$\begin{aligned}
 & \blacksquare \blacksquare (lo_1 \leq srcIp \leq hi_1 \vee lo_2 \leq srcIp \leq hi_2) \wedge lo_1 \leq srcPort \leq hi_1 \wedge \\
 & lo_2 \leq srcPort \leq hi_2 \wedge (lo_3 \leq dstIp \leq hi_3 \vee lo_4 \leq dstIp \leq hi_4) \wedge \text{Policy}_1 \wedge \neg \text{Policy}_2
 \end{aligned}$$

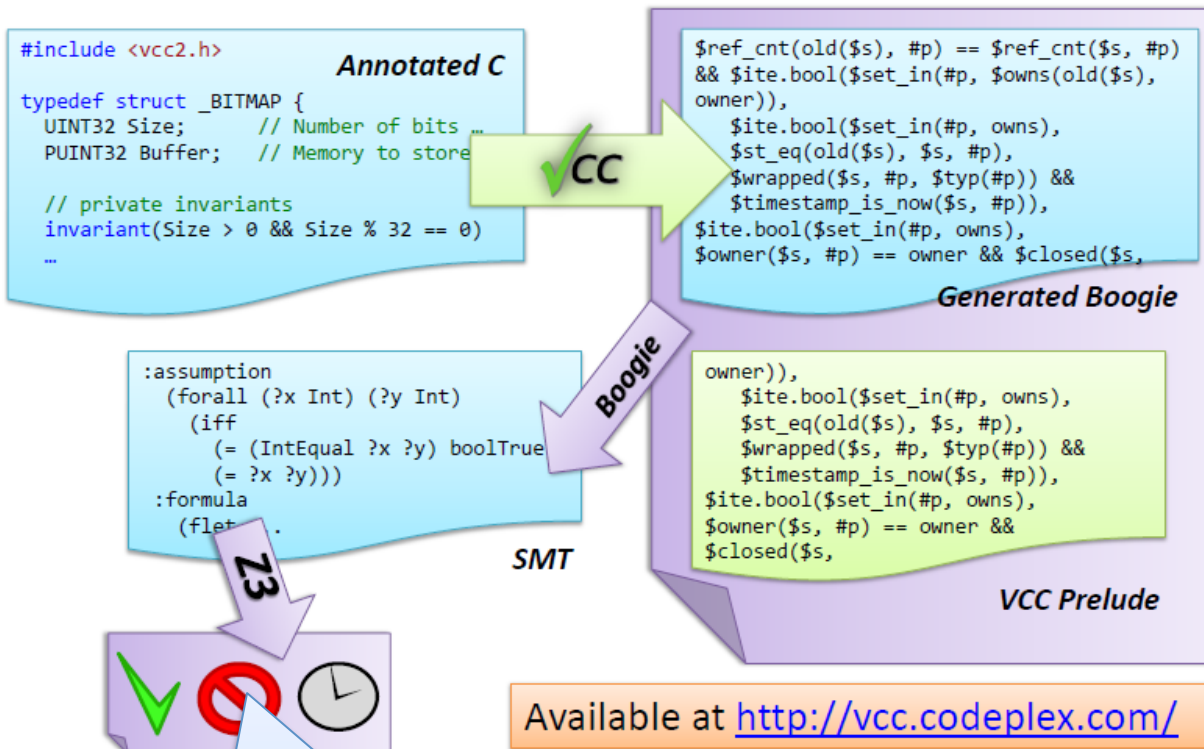
Result is a *multi-cube*:





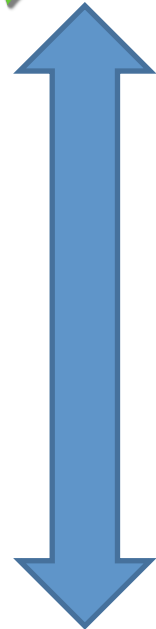
# Satisfiability of Horn Clauses

# Divide and Conquer



SMT solvers have specialized algorithms for  $T$

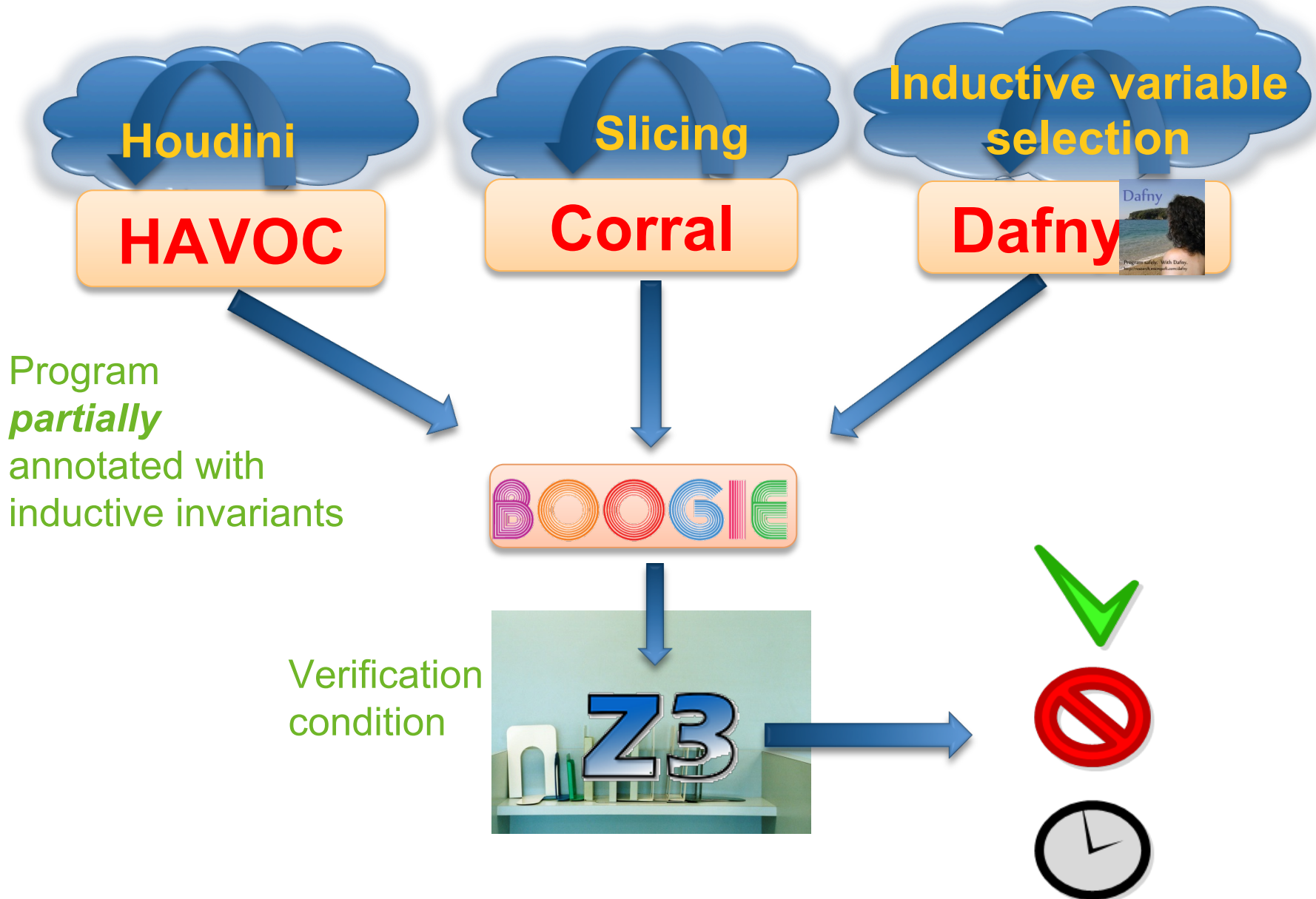
Front-end Programming language semantics



Z3

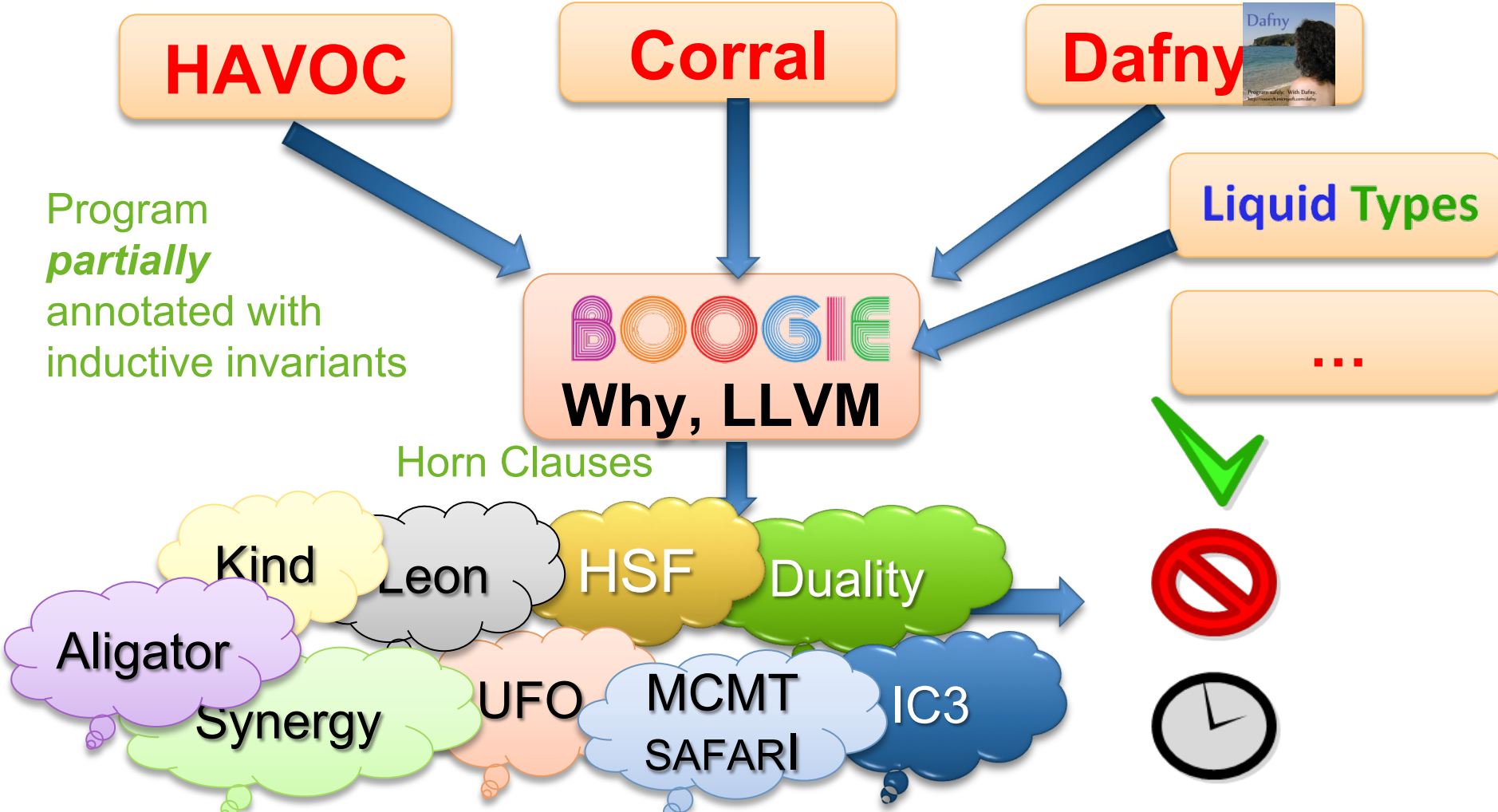
Back-end Logic engine

# Verification Tool Workflow



# Envisioned: Verification Tool Workflow

Verification Condition Generators can already produce Horn Clauses



# Motivation: Recursive Procedures

**mc(x) = x-10**                      **if x > 100**

**mc(x) = mc(mc(x+11))**              **if x ≤ 100**

**assert (mc(x) ≥ 91)**

# Motivation: Recursive Procedures

*Formulate as Horn clauses:*

$$\forall X. X > 100 \rightarrow \text{mc}(X, X-10)$$

$$\forall X, Y, R. X \leq 100 \wedge \text{mc}(X+11, Y) \wedge \text{mc}(Y, R) \rightarrow \text{mc}(X, R)$$

$$\forall X, R. \text{mc}(X, R) \rightarrow R \geq 91$$

*Solve for mc*



# Motivation: Recursive Procedures

*Formulate as Predicate Transformer:*

$$\begin{aligned} \blacksquare F (mc)(X,R) = & \blacksquare X > 100 \wedge R = X - 10 @ \forall \\ & X \leq 100 \wedge \exists Y. mc(X+11, Y) \wedge mc(Y, R) \end{aligned}$$

Check:  $\mu F (mc)(X,R) \rightarrow R \geq 91$

# Motivation: Recursive Procedures

Instead of computing  $\mu F (mc)(X,R)$ ,  
then checking  $\mu F (mc)(X,R) \rightarrow R \geq 91$

Suffices to find post-fixed point  $mc \downarrow \mathbf{post}$  satisfying:

$$\forall \mathbf{X}, \mathbf{R}. F (mc \downarrow \mathbf{post})(\mathbf{X}, \mathbf{R}) \rightarrow mc \downarrow \mathbf{post}(\mathbf{X}, \mathbf{R})$$

# Program Verification as SMT

[Bjørner, McMillan, Rybalchenko, SMT workshop 2012]

Hilbert Sausage Factory: [Grebenshchikov, Lopes, Popeea, Rybalchenko, PLDI 2012]

***Program Verification (Safety)***

***as Solving fixed-points***

***as Satisfiability of Horn clauses***

# Many problems expressible in SMT-LIB2

```
(set-logic HORN)
(declare-fun f (Int Int) Bool)

(assert (forall ((p Int)) (=> (> p 100) (f p (- p 10)))))
(assert (forall ((p Int) (p2 Int) (p3 Int) (p4 Int) (r Int))
  (=> (and (<= p 100) (f (+ p 31) p2)
    (f p2 p3) (f p3 p4) (f p4 r))
    (f p r))))

(assert (forall ((p Int) (r Int))
  (=> (and (f p r) (<= p (- 10)) (not (= r 91))) false)))

(check-sat)
```

Raw: T-----XEmacs: f4-pure.smt2 (Fundamental)-----All-----

```
C:\tvm\src\z3_2\regression\pdr>z3 f4-pure.smt2 /v:1
Entering level 1
Entering level 2
Entering level 3
Entering level 4
Entering level 5
Entering level 6
(define-fun f ((x!1 Int) (x!2 Int)) Bool
  (let ((a!1 (or (<= (+ x!2 (* (- 1) x!1)) (- 10)) (<= x!1 100))))
    (and (or (>= x!1 101) (<= x!2 91))
      a!1
      (<= (+ x!1 (* (- 1) x!2)) 10)
      (>= x!2 91))))
sat
```

# Solvers for recursive Horn Clauses

- Several newer tools:
  - PDR in Z3 [Hoder, B] (more about this later)
  - Duality [McMillan]
  - HSF [Rybalchenko et.al.]
  - Eldarica [Rümmer, Hojjati, Kuncak]
  - SPACER [Komuravelli, Gurfinkel, Chaki, Clarke]
- Many cousins (indirectly solve Horn clauses)
  - FLATA, Corral, Yogi, UFO, Kind, Leon, Safari, MCMT



## sv-benchmarks — Revision 213

/trunk/clauses/ALIA/sdv

[\[Parent Directory\]](#)

sdv0.smt2

sdv1.smt2

sdv10.smt2

sdv100.smt2

sdv1000.smt2

sdv1001.smt2

sdv1002.smt2

sdv1003.smt2

sdv1004.smt2

sdv1005.smt2

sdv1006.smt2

sdv1007.smt2

sdv1008.smt2

sdv1009.smt2

sdv101.smt2

sdv1010.smt2

sdv1011.smt2

sdv1012.smt2

sdv1013.smt2

sdv1014.smt2

sdv1015.smt2



# IC3/PDR: Property Directed Reachability in Z3

The IC3 Algorithm for Symbolic Model Checking by Aaron Bradley

## **Procedures**

*Regular vs. Push Down systems*  
*As a Conflict-driven solver for  
recursive Horn clauses*

## **Beyond Propositional Logic**

*Linear Real Arithmetic*  
*- Timed Automata Decision Procedure*  
*- Interpolants from models*

[SAT 2012. Kryštof Hoder & Nikolaj Bjørner]

# PDR – the algorithm

Objective is to solve for  $R$  such that

,

**Key elements of PDR algorithm:**

**Over-approximate reachable states**

**Propagate back from**

**Resolve conflicts**

**Strengthen/propagate using induction**

# PDR – the algorithm

Objective is to solve for  $R$  such that

,

**Initialize:**

■  $Safe \&\&\& R \downarrow 1 := true \&\& @ \&\wedge \&\&\uparrow \&\&\wedge \& @ \&\& R \downarrow 0 := F$  (fa

**Main invariant:**

■  $Safe \&\&\& R \downarrow i+1 \&\& @ \&\wedge \&\&\uparrow \&\&\wedge \& @$

# Search: Mile-high perspective

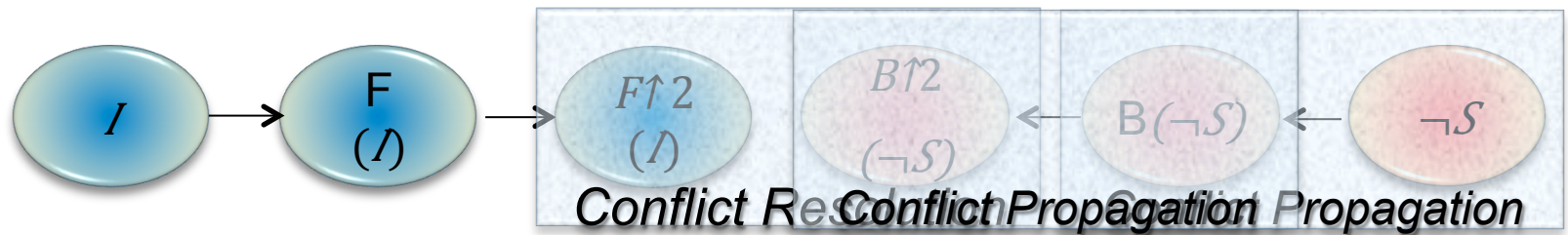
Modern SMT solver



Fixedpoint solver



# Search: Mile-high perspective



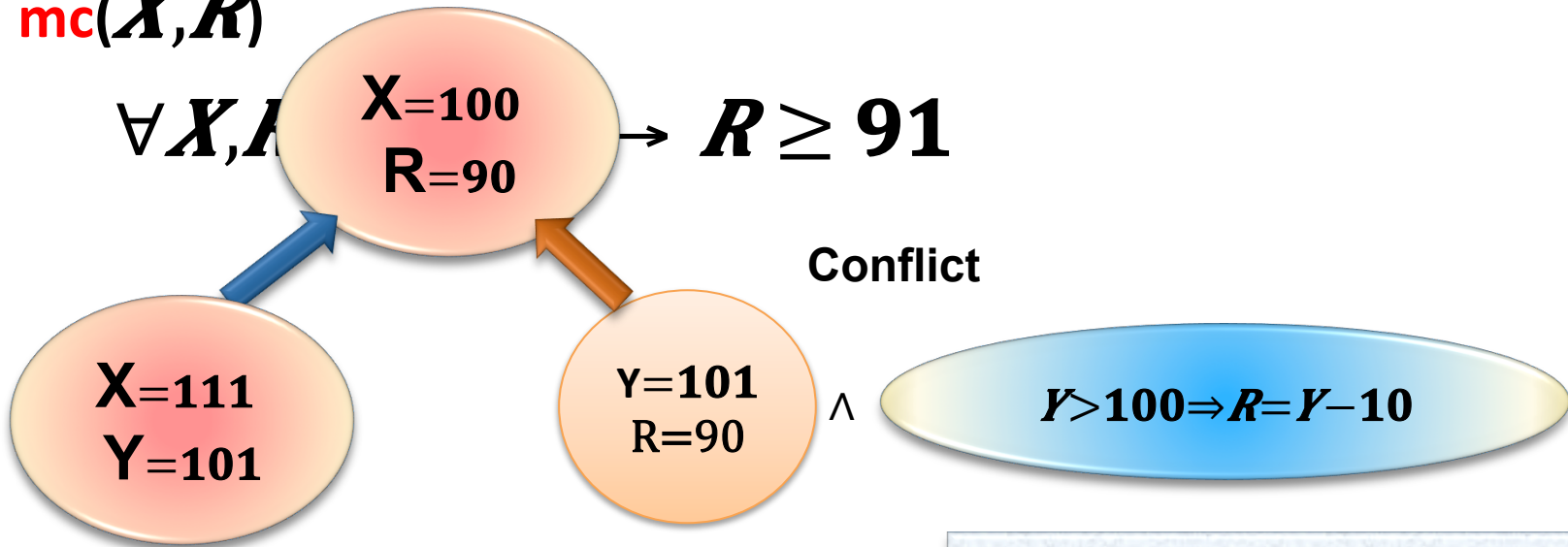
# PDR(LRA): Conflict resolution

$$\forall X. X > 100 \rightarrow \text{mc}(X, X-10)$$

$$\forall X, Y, R. X \leq 100 \wedge \text{mc}(X+11, Y) \wedge \text{mc}(Y, R) \rightarrow$$

$$\text{mc}(X, R)$$

$$\forall X, R. \begin{matrix} X=100 \\ R=90 \end{matrix} \rightarrow R \geq 91$$



Resolution

Get Generalization from Farkas Lemma





# Conflict resolution with arithmetic

initially  $y_1 := y_2 := 0;$

$$P_1 ::= \left[ \begin{array}{l} \text{loop forever do} \\ \ell_0 : y_1 := y_2 + 1; \\ \ell_1 : \text{await } y_2 = 0 \vee y_1 \leq y_2; \\ \ell_2 : \text{critical}; \\ \ell_3 : y_1 := 0; \end{array} \right] \parallel P_2 ::= \left[ \begin{array}{l} \text{loop forever do} \\ \ell_0 : y_2 := y_1 + 1; \\ \ell_1 : \text{await } y_1 = 0 \vee y_2 \leq y_1; \\ \ell_2 : \text{critical}; \\ \ell_3 : y_2 := 0; \end{array} \right]$$

$R(0,0,0,0).$

$T(L,M,Y1,Y2,L',M',Y1',Y2') \wedge R(L,M,Y1,Y2) \rightarrow R(L',M$

$R(2,2,Y1,Y2) \rightarrow \text{false}$

$\text{Step}(L,L',Y1,Y2,Y1') \rightarrow T(L,M,Y1,Y2,L',M',Y1',Y2)$

$\text{Step}(M,M',Y2,Y1,Y2') \rightarrow T(L,M,Y1,Y2,L,M',Y1,Y2')$

$\text{Step}(0,1,Y1,Y2,Y2+1).$

$(Y1 \leq Y2 \vee Y2 = 0) \rightarrow \text{Step}(1,2,Y1,Y2,Y1).$

$\text{Step}(2,3,Y1,Y2,Y1).$

$\text{Step}(3,0,Y1,Y2,0).$

Mutual Exclusion

$\Leftrightarrow$

Clauses have model

$P_2$  takes a step

$\ell \downarrow 0 : y := y + 1; \text{goto } \ell \downarrow 1$

$\ell \downarrow 1 : \text{await } y = 0 \vee y \leq y; \text{goto } \ell \downarrow 2$

$\ell \downarrow 2 : \text{critical}; \text{goto } \ell \downarrow 3$

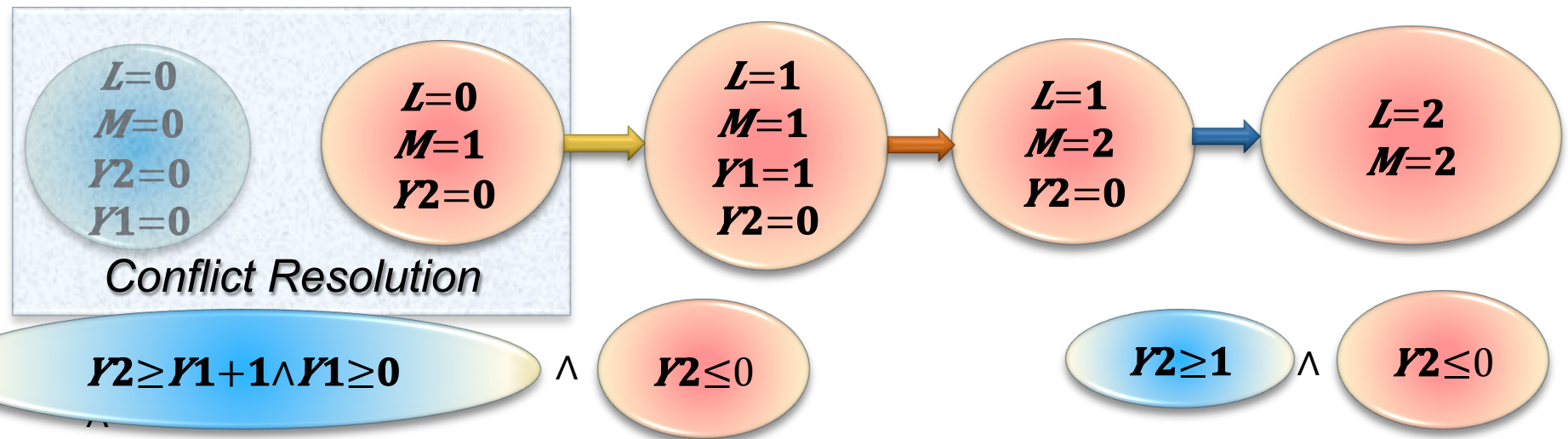
$\ell \downarrow 3 : y := 0; \text{goto } \ell \downarrow 0$

# PDR(T): Conflict Resolution

initially  $y_1 := y_2 := 0;$

$P_1 ::= \left[ \begin{array}{l} \text{loop forever do} \\ \ell_0 : y_1 := y_2 + 1; \\ \ell_1 : \text{await } y_2 = 0 \vee y_1 \leq y_2; \\ \ell_2 : \text{critical}; \\ \ell_3 : y_1 := 0; \end{array} \right]$

$P_2 ::= \left[ \begin{array}{l} \text{loop forever do} \\ \ell_0 : y_2 := y_1 + 1; \\ \ell_1 : \text{await } y_1 = 0 \vee y_2 \leq y_1; \\ \ell_2 : \text{critical}; \\ \ell_3 : y_2 := 0; \end{array} \right]$



Conflict

Resolution

Get Generalization from Farkas Lemma  
 Eg., resolve away **blue** internal variables

# PDR(T): Conflict Resolution

initially  $y_1 := y_2 := 0;$

$P_1 ::= \left[ \begin{array}{l} \text{loop forever do} \\ \downarrow \\ \ell_0 : y_1 := y_2 + 1; \\ \downarrow \\ \ell_1 : \text{await } y_2 = 0 \vee y_1 \leq y_2; \\ \downarrow \\ \ell_2 : \text{critical}; \\ \downarrow \\ \ell_3 : y_1 := 0; \end{array} \right] \parallel P_2 ::= \left[ \begin{array}{l} \text{loop forever do} \\ \downarrow \\ \ell_0 : y_2 := y_1 + 1; \\ \downarrow \\ \ell_1 : \text{await } y_1 = 0 \vee y_2 \leq y_1; \\ \downarrow \\ \ell_2 : \text{critical}; \\ \downarrow \\ \ell_3 : y_2 := 0; \end{array} \right]$



# PDR(T): Generalization from T-lemmas

**Can we satisfy?**

$R(0,0,0,0)$ .

*Initial states*

$T(L,M,Y1,Y2,L',M',Y1',Y2'), R(L,M,Y1,Y2) \boxtimes R(L',M',Y1',Y2')$

*Reachable states*

$R(L,M,Y1,Y2) \boxtimes \neg(L=2 \wedge M=2)$ .

*Unsafe state is unreachable*

$\underbrace{L=0 \wedge M=1 \wedge Y2=0}_{\neg M} \wedge \underbrace{F(R \neq 0)}_{\neg Pre}$  is unsatisfiable

E.g., there is unsat core of:

$\underbrace{\bigwedge j \uparrow \dots c \downarrow j \leq x \downarrow j \leq c \downarrow j}_{\neg M} \wedge \underbrace{F(R \neq i)}_{\neg Pre}$

Unsat proof uses T-lemmas

$(\underbrace{5 > x \downarrow 1 \vee 3 < x \downarrow 3}_{\neg From} \neg M \vee \underbrace{x \downarrow 1 - x \downarrow 2 > 2 \vee 2x \downarrow 2 - x \downarrow 3 > 1}_{\neg From} \neg Pre)$

# PDR(T): Generalization from T-lemmas

**Can we satisfy?**

$R(0,0,0,0)$ .

*Initial states*

$T(L,M,Y1,Y2,L',M',Y1',Y2'), R(L,M,Y1,Y2) \boxtimes R(L',M',Y1',Y2')$

*Reachable states*

$R(L,M,Y1,Y2) \boxtimes \neg(L=2 \wedge M=2)$ .

*Unsafe state is unreachable*

Unsat proof uses T-lemmas

$(\underbrace{5 > x1 \vee 3 < x3}_{\neg From} \vee \underbrace{\neg M \vee x1 - x2 > 2 \vee 2x2 - x3 > 1}_{\neg From} \neg Pre)$

$\blacksquare 2 \cdot (-x1 \leq -5) @ x3 \leq 3 @ 2 \cdot (x1 - x2 \leq 2) @ 2x2 - x3 \leq 1 @$

$\blacksquare -2x1 \leq -10 @ x3 \leq 3 @ 2x1 - 2x2 \leq 4 @ \blacksquare 2x2 - x3 \leq 1 \text{-----}$

$- @ 0 \leq -2$

$\underbrace{2x1 - x3 \leq 5}_{\neg} \blacksquare \text{Block any models satisfying this} \downarrow$

# PDR(LRA): Timed automata

***Observation:***

***PDR + Model refinement using Farkas strengthening is a decision procedure for timed push-down systems***

***Justification:***

Every lemma produced is a sum of differences from the input

~

Acyclic path in difference graph.

⇒ Finite set of Farkas lemmas possible.



# N+1 degrees of separation

**Objective:** Synthesize inductive invariant to prove property.

## Reaching objective with interpolants:

Synthesize interpolants, use for proving invariants.

Admirable

Synthesize interpolants, evaluate on random formulas.

Admire them

Write papers about theory of interpolants.

Admire the theorems

Review papers about generating interpolants.

Write admirable review

Participate in PC discussion about paper.

Admire Kevin Bacon

Write paper about proof transformations,  
maybe good for interpolation.

Who knows, it may be used?

## Reaching objective with PDR:

... Nevertheless, interpolants arise.

# Side-effect: Horn Craig Interpolants

Suppose  $A \Rightarrow B$

A Craig Interpolant is formula  $I$ :

$$Lang(I) \subseteq Lang(A) \cap Lang(B)$$

$$A \Rightarrow I, I \Rightarrow B$$

Horn version. Establish satisfiability of:

$$\forall x, y. A[x, y] \Rightarrow I(x),$$

$$\forall x, z. I(x) \Rightarrow B[x, z]$$

and find solution for  $I$ .

# Side-effect: Horn Craig Interpolants

Suppose  $A \Rightarrow B$

A Craig Interpolant is formula  $I$ :

$$Lang(I) \subseteq Lang(A) \cap Lang(B)$$

$$A \Rightarrow I, I \Rightarrow B$$

$$\forall X. \quad X > 100 \rightarrow \mathbf{mc}_0(X, X-10)$$

$$\forall X, Y, R. \quad X \leq 100 \wedge \mathbf{mc}_0(X+11, Y) \wedge \mathbf{mc}_0(Y, R) \rightarrow \mathbf{mc}_1(X, R)$$

$$\forall X. \quad X > 100 \rightarrow \mathbf{mc}_1(X, X-10)$$

$$\forall X, Y, R. \quad X \leq 100 \wedge \mathbf{mc}_1(X+11, Y) \wedge \mathbf{mc}_1(Y, R) \rightarrow \mathbf{mc}_2(X, R)$$

$$\forall X. \quad X > 100 \rightarrow \mathbf{mc}_2(X, X-10)$$

$$\forall X, R. \quad \mathbf{mc}_2(X, R) \rightarrow R \geq 91$$

Solve for  $\mathbf{mc}_0, \mathbf{mc}_1, \mathbf{mc}_2$

# PDR(T): Interpolants as a side-effect

**Intermediary solutions:**  $\forall X. F(R\downarrow 0)(X) \rightarrow R\downarrow 1(X),$   
 $\forall X. F(R\downarrow 1)(X) \rightarrow R\downarrow 2(X),$   
 $\forall X. F(R\downarrow 2)(X) \rightarrow \mathit{Safe}(X),$

**Observation:** *Farkas strengthening computes a “Horn Interpolant” for LRA*

*i.e., solves for non-recursive Horn clauses*

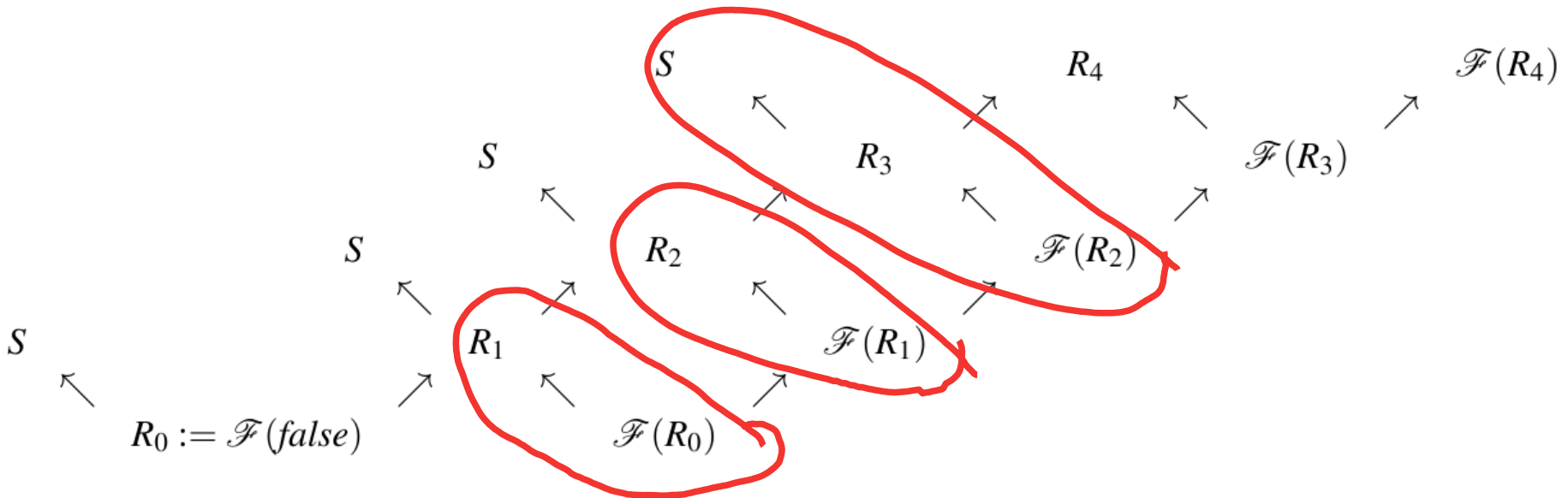
# PDR(T): Interpolants as a side-effect

**Intermediary solutions:**

$$\forall X. \mathcal{F}(R_0)(X) \rightarrow R_1(X),$$
$$\forall X. \mathcal{F}(R_1)(X) \rightarrow R_2(X),$$
$$\forall X. \mathcal{F}(R_2)(X) \rightarrow \text{Safe}(X),$$

**Observation:** *Farkas strengthening computes a “Horn Interpolant” for LRA*

*i.e., solves for non-recursive Horn clauses*



# Universally Quantified Horn Clauses

```
void init(int n, int* A, inc c) {
  for (int i = 0; i < n; ++i) {
    A[i] = c;
  }
  assert( $\forall j. 0 \leq j < n \Rightarrow A[j] = c$ );
}
```

Find  $R$  to satisfy:

$$\forall c, n, A. \quad R(0, c, n, A).$$

$$\forall i, c, n, A. \quad R(i, c, n, A) \Rightarrow R(i+1, c, n, A[i \mapsto c])$$

$$\forall i, c, n, A, j. \quad R(i, c, n, A) \wedge 0 \leq j < n = i \Rightarrow A[j] = c$$

Problem: Inductive invariant is **quantified**

$$R(i, c, n, A) \equiv \forall j. 0 \leq j < i \Rightarrow A[j] = c$$

Horn clause solvers compute **quantifier-free** symbolic solutions.

# Universally Quantified Horn Clauses

```

void init(int n, int* A, inc c) {
  for (int i = 0; i < n; ++i) {
    A[i] = c;
  }
  assert( $\forall j. 0 \leq j < n \Rightarrow A[j] = c$ );
}

```

Find instead  $Q$  to satisfy:

$$\forall c, n, A. \quad \forall k. Q(0, c, n, A[k], k).$$

$$\forall i, c, n, A, k. \quad (\forall k. Q(i, c, n, A[k], k)) \Rightarrow Q(i+1, c, n, A[i \mapsto c][k], k)$$

$$\forall i, c, n, A, j. \quad (\forall k. Q(i, c, n, A[k], k)) \wedge 0 \leq j < n = i \Rightarrow A[j] = c$$

**Problem:** Horn clause solvers don't handle  $\forall k. Q(..)$  formulas



# Universally Quantified Horn Clauses

```

void init(int n, int* A, inc c) {
  for (int i = 0; i < n; ++i) {
    A[i] = c;
  }
  assert( $\forall j. 0 \leq j < n \Rightarrow A[j] = c$ );
}

```

SAS 2013 paper approach: Pre-instantiate  $\forall k. Q$  using pattern matching

$$\forall c, n, A, k. \quad Q(0, c, n, A[k], k).$$

$$\forall i, c, n, A, k. \quad Q(i, c, n, A[k], k) \Rightarrow Q(i+1, c, n, A[i \mapsto c][k], k)$$

$$\forall i, c, n, A, j. \quad Q(i, c, n, A[j], j) \wedge 0 \leq j < n = i \Rightarrow A[j] = c$$

**Solution:**  $Q(i, c, n, a, j) \equiv 0 \leq j < i \Rightarrow a = c$

# Other Horn-clause solving methods

- Datalog engine over abstract tables
  - Hash-tables for JavaScript points-to analysis
  - Header-space algebra tables (ternary simulation) for checking routers [Lopes, Varghese & B]
- Tabulation as Infinite Descente  
(a.k.a cyclic induction proofs)  
Popular in CLP community.  
*New super-acceleration* [Voronkov & B]
- Recursive functions in *Leon*

# Summary

**Z3**

An efficient SMT solver

<http://research.microsoft.com/projects/z3>

**Z3**

Testing, Verification, Validation, Modeling

<http://rise4fun.com>

**Z3**

Fixedpoints:

Satisfiability of *Horn Clauses* Modulo Theories

<http://rise4fun.com/z3py/tutorial/fixedpoints.htm>